



# CINT2000 Result

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## IBM Corporation

IBM BladeCenter HS21 XM (1.60 GHz Xeon E5310, 8MB L2 Cache)

SPECint\_rate2000 = --

SPECint\_rate\_base2000 = 130

SPEC license #: 11 | Tested by: IBM Corporation | Test date: Jan-2007 | Hardware Avail: Feb-2007 | Software Avail: Mar-2006

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	8	147	88.6			
175.vpr	8	137	94.5			
176.gcc	8	70.4	145			
181.mcf	8	161	104			
186.crafty	8	67.4	138			
197.parser	8	159	105			
252.eon	8	62.7	192			
253.perlbmk	8	95.8	174			
254.gap	8	97.2	105			
255.vortex	8	78.3	225			
256.bzip2	8	143	97.0			
300.twolf	8	164	170			

### Hardware

CPU: Intel Xeon processor E5310 (1.60 GHz, 1066 MHz bus)  
CPU MHz: 1600  
FPU: Integrated  
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
CPU(s) orderable: 1, 2  
Parallel: No  
Primary Cache: 32KB(I) + 32KB(D) on chip (per core)  
Secondary Cache: 8MB(I+D) on chip, per chip (4MB shared per 2 cores)  
L3 Cache: N/A  
Other Cache: N/A  
Memory: 8 x 2048 MB ECC PC2-5300F  
Disk Subsystem: 73GB SAS 10K RPM  
Other Hardware:

### Software

Operating System: Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)  
Compiler: Intel C++ Compiler 9.1 for 32-bit applications  
Build 20060323Z  
Microsoft Visual Studio 2005(for libraries)  
SmartHeap Library Version 8.0 from <http://www.microquill.com/>  
File System: NTFS  
System State: Default

## Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
Base tuning for C programs: -fast +FDO shlw32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO shlw32M.lib
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafty: -DNT_i386
252.eon: -DHAS_ERRLIST
253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
```