SPEC® CINT2006 Result
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Intel Corporation

Intel DG965SS motherboard
(1.8 GHz, Intel Core 2 Duo processor E4300)

$SPECint^{®}2006 =$ **Not Run**
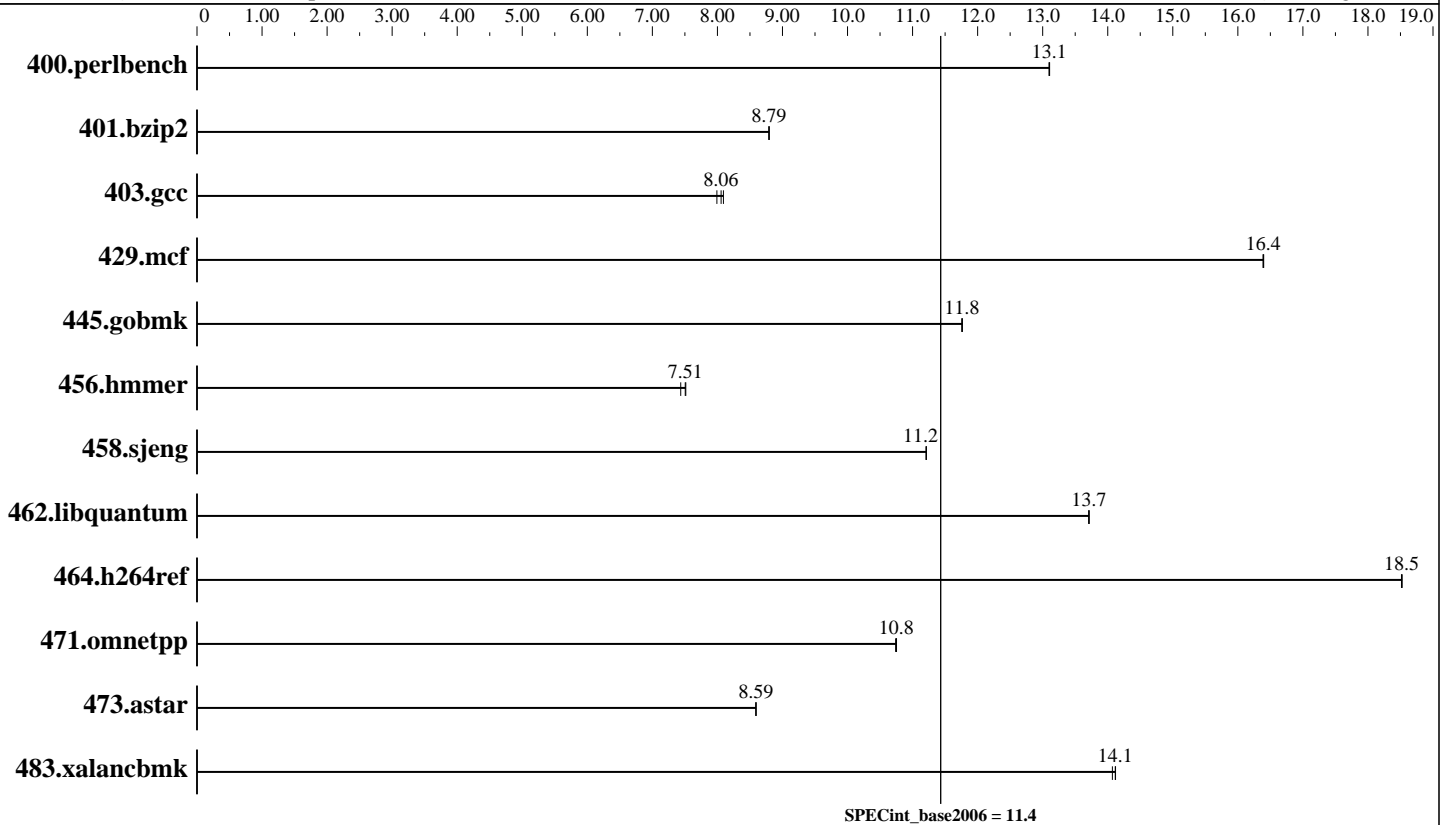
$SPECint\_base2006 =$ **11.4**

CPU2006 license: 13	**Test date:** Jan-2007
Test sponsor: Intel Corporation	**Hardware Availability:** Jan-2007
Tested by: Intel Corporation	**Software Availability:** Aug-2006

Results by benchmark (SPECint_base2006 = 11.4):

- 400.perlbench: 13.1
- 401.bzip2: 8.79
- 403.gcc: 8.06
- 429.mcf: 16.4
- 445.gobmk: 11.8
- 456.hmmer: 7.51
- 458.sjeng: 11.2
- 462.libquantum: 13.7
- 464.h264ref: 18.5
- 471.omnetpp: 10.8
- 473.astar: 8.59
- 483.xalancbmk: 14.1

SPECint_base2006 = 11.4

Hardware

CPU Name:	Intel Core 2 Duo E4300
CPU Characteristics:	1.8 GHz, 800 MHz bus
CPU MHz:	1800
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	2 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	2 GB (2 1GB Micron MT16HTF12864AY-80ED4 DDR2 800, CL5)
Disk Subsystem:	Maxtor DiamondMax 10 6B300S0 300GB NCQ Serial ATA (7200 RPM, 16MB cache)
Other Hardware:	SoundBlaster Live! PCI card

Software

Operating System:	Windows XP Professional w/ SP2
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	SmartHeap Library Version 8.0 from http://www.microquill.com/

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	**<u>746</u>**	**<u>13.1</u>**	746	13.1	745	13.1						
401.bzip2	**<u>1097</u>**	**<u>8.79</u>**	1098	8.79	1097	8.80						
403.gcc	995	8.09	**<u>999</u>**	**<u>8.06</u>**	1007	7.99						
429.mcf	556	16.4	556	16.4	**<u>556</u>**	**<u>16.4</u>**						
445.gobmk	892	11.8	892	11.8	**<u>892</u>**	**<u>11.8</u>**						
456.hmmer	1242	7.51	**<u>1243</u>**	**<u>7.51</u>**	1255	7.44						
458.sjeng	1079	11.2	**<u>1079</u>**	**<u>11.2</u>**	1079	11.2						
462.libquantum	1512	13.7	1511	13.7	**<u>1511</u>**	**<u>13.7</u>**						
464.h264ref	1195	18.5	**<u>1195</u>**	**<u>18.5</u>**	1195	18.5						
471.omnetpp	582	10.7	581	10.8	**<u>581</u>**	**<u>10.8</u>**						
473.astar	**<u>817</u>**	**<u>8.59</u>**	817	8.59	817	8.59						
483.xalancbmk	490	14.1	**<u>489</u>**	**<u>14.1</u>**	489	14.1						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Tested systems can be used with Shin-G ATX case, Antec NeoPower 480W power supply
Product description located as of 1/2007:
http://www.intel.com/products/motherboard/DG965SS/index.htm
The system bus runs at 800 MHz
```

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Base Portability Flags

```
     403.gcc: -DSPEC_CPU_WIN32
 464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

Base Optimization Flags

C benchmarks:
```
-fast /F512000000 shlw32m.lib          -link /FORCE:MULTIPLE
```

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Base Optimization Flags (Continued)

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

> 403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.04.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.04.xml`

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