



SPEC® CINT2006 Result

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IBM Corporation

SPECint®_rate2006 = Not Run

IBM System x3850 (Intel Xeon 7130N)

SPECint_rate_base2006 = 57.9

CPU2006 license: 11

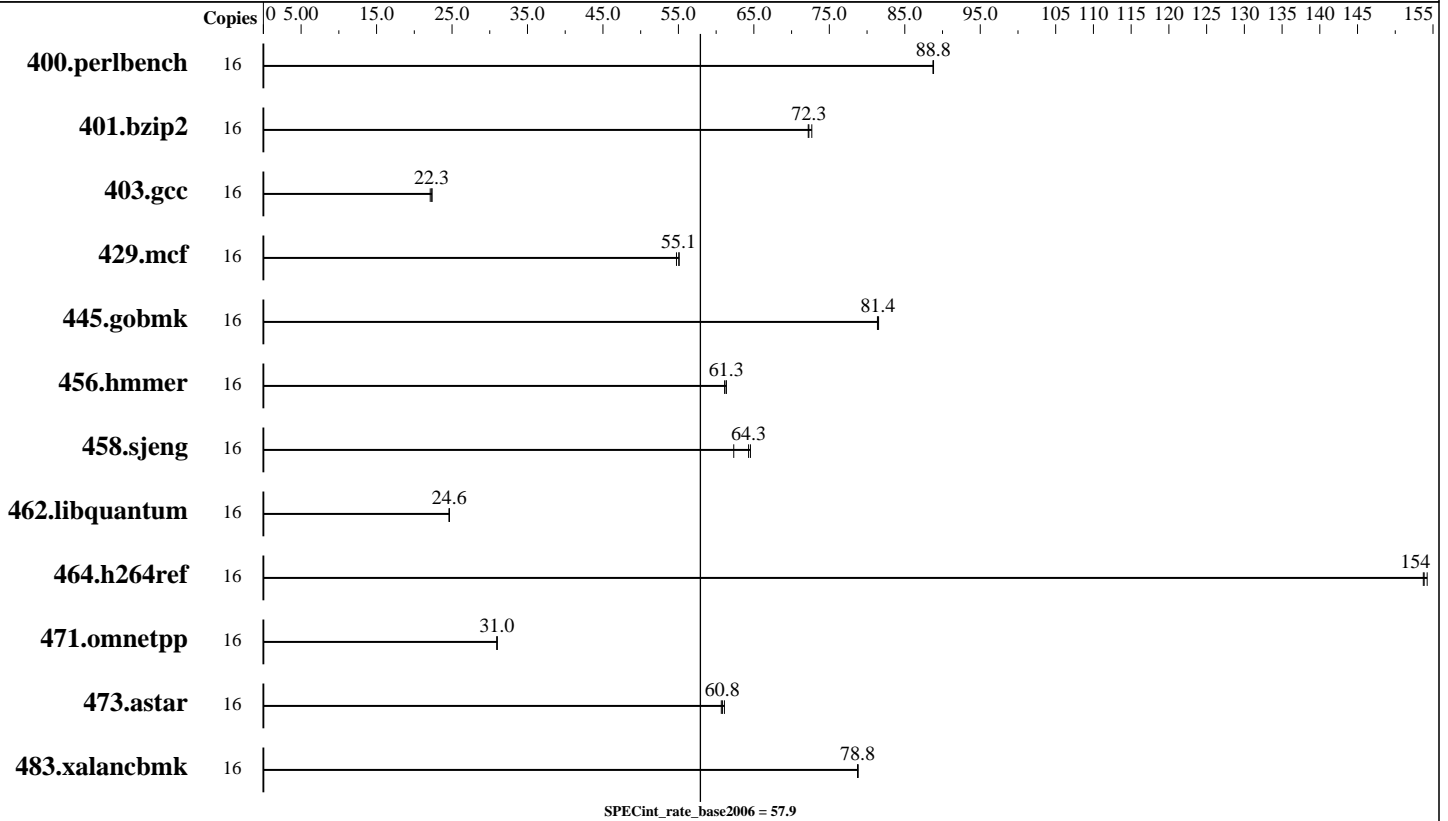
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Feb-2007

Hardware Availability: Oct-2006

Software Availability: Aug-2006



Hardware

CPU Name: Intel Xeon 7130N
 CPU Characteristics: 667 MHz bus
 CPU MHz: 3166
 FPU: Integrated
 CPU(s) enabled: 8 cores, 4 chips, 2 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2,4 chips
 Primary Cache: 12 K micro-ops I + 16 KB D on chip per core
 Secondary Cache: 1 MB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 32 GB (16 x 2048 MB ECC PC2-3200)
 Disk Subsystem: 73 GB SAS, 10k RPM
 Other Hardware: None

Software

Operating System: Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
 Compiler: Intel C++ Compiler for IA32 version 9.1
 Build no 20060816
 Microsoft Visual Studio .Net 2003 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: Smart Heap Library, Version 8



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Results Table

| Benchmark | Base | | | | | | Peak | | | | | | | |
|----------------|--------|---------------------|--------------------|--------------------|--------------------|--------------------|--------------------|--------|---------|-------|---------|-------|---------|-------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 16 | 1760 | 88.8 | <u>1761</u> | <u>88.8</u> | 1761 | 88.7 | | | | | | | |
| 401.bzip2 | 16 | 2124 | 72.7 | 2138 | 72.2 | <u>2136</u> | <u>72.3</u> | | | | | | | |
| 403.gcc | 16 | 5825 | 22.1 | 5765 | 22.3 | <u>5770</u> | <u>22.3</u> | | | | | | | |
| 429.mcf | 16 | <u>2650</u> | <u>55.1</u> | 2648 | 55.1 | 2665 | 54.7 | | | | | | | |
| 445.gobmk | 16 | 2059 | 81.5 | <u>2061</u> | <u>81.4</u> | 2063 | 81.4 | | | | | | | |
| 456.hammer | 16 | <u>2434</u> | <u>61.3</u> | 2443 | 61.1 | 2433 | 61.3 | | | | | | | |
| 458.sjeng | 16 | 3106 | 62.3 | 2999 | 64.5 | <u>3011</u> | <u>64.3</u> | | | | | | | |
| 462.libquantum | 16 | <u>13459</u> | <u>24.6</u> | 13454 | 24.6 | 13461 | 24.6 | | | | | | | |
| 464.h264ref | 16 | <u>2302</u> | <u>154</u> | 2296 | 154 | 2303 | 154 | | | | | | | |
| 471.omnetpp | 16 | 3227 | 31.0 | 3231 | 31.0 | <u>3230</u> | <u>31.0</u> | | | | | | | |
| 473.astar | 16 | 1851 | 60.7 | 1838 | 61.1 | <u>1846</u> | <u>60.8</u> | | | | | | | |
| 483.xalancbmk | 16 | 1401 | 78.8 | <u>1402</u> | <u>78.8</u> | 1402 | 78.7 | | | | | | | |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Bios Settings

Hardware Prefetch enabled

Memory Array set to High-Performance Memory Array

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Base Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
```

```
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlw32m.lib
```

```
-link /FORCE:MULTIPLE
```

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info@spec.org

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Base Optimization Flags (Continued)

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib  
-link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.01.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.01.xml>

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For other inquiries, please contact webmaster@spec.org.

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