



SPEC® CINT2006 Result

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Fujitsu Siemens Computers

PRIMERGY RX200 S3, Intel Xeon processor E5310, 1.60 GHz

SPECint®_rate2006 = 62.6

SPECint_rate_base2006 = 59.4

CPU2006 license: 22

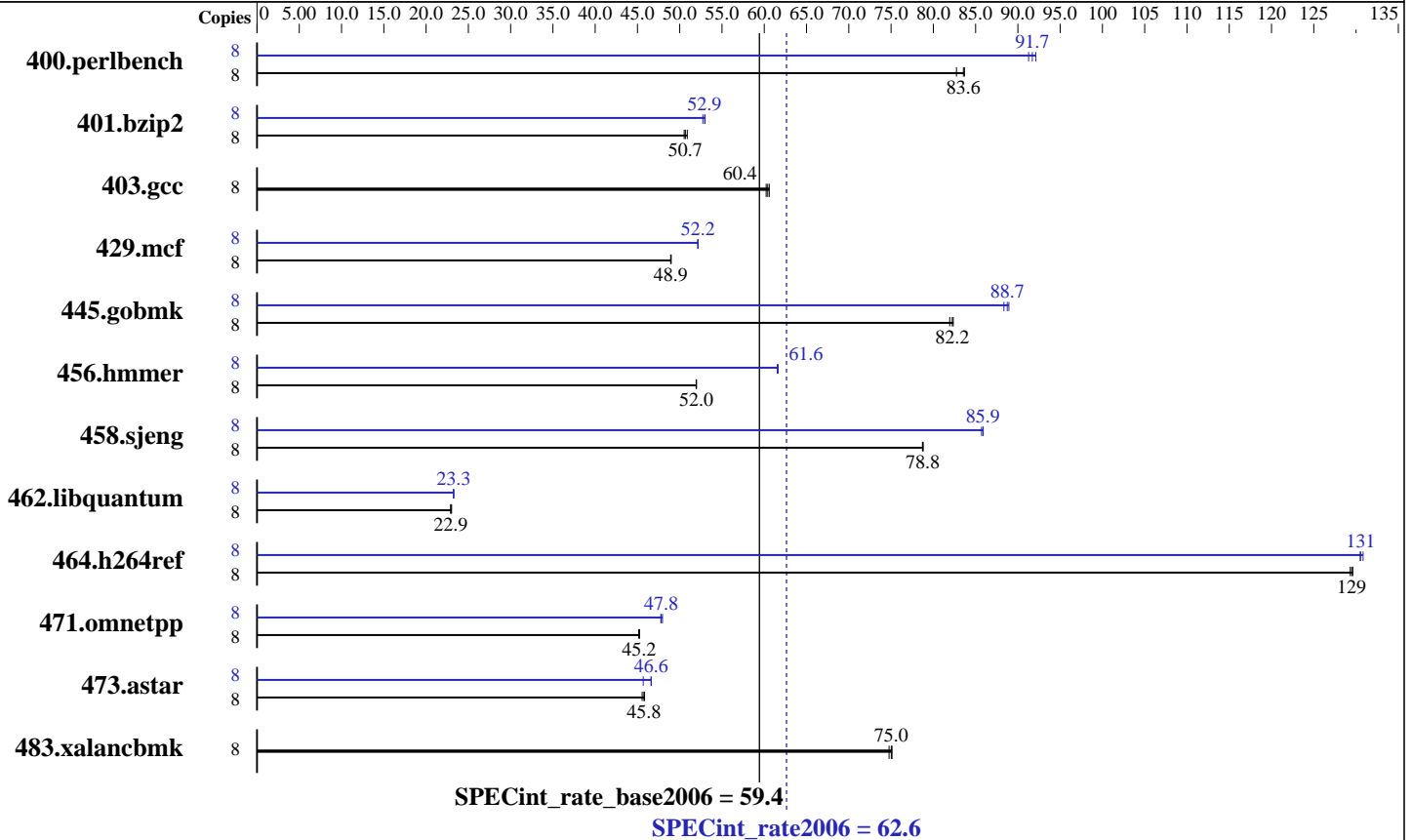
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: May-2007

Hardware Availability: Nov-2006

Software Availability: Mar-2007



Hardware

CPU Name: Intel Xeon E5310
 CPU Characteristics: 1067 MHz system bus
 CPU MHz: 1600
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x2 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
 Disk Subsystem: SAS (73GB 15400 rpm)
 Other Hardware: None

Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
 Compiler: Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047
 Auto Parallel: No
 File System: ext2
 System State: Multiuser, Runlevel 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Smart Heap Library, Version 8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	934	83.7	<u>935</u>	<u>83.6</u>	945	82.7	8	856	91.3	<u>852</u>	<u>91.7</u>	849	92.1
401.bzip2	8	1528	50.5	<u>1524</u>	<u>50.7</u>	1517	50.9	8	1464	52.7	<u>1458</u>	<u>52.9</u>	1456	53.0
403.gcc	8	<u>1066</u>	<u>60.4</u>	1063	60.6	1069	60.2	8	<u>1066</u>	<u>60.4</u>	1063	60.6	1069	60.2
429.mcf	8	<u>1491</u>	<u>48.9</u>	1491	48.9	1489	49.0	8	1400	52.1	1399	52.2	<u>1399</u>	<u>52.2</u>
445.gobmk	8	<u>1020</u>	<u>82.2</u>	1024	81.9	1019	82.4	8	<u>946</u>	<u>88.7</u>	944	88.9	950	88.3
456.hmmer	8	<u>1437</u>	<u>52.0</u>	1436	52.0	1437	51.9	8	1213	61.5	1211	61.6	<u>1212</u>	<u>61.6</u>
458.sjeng	8	1228	78.8	1230	78.7	<u>1229</u>	<u>78.8</u>	8	<u>1127</u>	<u>85.9</u>	1130	85.7	1127	85.9
462.libquantum	8	<u>7229</u>	<u>22.9</u>	7236	22.9	7202	23.0	8	7119	23.3	<u>7127</u>	<u>23.3</u>	7136	23.2
464.h264ref	8	<u>1367</u>	<u>129</u>	1369	129	1366	130	8	1353	131	1357	130	<u>1356</u>	<u>131</u>
471.omnetpp	8	1107	45.2	1105	45.3	<u>1106</u>	<u>45.2</u>	8	1042	48.0	1047	47.8	<u>1046</u>	<u>47.8</u>
473.astar	8	<u>1227</u>	<u>45.8</u>	1233	45.6	1225	45.8	8	1229	45.7	1205	46.6	<u>1205</u>	<u>46.6</u>
483.xalancbmk	8	<u>736</u>	<u>75.0</u>	738	74.8	735	75.1	8	<u>736</u>	<u>75.0</u>	738	74.8	735	75.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs

General Notes

The system bus runs at 1067 MHz

All binaries were built with 32-bit Intel compiler except:
401.bzip2, 456.hmmer and 462.libquantum in peak were built with
64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:
Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

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Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

456.hmmer: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

462.libquantum: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof_gen(pass 1) -prof_use(pass 2) -fast

401.bzip2: -fast

403.gcc: basepeak = yes

429.mcf: -prof_gen(pass 1) -prof_use(pass 2) -fast
-L/opt/SmartHeap_8_1/lib -lsmartheap

445.gobmk: Same as 429.mcf

456.hmmer: Same as 400.perlbench

458.sjeng: Same as 429.mcf

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 429.mcf

C++ benchmarks:

471.omnetpp: -prof_gen(pass 1) -prof_use(pass 2) -xP -O3 -ipo
-no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

473.astar: -prof_gen(pass 1) -prof_use(pass 2) -fast
-L/opt/SmartHeap_8_1/lib -lsmartheap

483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 11:16:48 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 26 June 2007.