



SPEC® CINT2006 Result

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Itautec

SPECint®_rate2006 = 92.7

Servidor Itautec MX201 (Intel Xeon X5355)

SPECint_rate_base2006 = 85.9

CPU2006 license: 9001

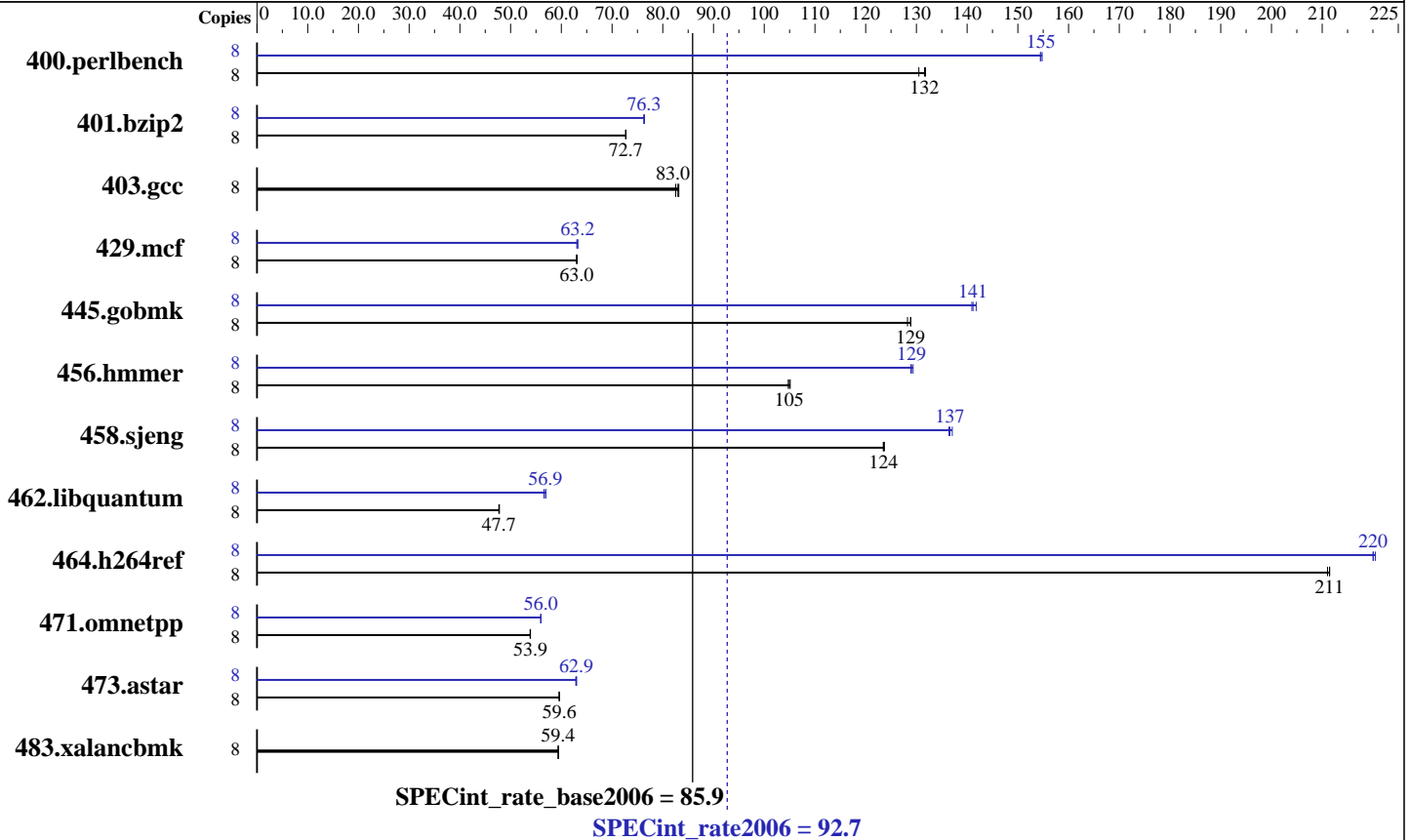
Test date: Nov-2007

Test sponsor: Itautec

Hardware Availability: Feb-2007

Tested by: Itautec

Software Availability: Aug-2007



Hardware

CPU Name: Intel Xeon X5355
 CPU Characteristics: Quad Core, 2.66 GHz
 CPU MHz: 2660
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 * 2GB DDR2 5300F, 2 rank, CL5 ECC)
 Disk Subsystem: 73GB SCSI, 15000rpm
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64), Kernel 2.6.16.21-0.8-smp
 Compiler: Intel C++ Compiler for Linux32 version 10.0 Build 20070809 Package ID: l_cc_p_10.0.026
 Auto Parallel: No
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Binutils 2.16.91.0.7



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	593	132	<u>594</u>	<u>132</u>	599	130	8	505	155	<u>506</u>	<u>155</u>	506	154
401.bzip2	8	1062	72.7	1061	72.8	<u>1062</u>	<u>72.7</u>	8	<u>1012</u>	<u>76.3</u>	1011	76.4	1012	76.3
403.gcc	8	780	82.5	<u>776</u>	<u>83.0</u>	774	83.2	8	780	82.5	<u>776</u>	<u>83.0</u>	774	83.2
429.mcf	8	<u>1157</u>	<u>63.0</u>	1158	63.0	1156	63.1	8	1154	63.2	<u>1154</u>	<u>63.2</u>	1157	63.0
445.gobmk	8	651	129	<u>651</u>	<u>129</u>	654	128	8	595	141	592	142	<u>595</u>	<u>141</u>
456.hmmer	8	710	105	<u>711</u>	<u>105</u>	713	105	8	577	129	<u>578</u>	<u>129</u>	579	129
458.sjeng	8	<u>783</u>	<u>124</u>	783	124	784	123	8	710	136	706	137	<u>709</u>	<u>137</u>
462.libquantum	8	3468	47.8	3475	47.7	<u>3475</u>	<u>47.7</u>	8	<u>2914</u>	<u>56.9</u>	2930	56.6	2910	57.0
464.h264ref	8	<u>837</u>	<u>211</u>	837	211	839	211	8	<u>805</u>	<u>220</u>	805	220	803	220
471.omnetpp	8	927	53.9	929	53.8	<u>928</u>	<u>53.9</u>	8	894	56.0	893	56.0	<u>893</u>	<u>56.0</u>
473.astar	8	943	59.5	<u>942</u>	<u>59.6</u>	942	59.6	8	<u>892</u>	<u>62.9</u>	893	62.9	891	63.0
483.xalancbmk	8	929	59.4	<u>930</u>	<u>59.4</u>	930	59.4	8	929	59.4	<u>930</u>	<u>59.4</u>	930	59.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Bios settings:
Hardware Prefetcher: Disabled
Adjacent Sector Prefetch: Disabled
ulimit -s unlimited used to set stack size to unlimited
taskset command used to assign copies to cores
All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer,
for peak, are compiled in 64-bit mode
This result was measured on the Servidor Itautec MX201.
The Servidor Itautec MX201 and the Servidor Itautec MX221 are electronically equivalent.

Base Compiler Invocation

C benchmarks:
icc
C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc
401.bzip2: /opt/intel/cce/10.0.026/bin/icc
456.hmmer: /opt/intel/cce/10.0.026/bin/icc

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:
400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch
401.bzip2: -L/opt/intel/cce/10.0.026/lib -I/opt/intel/cce/10.0.026/include
-prof-gen(pass 1) -prof-use(pass 2) -fast

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Peak Optimization Flags (Continued)

403.gcc: basepeak = yes

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec_div -ansi-alias

456.hmmr: -L/opt/intel/cce/10.0.026/lib -I/opt/intel/cce/10.0.026/include
-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -Ob0
-prefetch -opt-streaming-stores always

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec_div -ansi-alias -Wl,-z,muldefs

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itaotec-ic10-ia32-intel64-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itaotec-ic10-ia32-intel64-linux-flags.xml>



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