



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint®_rate2006 = 109

PowerEdge 2950 III (Intel Xeon E5410, 2.33 GHz)

SPECint_rate_base2006 = 93.5

CPU2006 license: 55

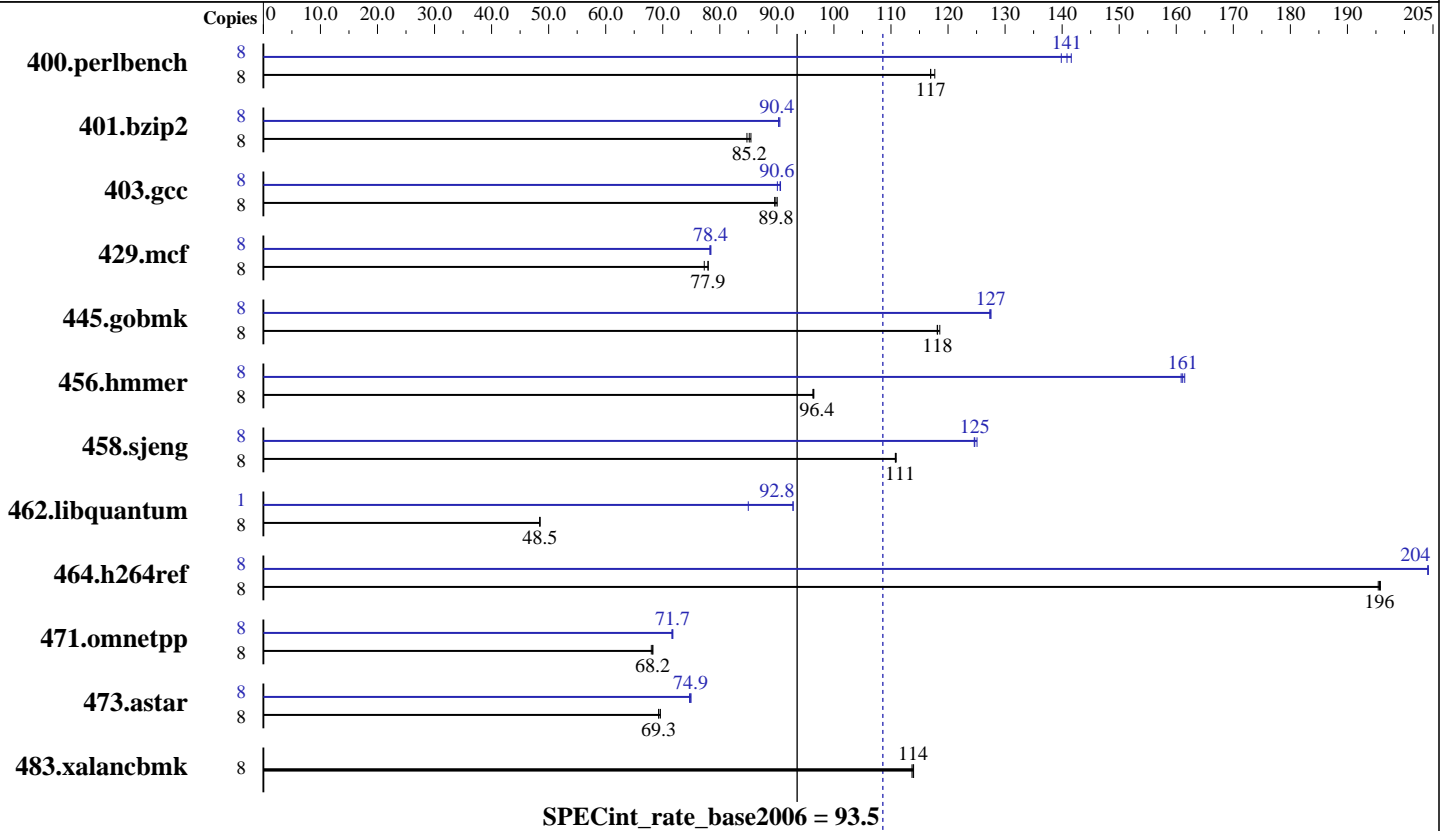
Test date: Jan-2008

Test sponsor: Dell Inc.

Hardware Availability: Nov-2007

Tested by: Dell Inc.

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5410
 CPU Characteristics: 1333 MHz system bus
 CPU MHz: 2333
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x2 GB DDR2-667 ECC CL5 FB-DIMM)
 Disk Subsystem: 1 x 73 GB SAS 15k RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.17.50.0.15



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	664	118	668	117	668	117	8	559	140	552	142	555	141
401.bzip2	8	907	85.2	903	85.5	911	84.8	8	853	90.5	855	90.3	854	90.4
403.gcc	8	719	89.6	715	90.1	717	89.8	8	710	90.6	715	90.1	711	90.6
429.mcf	8	944	77.3	936	77.9	936	78.0	8	932	78.3	930	78.4	931	78.4
445.gobmk	8	711	118	710	118	708	119	8	658	128	659	127	658	127
456.hammer	8	775	96.3	774	96.4	774	96.5	8	462	161	464	161	463	161
458.sjeng	8	874	111	873	111	874	111	8	776	125	774	125	777	125
462.libquantum	8	3421	48.5	3421	48.5	3420	48.5	1	223	92.8	244	85.0	223	92.8
464.h264ref	8	904	196	906	195	905	196	8	867	204	867	204	867	204
471.omnetpp	8	733	68.2	735	68.0	733	68.2	8	697	71.8	698	71.6	697	71.7
473.astar	8	810	69.3	810	69.3	807	69.6	8	752	74.7	749	74.9	750	74.9
483.xalancbmk	8	485	114	486	114	484	114	8	485	114	486	114	484	114

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

OMP_NUM_THREADS set to number of cores
KMP_STACK_SIZE set to 64M
KMP_AFFINITY set to physical,0

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs

Platform Notes

BIOS Settings:
Adjacent Cache Line Prefetch = Disabled (default Enabled)
Hardware Prefetcher = Disabled (default Enabled)

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

483.xalanbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

483.xalanbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.12.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.12.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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