SPEC® CINT2006 Result

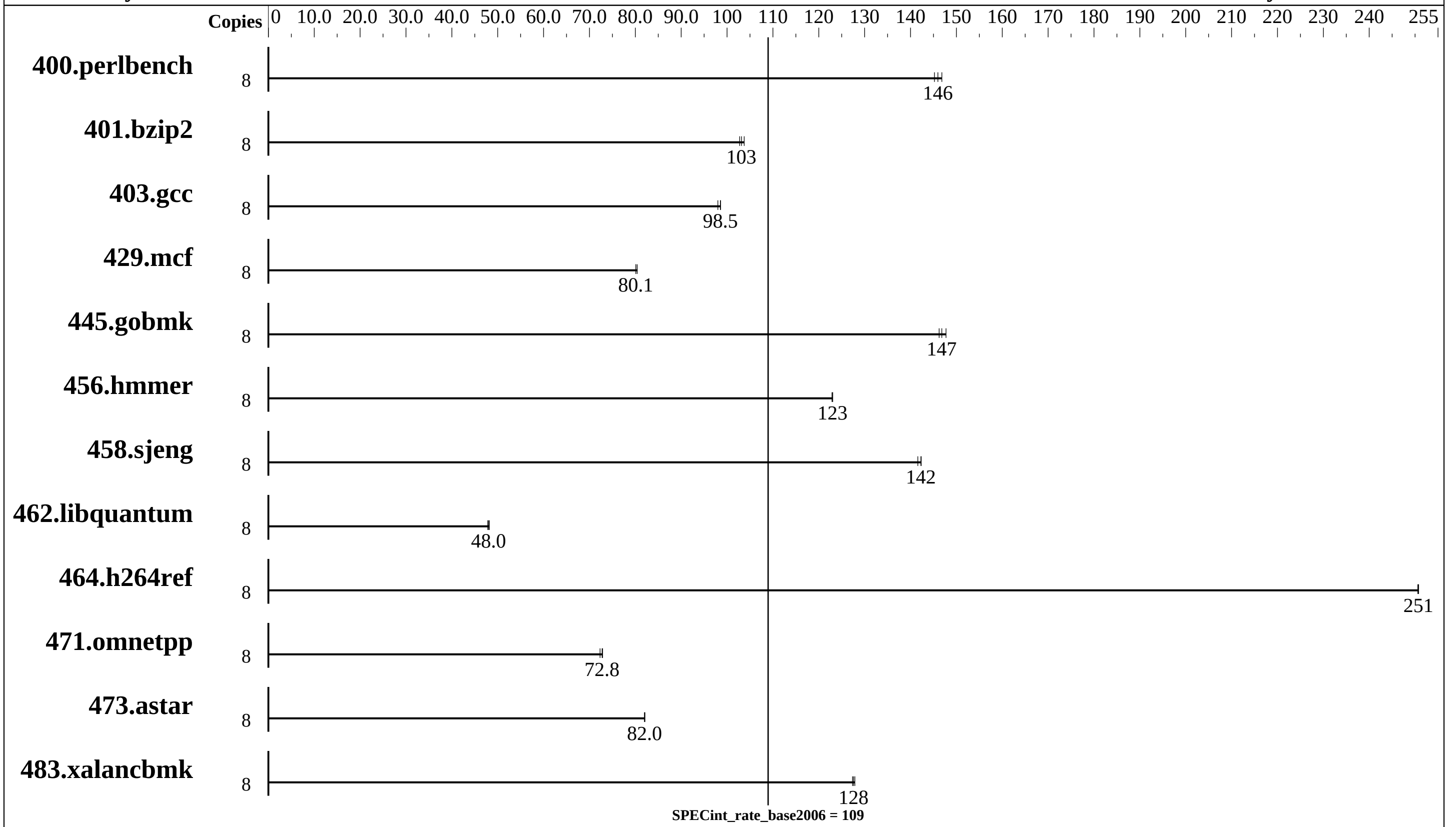
Dell Inc.

PowerEdge M600 (Intel Xeon E5450, 3.00 GHz)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 109

CPU2006 license:	55	**Test date:**	Feb-2008
Test sponsor:	Dell Inc.	**Hardware Availability:**	Jan-2008
Tested by:	Dell Inc.	**Software Availability:**	Nov-2007



Hardware

CPU Name:	Intel Xeon E5450
CPU Characteristics:	1333 MHz system bus
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (4x4 GB 667 MHz ECC CL5 FB-DIMM)
Disk Subsystem:	1 x 80 GB 7.2k SATA
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16-60.0.21-smp
Compiler:	Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
Auto Parallel:	No
File System:	ReiserFS
System State:	Multi-user, run level 3
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Microquill SmartHeap V8.1 Binutils 2.17.50.0.15

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	**535**	**146**	532	147	538	145							
401.bzip2	8	**748**	**103**	751	103	744	104							
403.gcc	8	657	98.0	**653**	**98.5**	653	98.6							
429.mcf	8	907	80.4	**911**	**80.1**	911	80.1							
445.gobmk	8	568	148	**572**	**147**	574	146							
456.hmmer	8	**607**	**123**	607	123	607	123							
458.sjeng	8	684	142	680	142	**680**	**142**							
462.libquantum	8	3466	47.8	**3453**	**48.0**	3441	48.2							
464.h264ref	8	**706**	**251**	706	251	706	251							
471.omnetpp	8	**687**	**72.8**	691	72.3	686	72.9							
473.astar	8	**685**	**82.0**	684	82.1	685	82.0							
483.xalancbmk	8	433	127	432	128	**433**	**128**							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

```
OMP_NUM_THREADS set to number of cores
KMP_STACK_SIZE set to 64M
KMP_AFFINITY set to physical,0
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-fast -inline-calloc -opt-malloc-options=3`

C++ benchmarks:
`-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs`
`-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Dell-Intel-ic10.1-int-linux64-revC.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Dell-Intel-ic10.1-int-linux64-revC.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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