



SPEC[®] CINT2006 Result

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Dell Inc.

SPECint[®]_rate2006 = Not Run

PowerEdge M600 (Intel Xeon E5420, 2.50 GHz)

SPECint_rate_base2006 = 99.1

CPU2006 license: 55

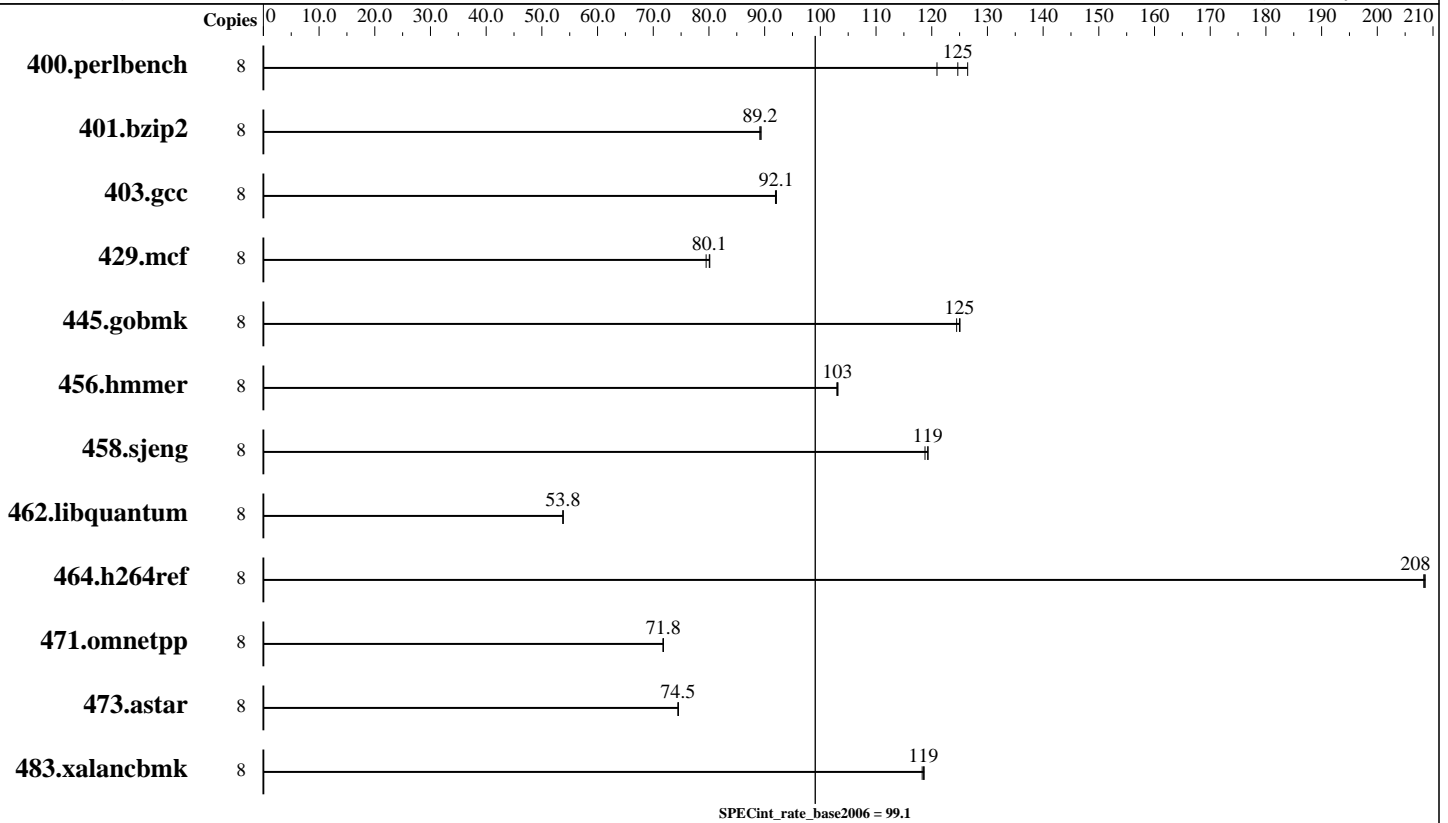
Test date: Jul-2008

Test sponsor: Dell Inc.

Hardware Availability: Jan-2008

Tested by: Dell Inc.

Software Availability: May-2008



Hardware

CPU Name: Intel Xeon E5420
 CPU Characteristics:
 CPU MHz: 2500
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (4x4 GB, DDR2-667 MHz, CL5, Reg, Dual Rank)
 Disk Subsystem: 1 x 80 GB 5400 RPM SATA
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16-60.0.21-smp
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20080312 Package ID: l_cc_p_10.1.015
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	646	121	627	125	618	126							
401.bzip2	8	866	89.2	864	89.4	866	89.2							
403.gcc	8	699	92.1	700	91.9	699	92.1							
429.mcf	8	910	80.2	918	79.5	911	80.1							
445.gobmk	8	671	125	671	125	674	124							
456.hammer	8	724	103	725	103	723	103							
458.sjeng	8	811	119	812	119	815	119							
462.libquantum	8	3081	53.8	3081	53.8	3081	53.8							
464.h264ref	8	850	208	849	208	849	209							
471.omnetpp	8	696	71.8	697	71.7	696	71.8							
473.astar	8	754	74.5	754	74.5	754	74.5							
483.xalancbmk	8	467	118	465	119	466	119							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

Platform Notes

BIOS Settings:

Adjacent Cache Line Prefetch = Disabled (Default Enabled)

Hardware Prefetcher = Disabled (Default Enabled)

General Notes

taskset was used to bind processes to cores except for 462.libquantum peak

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revD.20090713.00.html>
<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.07.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revD.20090713.00.xml>
<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.07.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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