



SPEC® CINT2006 Result

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Fujitsu Siemens Computers

SPECint®2006 = 25.1

CELSIUS M460, Intel Core 2 Duo E8600 processor

SPECint_base2006 = 22.6

CPU2006 license: 22

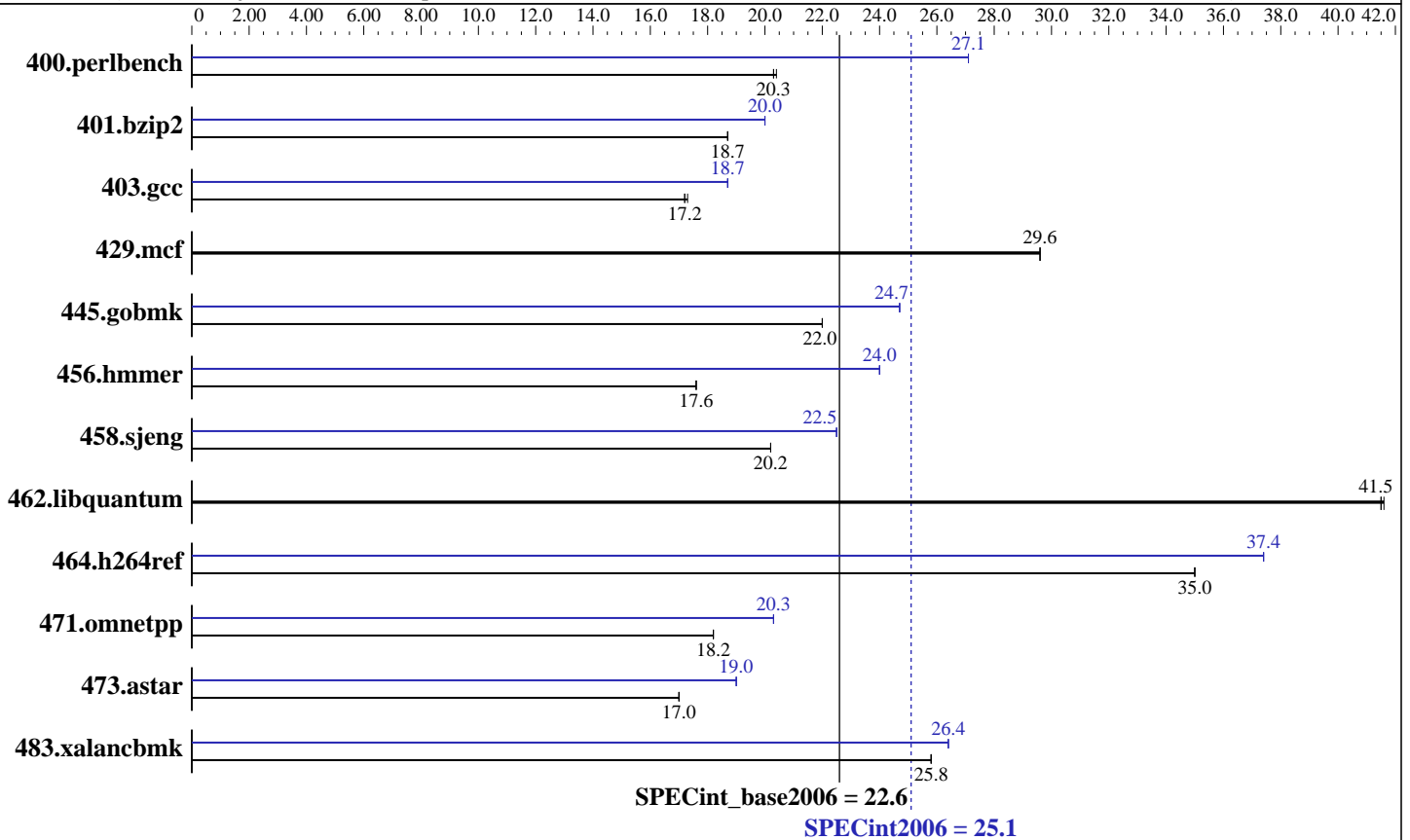
Test date: Oct-2008

Test sponsor: Fujitsu Siemens Computers

Hardware Availability: Sep-2008

Tested by: Fujitsu Siemens Computers

Software Availability: Feb-2008



Hardware

CPU Name: Intel Core 2 Duo E8600
 CPU Characteristics: 3333
 CPU MHz: Integrated
 FPU: 2 cores, 1 chip, 2 cores/chip
 CPU(s) enabled: 1 chip
 CPU(s) orderable: 32 KB I + 32 KB D on chip per core
 Primary Cache: 6 MB I+D on chip per chip
 Secondary Cache: None
 L3 Cache: None
 Other Cache: None
 Memory: 4 GB (4x1 GB PC2-6400 CL6 SDRAM)
 Disk Subsystem: 1 x 400 GB SATA 7200 RPM
 Other Hardware: None

Software

Operating System: Windows Vista Ultimate (x64)
 Compiler: Intel C++ Compiler for applications running on IA-32, Version 10.1, Build 20080312
 Intel C++ Compiler for applications running on Intel 64, Version 10.1, Build 20080312
 Microsoft Visual Studio 2005 with SP1 (for libraries)
 Auto Parallel: Yes
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap Library 9



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	481	20.3	480	20.4	480	20.3	361	27.1	361	27.1	360	27.1
401.bzip2	517	18.7	517	18.7	517	18.7	482	20.0	482	20.0	482	20.0
403.gcc	469	17.2	467	17.2	466	17.3	430	18.7	430	18.7	430	18.7
429.mcf	308	29.6	308	29.6	308	29.6	308	29.6	308	29.6	308	29.6
445.gobmk	478	22.0	478	22.0	478	22.0	426	24.7	426	24.7	426	24.7
456.hammer	530	17.6	529	17.6	529	17.6	389	24.0	389	24.0	389	24.0
458.sjeng	598	20.2	598	20.2	598	20.2	539	22.5	539	22.5	539	22.5
462.libquantum	499	41.6	499	41.5	500	41.5	499	41.6	499	41.5	500	41.5
464.h264ref	632	35.0	632	35.0	632	35.0	591	37.4	591	37.4	591	37.4
471.omnetpp	343	18.2	343	18.2	343	18.2	307	20.3	308	20.3	308	20.3
473.astar	414	17.0	414	17.0	413	17.0	370	19.0	370	19.0	370	19.0
483.xalancbmk	267	25.8	267	25.8	267	25.8	261	26.4	261	26.4	261	26.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Screen Saver was disabled
Power scheme was set to Always On, modified to Turnoff monitor: Never.

Platform Notes

BIOS default settings have been used.

General Notes

For information about Fujitsu Siemens Computers please see:
<http://www.fujitsu-siemens.com>

Base Compiler Invocation

C benchmarks:
icl -Qvc8 -Qc99

C++ benchmarks:
icl -Qvc8



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Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

Base Optimization Flags

C benchmarks:
-fast -Qparallel -Qvec-guard-write -Qpar-runtime-control -F512000000
libguide40.lib

C++ benchmarks:
-fast -Qcxx-features -F512000000 libguide40.lib shlw32M.lib
-link -FORCE:MULTIPLE

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icl -Qvc8 -Qc99

401.bzip2: C:\DevelTools\Intel\Compiler\C++\10.1.021\EM64T\Bin\icl.exe
-IC:\DevelTools\Intel\Compiler\C++\10.1.021\EM64T\Include
-link -LIBPATH:C:\DevelTools\Intel\Compiler\C++\10.1.021\EM64T\Lib
-link -LIBPATH:C:\ProgramFiles\MicrosoftVisualStudio8\VC\lib\AMD64
-link -LIBPATH:C:\ProgramFiles\MicrosoftVisualStudio8\VC\PlatformSDK\lib\AMD64
-link -LIBPATH:C:\ProgramFiles\MicrosoftVisualStudio8\VC\lib

456.hmmr: C:\DevelTools\Intel\Compiler\C++\10.1.021\EM64T\Bin\icl.exe
-IC:\DevelTools\Intel\Compiler\C++\10.1.021\EM64T\Include
-link -LIBPATH:C:\DevelTools\Intel\Compiler\C++\10.1.021\EM64T\Lib
-link -LIBPATH:C:\ProgramFiles\MicrosoftVisualStudio8\VC\lib\AMD64
-link -LIBPATH:C:\ProgramFiles\MicrosoftVisualStudio8\VC\PlatformSDK\lib\AMD64
-link -LIBPATH:C:\ProgramFiles\MicrosoftVisualStudio8\VC\lib

C++ benchmarks:
icl -Qvc8



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Peak Portability Flags

401.bzip2: -DSPEC_CPU_P64
 403.gcc: -DSPEC_CPU_WIN32
 456.hmmr: -DSPEC_CPU_P64
 464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
 483.xalancbmk: -Qoption, cpp, --no_wchar_t_keyword

Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qparallel
 -Qpar-runtime-control -Qansi-alias -Qprefetch -F512000000
 libguide40.lib shlW32M.lib -link -FORCE:MULTIPLE

401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch
 -F512000000 libguide40.lib

403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F512000000
 libguide40.lib

429.mcf: basepeak = yes

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -O2 -Qipo -QxT
 -Qprec-div- -Qansi-alias -F512000000

456.hmmr: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
 -Qansi-alias -Qopt-multi-version-aggressive -F512000000
 libguide40.lib

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
 -F512000000 libguide40.lib

462.libquantum: basepeak = yes

464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
 -Qansi-alias -F512000000 libguide40.lib

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx-features
 -Qansi-alias -Qopt-ra-region-strategy=block -F512000000
 libguide40.lib shlW32M.lib -link -FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx-features
 -Qansi-alias -Qopt-ra-region-strategy=routine -F512000000
 libguide40.lib shlW32M.lib -link -FORCE:MULTIPLE

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Peak Optimization Flags (Continued)

```
483.xalancbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx-features
-Qansi-alias -F512000000 libguide40.lib shlw32M.lib
-link -FORCE:MULTIPLE
```

Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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