SPEC® CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

HITACHI

BladeSymphony BS2000 (Intel Xeon X5570)

SPECint®_rate2006 = 243

SPECint_rate_base2006 = 227
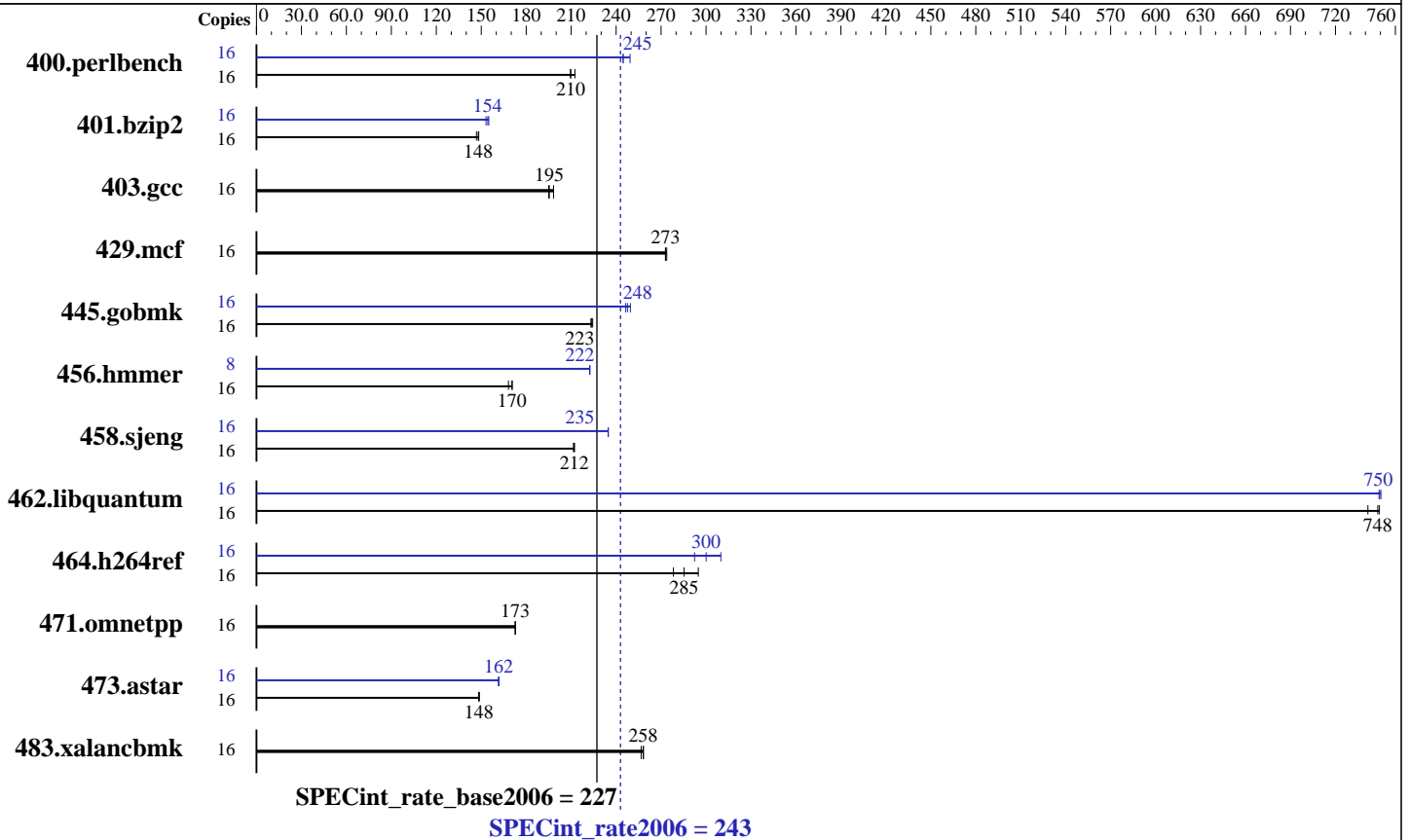
CPU2006 license: 872
Test sponsor: HITACHI
Tested by: HITACHI

Test date: Mar-2009
Hardware Availability: Mar-2009
Software Availability: Feb-2009

SPECint_rate_base2006 = 227

SPECint_rate2006 = 243

Hardware

CPU Name:	Intel Xeon X5570
CPU Characteristics:	Intel Turbo Boost Technology up to 3.33 GHz
CPU MHz:	2933
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1, 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	24 GB(6 x 4 GB PC3-10600R, 2 rank, CL=9)
Disk Subsystem:	1 x 147 GB 10000 rpm SAS
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 5.3, Advanced Platform, Kernel 2.6.18-128.el5 on an x86_64
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.081
Auto Parallel:	No
File System:	ext3
System State:	Multi-user run level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

HITACHI

BladeSymphony BS2000 (Intel Xeon X5570)

SPECint_rate2006 = 243

SPECint_rate_base2006 = 227

CPU2006 license: 872
Test sponsor: HITACHI
Tested by: HITACHI

Test date: Mar-2009
Hardware Availability: Mar-2009
Software Availability: Feb-2009

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	736	213	746	210	**745**	**210**	16	**639**	**245**	639	245	627	249
401.bzip2	16	1052	147	**1043**	**148**	1041	148	16	1008	153	**1000**	**154**	996	155
403.gcc	16	650	198	660	195	**659**	**195**	16	650	198	660	195	**659**	**195**
429.mcf	16	535	273	**535**	**273**	533	274	16	535	273	**535**	**273**	533	274
445.gobmk	16	748	224	**751**	**223**	752	223	16	**678**	**248**	673	250	681	246
456.hmmer	16	888	168	875	171	**876**	**170**	8	336	222	335	223	**336**	**222**
458.sjeng	16	913	212	**913**	**212**	915	212	16	**825**	**235**	825	235	825	235
462.libquantum	16	447	742	**443**	**748**	442	749	16	**442**	**750**	442	750	443	749
464.h264ref	16	1201	295	1273	278	**1241**	**285**	16	1143	310	**1180**	**300**	1211	292
471.omnetpp	16	579	173	**579**	**173**	579	173	16	579	173	**579**	**173**	579	173
473.astar	16	757	148	755	149	**757**	**148**	16	694	162	**695**	**162**	696	161
483.xalancbmk	16	**428**	**258**	430	257	427	258	16	**428**	**258**	430	257	427	258

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The config file option 'submit' was used.
    '/usr/bin/numactl' used to bind processes to CPUs

Operating System Notes

    'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Base Compiler Invocation

C benchmarks:
    icc

C++ benchmarks:
    icpc

Base Portability Flags

       400.perlbench: -DSPEC_CPU_LINUX_IA32
     462.libquantum: -DSPEC_CPU_LINUX
     483.xalancbmk: -DSPEC_CPU_LINUX

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

HITACHI

BladeSymphony BS2000 (Intel Xeon X5570)

SPECint_rate2006 = 243

SPECint_rate_base2006 = 227

CPU2006 license: 872
Test sponsor: HITACHI
Tested by: HITACHI

Test date: Mar-2009
Hardware Availability: Mar-2009
Software Availability: Feb-2009

Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/bsc/smartheap/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

```
401.bzip2: /opt/intel/Compiler/11.0/081/bin/intel64/icc
        -L/opt/intel/Compiler/11.0/081/ipp/em64t/lib
        -I/opt/intel/Compiler/11.0/081/ipp/em64t/include

456.hmmer: /opt/intel/Compiler/11.0/081/bin/intel64/icc
        -L/opt/intel/Compiler/11.0/081/ipp/em64t/lib
        -I/opt/intel/Compiler/11.0/081/ipp/em64t/include

458.sjeng: /opt/intel/Compiler/11.0/081/bin/intel64/icc
        -L/opt/intel/Compiler/11.0/081/ipp/em64t/lib
        -I/opt/intel/Compiler/11.0/081/ipp/em64t/include
```

C++ benchmarks:
```
icpc
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
   401.bzip2: -DSPEC_CPU_LP64
   456.hmmer: -DSPEC_CPU_LP64
   458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

HITACHI

BladeSymphony BS2000 (Intel Xeon X5570)

SPECint_rate2006 = 243

SPECint_rate_base2006 = 227

CPU2006 license: 872
Test sponsor: HITACHI
Tested by: HITACHI

Test date: Mar-2009
Hardware Availability: Mar-2009
Software Availability: Feb-2009

Peak Optimization Flags

C benchmarks:

    400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                   -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                   -prof-use(pass 2) -ansi-alias -opt-prefetch

        401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                   -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                   -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

          403.gcc: basepeak = yes

          429.mcf: basepeak = yes

        445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
                   -ipo -no-prec-div -ansi-alias

        456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
                   -ansi-alias -auto-ilp32

        458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                   -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                   -prof-use(pass 2) -unroll4 -auto-ilp32

    462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static
                    -opt-malloc-options=3 -opt-prefetch

       464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                    -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                    -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

      471.omnetpp: basepeak = yes

        473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                   -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                   -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
                   -L/home/bsc/smartheap/lib -lsmartheap

    483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

          403.gcc: -Dalloca=_alloca

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

HITACHI

BladeSymphony BS2000 (Intel Xeon X5570)

$SPECint\_rate2006 =$ 243

$SPECint\_rate\_base2006 =$ 227

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.05.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.05.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Tue Jul 22 23:30:00 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 31 March 2009.