



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

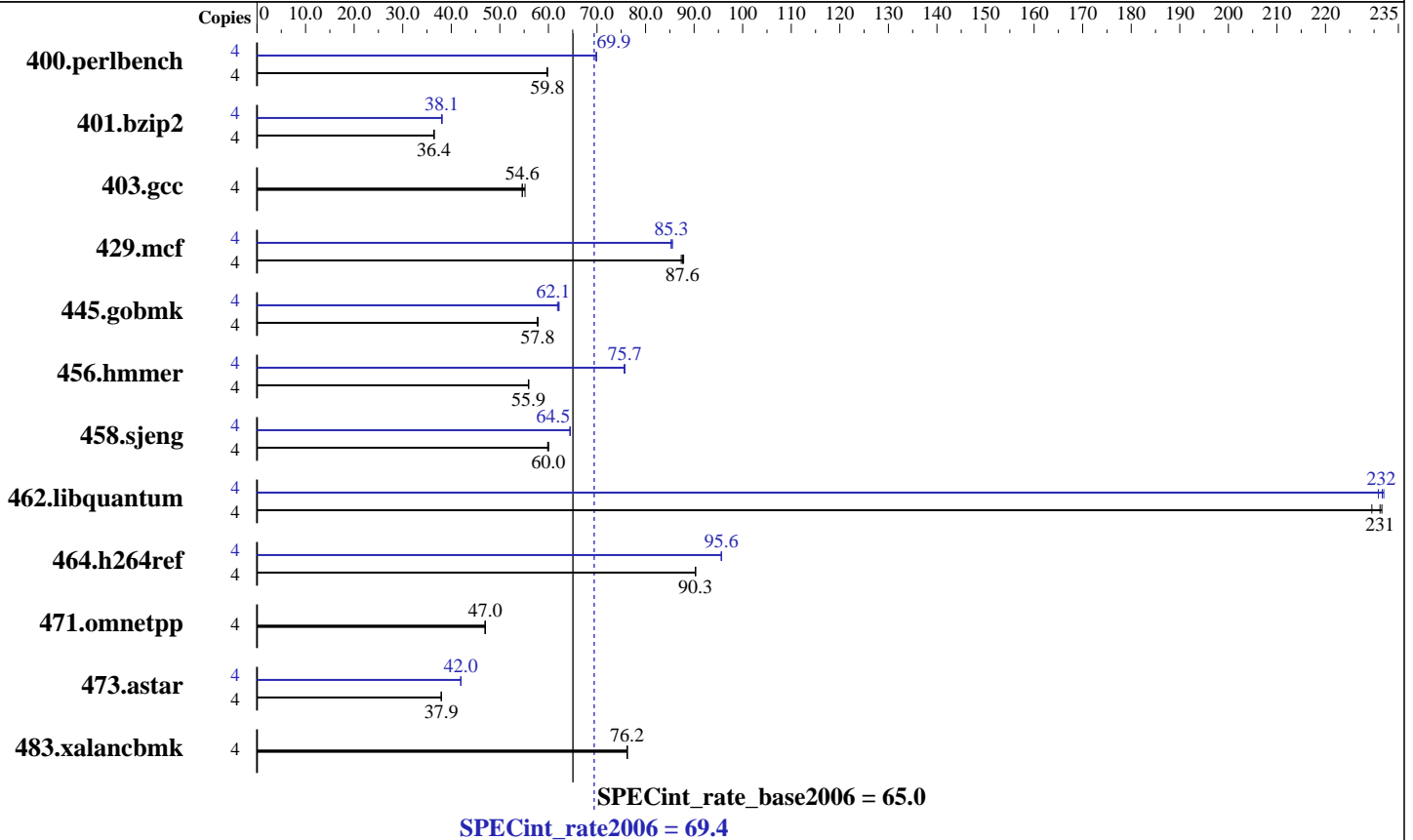
SPECint®_rate2006 = 69.4

Servidor Itautec MX223 (Intel Xeon E5504)

SPECint_rate_base2006 = 65.0

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Jul-2009
Hardware Availability: Mar-2009
Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon E5504
 CPU Characteristics:
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1, 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 4 MB I+D on chip per chip
 Other Cache: None
 Memory: 12 GB (6 x 2GB DDR3-1333, CL 9, ECC, running at 800MHz)
 Disk Subsystem: 1 x 160 GB SATA-2, 7200RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2 with patch Linux kernel 20090119, Kernel 2.6.16.60-0.34-smp
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.081
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECint_rate2006 = 69.4

Servidor Itaotec MX223 (Intel Xeon E5504)

SPECint_rate_base2006 = 65.0

CPU2006 license: 9001
Test sponsor: Itaotec
Tested by: Itaotec

Test date: Jul-2009
Hardware Availability: Mar-2009
Software Availability: Feb-2009

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
400.perlbench	4	654	59.8	655	59.7	654	59.8	4	559	69.9	559	69.9	560	69.8		
401.bzip2	4	1058	36.5	1059	36.4	1059	36.4	4	1015	38.0	1014	38.1	1013	38.1		
403.gcc	4	590	54.6	584	55.2	589	54.6	4	590	54.6	584	55.2	589	54.6		
429.mcf	4	415	87.8	416	87.6	418	87.4	4	428	85.3	426	85.5	428	85.3		
445.gobmk	4	726	57.8	725	57.9	727	57.7	4	675	62.2	678	61.9	675	62.1		
456.hammer	4	667	55.9	668	55.9	667	55.9	4	493	75.7	493	75.7	494	75.6		
458.sjeng	4	806	60.0	807	60.0	809	59.8	4	751	64.5	751	64.4	750	64.5		
462.libquantum	4	361	230	358	232	358	231	4	357	232	358	232	359	231		
464.h264ref	4	981	90.3	981	90.3	980	90.3	4	926	95.6	926	95.6	926	95.6		
471.omnetpp	4	532	47.0	532	47.0	532	47.0	4	532	47.0	532	47.0	532	47.0		
473.astar	4	740	37.9	740	37.9	739	38.0	4	669	42.0	669	42.0	669	41.9		
483.xalancbmk	4	362	76.2	362	76.2	362	76.2	4	362	76.2	362	76.2	362	76.2		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

General Notes

This result was measured on the Servidor Itaotec MX203.
The Servidor Itaotec MX223 and the Servidor Itaotec MX203 are electronically equivalent.

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECint_rate2006 = 69.4

Servidor Itaotec MX223 (Intel Xeon E5504)

SPECint_rate_base2006 = 65.0

CPU2006 license: 9001
Test sponsor: Itaotec
Tested by: Itaotec

Test date: Jul-2009
Hardware Availability: Mar-2009
Software Availability: Feb-2009

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/opt/sh/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/081/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/081/bin/intel64/icc

458.sjeng: /opt/intel/Compiler/11.0/081/bin/intel64/icc

C++ benchmarks (except as noted below):

icpc

473.astar: /opt/intel/Compiler/11.0/081/bin/intel64/icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECint_rate2006 = 69.4

Servidor Itaotec MX223 (Intel Xeon E5504)

SPECint_rate_base2006 = 65.0

CPU2006 license: 9001
Test sponsor: Itaotec
Tested by: Itaotec

Test date: Jul-2009
Hardware Availability: Mar-2009
Software Availability: Feb-2009

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: basepeak = yes

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static
-opt-malloc-options=3 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32
-Wl,-z,muldefs -L/opt/sh/SmartHeap_8.1/lib -lsmarheap64

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECint_rate2006 = 69.4

Servidor Itaotec MX223 (Intel Xeon E5504)

SPECint_rate_base2006 = 65.0

CPU2006 license: 9001
Test sponsor: Itaotec
Tested by: Itaotec

Test date: Jul-2009
Hardware Availability: Mar-2009
Software Availability: Feb-2009

Peak Other Flags (Continued)

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itaotec-Intel-ic11.0-int-linux64-revA.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itaotec-Intel-ic11.0-int-linux64-revA.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 03:07:11 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 5 August 2009.