SPEC® CINT2006 Result
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ACTION S.A.

ACTINA SOLAR 200 X3 (Intel Xeon E5504)

$SPECint^{®}\_rate2006 =$ Not Run

$SPECint\_rate\_base2006 = 64.6$
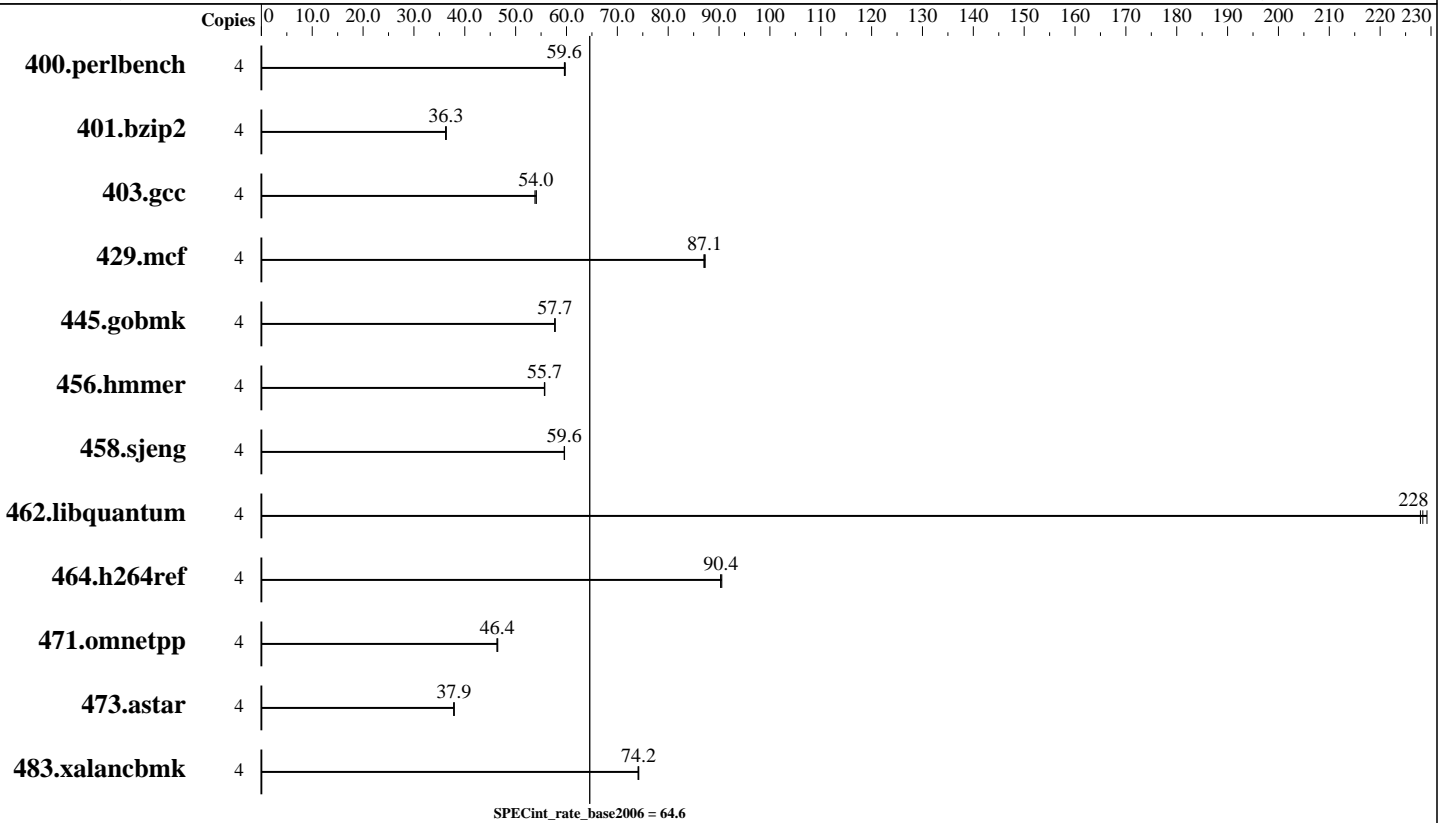
CPU2006 license: 9008
Test sponsor: ACTION S.A.
Tested by: ACTION S.A.

Test date: Jul-2009
Hardware Availability: Apr-2009
Software Availability: Feb-2009

Benchmark	Copies	Result
400.perlbench	4	59.6
401.bzip2	4	36.3
403.gcc	4	54.0
429.mcf	4	87.1
445.gobmk	4	57.7
456.hmmer	4	55.7
458.sjeng	4	59.6
462.libquantum	4	228
464.h264ref	4	90.4
471.omnetpp	4	46.4
473.astar	4	37.9
483.xalancbmk	4	74.2

SPECint_rate_base2006 = 64.6

Hardware

CPU Name: Intel Xeon E5504
CPU Characteristics:
CPU MHz: 2000
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 4 MB I+D on chip per chip
Other Cache: None
Memory: 24 GB (6 x 4 GB DDR3, 1066 MHz, Reg, ECC, running at 800
Disk Subsystem: 147 GB SAS, 10000 RPM
Other Hardware: None

Software

Operating System: SuSe Linux Enterprise Server 10 (x86_64) with SP2, kernel 2.6.16.60-0.21-smp
Compiler: Intel C++ Compiler 11.0 for Linux Build 20080930 Package ID: l_cproc_p_11.0.066
Auto Parallel: No
File System: ReiserFS
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502 Microquill SmartHeap V8.1

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Results Table

Benchmark	Copies	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Copies	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio
400.perlbench	4	**656**	**59.6**	656	59.6	654	59.8							
401.bzip2	4	**1063**	**36.3**	1066	36.2	1062	36.4							
403.gcc	4	**596**	**54.0**	596	54.1	599	53.8							
429.mcf	4	**419**	**87.1**	418	87.3	419	87.0							
445.gobmk	4	727	57.7	**727**	**57.7**	726	57.8							
456.hmmer	4	670	55.7	671	55.6	**670**	**55.7**							
458.sjeng	4	812	59.6	813	59.6	**812**	**59.6**							
462.libquantum	4	362	229	**363**	**228**	364	228							
464.h264ref	4	**979**	**90.4**	980	90.3	978	90.5							
471.omnetpp	4	538	46.5	**539**	**46.4**	539	46.3							
473.astar	4	743	37.8	741	37.9	**741**	**37.9**							
483.xalancbmk	4	372	74.2	**372**	**74.2**	373	74.1							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

General Notes

'numactl' was used to bind copies to the cores
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Base Compiler Invocation

C benchmarks:
     icc

C++ benchmarks:
     icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

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Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

> 403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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