



# SPEC® CINT2006 Result

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Dell Inc.

SPECint®\_rate2006 = 268

Dell Precision T7500 (Intel Xeon W5590, 3.33 GHz)

SPECint\_rate\_base2006 = 250

CPU2006 license: 55

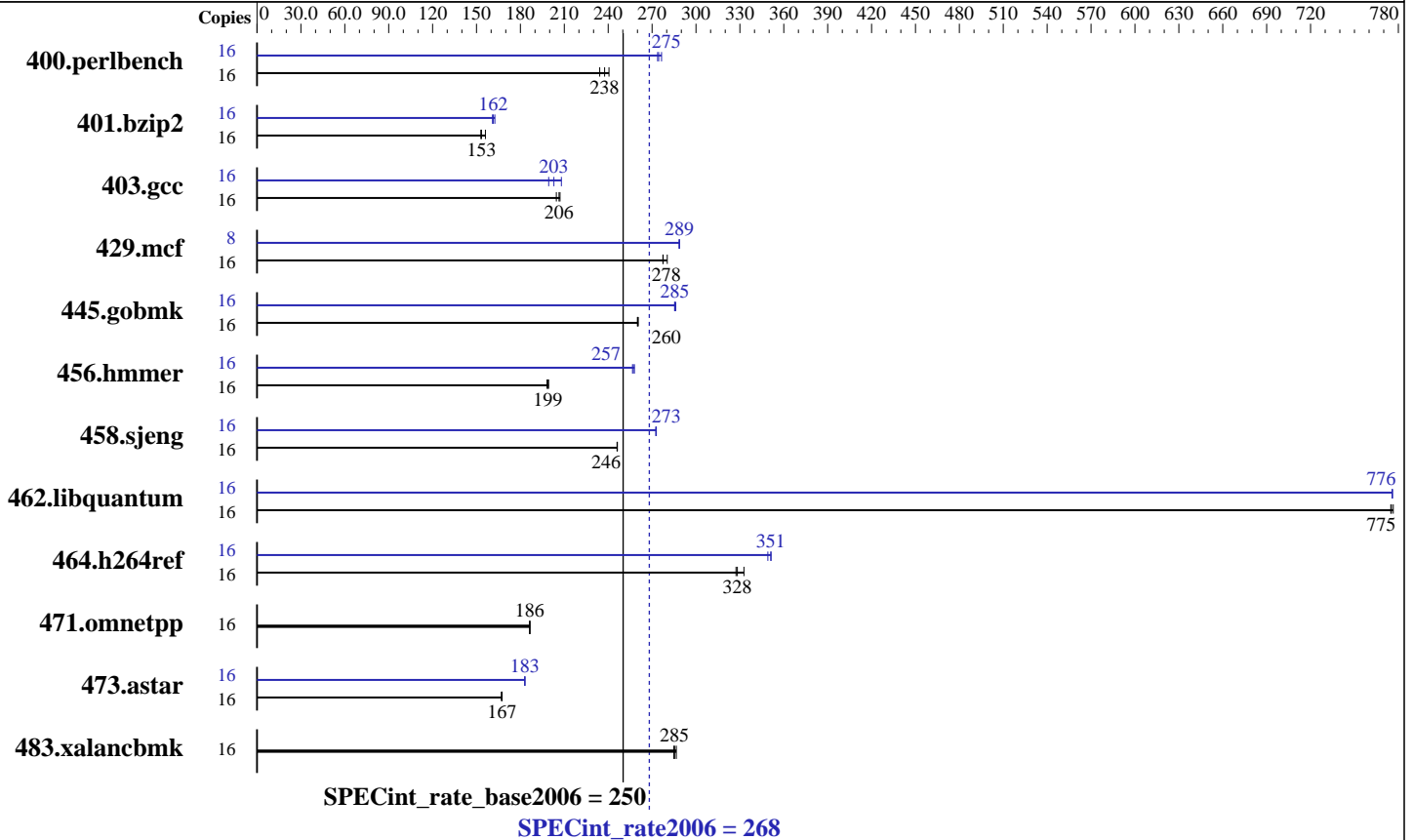
Test date: Aug-2009

Test sponsor: Dell Inc.

Hardware Availability: Aug-2009

Tested by: Dell Inc.

Software Availability: May-2009



## Hardware

CPU Name: Intel Xeon W5590  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz  
 CPU MHz: 3333  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (12 x 4 GB PC3-10600R)  
 Disk Subsystem: 1 x 146 GB 15000 RPM SAS  
 Other Hardware: None

## Software

Operating System: Red Hat Enterprise Linux Client release 5.3, Kernel 2.6.18-128.el5 on an x86\_64  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l\_cproc\_p\_11.0.080  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
400.perlbench	16	650	241	667	234	<b>658</b>	<b>238</b>	16	571	274	<b>569</b>	<b>275</b>	565	276		
401.bzip2	16	989	156	<b>1006</b>	<b>153</b>	1009	153	16	950	163	<b>956</b>	<b>162</b>	959	161		
403.gcc	16	630	205	<b>624</b>	<b>206</b>	622	207	16	<b>635</b>	<b>203</b>	619	208	646	199		
429.mcf	16	520	280	<b>526</b>	<b>278</b>	526	277	8	253	288	<b>253</b>	<b>289</b>	253	289		
445.gobmk	16	644	261	<b>645</b>	<b>260</b>	646	260	16	588	285	<b>588</b>	<b>285</b>	586	286		
456.hammer	16	753	198	<b>751</b>	<b>199</b>	749	199	16	<b>580</b>	<b>257</b>	579	258	582	257		
458.sjeng	16	787	246	<b>787</b>	<b>246</b>	786	246	16	<b>710</b>	<b>273</b>	709	273	710	273		
462.libquantum	16	<b>428</b>	<b>775</b>	427	777	428	775	16	427	776	<b>427</b>	<b>776</b>	427	776		
464.h264ref	16	<b>1079</b>	<b>328</b>	1082	327	1064	333	16	1008	351	1014	349	<b>1009</b>	<b>351</b>		
471.omnetpp	16	<b>536</b>	<b>186</b>	536	187	537	186	16	<b>536</b>	<b>186</b>	536	187	537	186		
473.astar	16	673	167	671	167	<b>671</b>	<b>167</b>	16	<b>614</b>	<b>183</b>	614	183	613	183		
483.xalanbmk	16	385	287	<b>387</b>	<b>285</b>	388	285	16	385	287	<b>387</b>	<b>285</b>	388	285		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Platform Notes

BIOS Settings:  
Memory Node Interleaving: NUMA  
Hyper-threading: ENABLE

## Base Compiler Invocation

C benchmarks:  
icc  
  
C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalanbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc

458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc

C++ benchmarks (except as noted below):

icpc

473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

401.bzip2: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LINUX

473.astar: -DSPEC\_CPU\_LP64

483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static  
-opt-malloc-options=3 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmarheap64

483.xalancbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/dell.flags.ic11.0.lin.20090915.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/dell.flags.ic11.0.lin.20090915.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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