



SPEC[®] CFP2006 Result

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Supermicro

SPECfp[®]2006 = 17.9

H8DM8-2, AMD Opteron 2423 HE

SPECfp_base2006 = 14.5

CPU2006 license: 001176

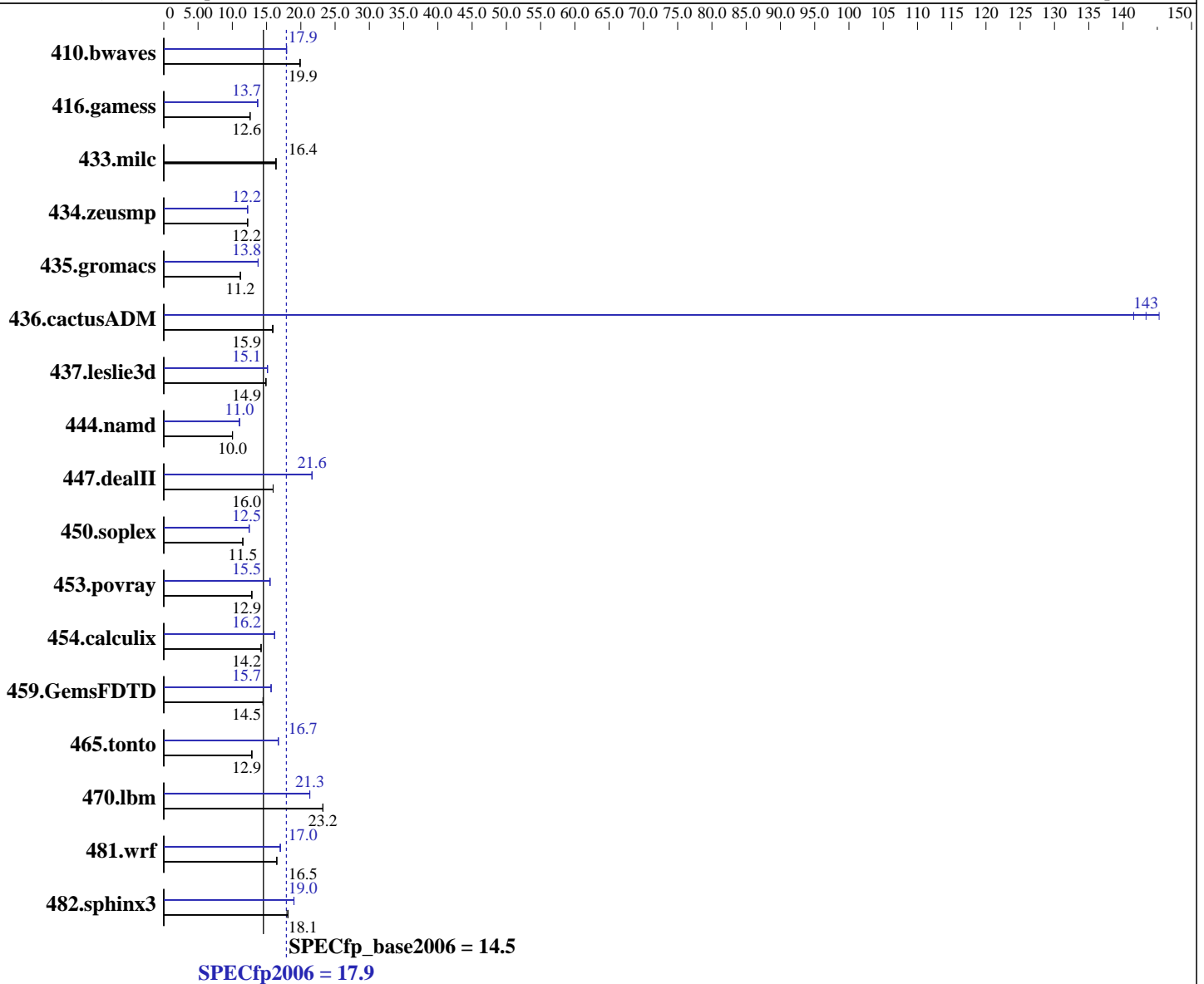
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Jul-2009

Hardware Availability: Jun-2009

Software Availability: Apr-2009



Hardware

CPU Name: AMD Opteron 2423 HE
 CPU Characteristics:
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 64 KB I + 64 KB D on chip per core
 Secondary Cache: 512 KB I+D on chip per core

Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 5.3, Advanced Platform, Kernel 2.6.18-128.el5
 Compiler: PGI Server Complete Version 8.0 x86 Open64 4.2.2 Compiler Suite (from AMD)
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 2 (Local multiuser without remote network)
 Base Pointers: 64-bit

Continued on next page



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L3 Cache: 6 MB I+D on chip per chip
Other Cache: None
Memory: 32 GB (8x4 GB, DDR2-800, CL5, Reg, Dual Rank)
Disk Subsystem: 1 x 500 GB SATA, 7200 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: binutils 2.18

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	685	19.9	684	19.9	682	19.9	759	17.9	758	17.9	758	17.9
416.gamess	1553	12.6	1556	12.6	1555	12.6	1432	13.7	1429	13.7	1428	13.7
433.milc	562	16.3	560	16.4	560	16.4	562	16.3	560	16.4	560	16.4
434.zeusmp	744	12.2	743	12.2	744	12.2	744	12.2	744	12.2	744	12.2
435.gromacs	638	11.2	638	11.2	639	11.2	519	13.8	519	13.8	519	13.8
436.cactusADM	752	15.9	754	15.8	749	16.0	84.4	142	83.4	143	82.3	145
437.leslie3d	630	14.9	632	14.9	630	14.9	622	15.1	621	15.1	620	15.1
444.namd	801	10.0	800	10.0	799	10.0	727	11.0	725	11.1	726	11.0
447.dealII	717	16.0	717	16.0	717	16.0	529	21.6	529	21.6	529	21.6
450.soplex	723	11.5	723	11.5	723	11.5	669	12.5	669	12.5	669	12.5
453.povray	414	12.9	413	12.9	415	12.8	342	15.5	344	15.5	343	15.5
454.calculix	582	14.2	581	14.2	580	14.2	510	16.2	510	16.2	509	16.2
459.GemsFDTD	732	14.5	733	14.5	732	14.5	677	15.7	678	15.7	677	15.7
465.tonto	765	12.9	765	12.9	768	12.8	589	16.7	589	16.7	589	16.7
470.lbm	592	23.2	592	23.2	592	23.2	645	21.3	645	21.3	645	21.3
481.wrf	677	16.5	677	16.5	677	16.5	657	17.0	656	17.0	657	17.0
482.sphinx3	1074	18.1	1085	18.0	1074	18.1	1027	19.0	1026	19.0	1025	19.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152' was used to set environment locked pages in memory limit

Set vm/nr_hugepages=5400 in /etc/sysctl.conf
mount -t hugetlbfs nodev /mnt/hugepages



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General Notes

Environment variables set by runspec before the start of the run:

HUGETLB_LIMIT = "450"

LD_LIBRARY_PATH = "/spec/amd0905is-libs/64:/spec/amd0905is-libs/32"

NCPUS = "12"

PGI_HUGE_PAGES = "450"

The x86 Open64 Compiler Suite is only available from (and supported by) AMD at <http://developer.amd.com/cpu/open64>.

To ensure system stability, a 550W (minimum) ATX power supply [4-pin (+12V), 8-pin (+12V) and 24-pin] are required.

Product description can be obtained at:

<http://www.supermicro.com/Aplus/motherboard/Opteron2000/MCP55/H8DM8-2.cfm>

Base Compiler Invocation

C benchmarks:

pgcc

C++ benchmarks:

pgcpp

Fortran benchmarks:

pgf95

Benchmarks using both Fortran and C:

pgcc pgf95

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -Mnomain
 436.cactusADM: -DSPEC_CPU_LP64 -Mnomain
 437.lelie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.deallI: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -Mnomain
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Continued on next page



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Base Portability Flags (Continued)

482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-fastsse -Msmartalloc=huge -Mfprelaxed -Mipa=fast -Mipa=inline
-tp shanghai-64 -Bstatic_pgi

C++ benchmarks:

-fastsse -Msmartalloc=huge -Mfprelaxed --zc_eh -Mipa=fast
-Mipa=inline -tp shanghai-64 -Bstatic_pgi

Fortran benchmarks:

-fastsse -Msmartalloc=huge -Mfprelaxed -Mvect=short -Mipa=fast
-Mipa=inline -tp shanghai-64 -Bstatic_pgi

Benchmarks using both Fortran and C:

-fastsse -Msmartalloc=huge -Mfprelaxed -Mipa=fast -Mipa=inline
-tp shanghai-64 -Mvect=short -Bstatic_pgi

Base Other Flags

C benchmarks:

-Mipa=jobs:4

C++ benchmarks:

-Mipa=jobs:4

Fortran benchmarks:

-Mipa=jobs:4

Benchmarks using both Fortran and C:

-Mipa=jobs:4

Peak Compiler Invocation

C benchmarks:

pgcc

C++ benchmarks (except as noted below):

openCC

444.namd: pgcpp

Continued on next page



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Peak Compiler Invocation (Continued)

Fortran benchmarks (except as noted below):

openf95
410.bwaves: pgf95
434.zeusmp: pgf95
437.leslie3d: pgf95

Benchmarks using both Fortran and C (except as noted below):

pgcc pgf95
435.gromacs: opencc openf95

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64
436.cactusADM: -DSPEC_CPU_LP64 -Mnomain
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -Mnomain
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes
470.lbm: -fastsse -Msmartalloc=huge -Mprefetch=t0 -Mloop32
-Mfprefaxed -Mipa=fast -Mipa=inline -tp shanghai-64
-Bstatic_pgi
482.sphinx3: -Mphi=indirect(pass 1) -Mpfo=indirect(pass 2)
-Mipa=fast(pass 2) -Mipa=inline(pass 2) -fastsse
-Mfprefaxed -Msmartalloc -tp shanghai-64 -Bstatic_pgi

Continued on next page



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Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -Mpfi(pass 1) -Mpfo(pass 2) -Mipa=fast(pass 2)
 -Mipa=inline(pass 2) -fastsse -Munroll=n:4 -Munroll=m:8
 -Msmartalloc=huge -Mnodepchk -Mfprelaxed --zc_eh
 -tp shanghai-64 -Bstatic_pgi

447.dealIII: -march=barcelona -Ofast -static -INLINE:aggressive=on
 -LNO:opt=0 -Wf,-fno-exceptions -m32 -OPT:unroll_times_max=8
 -OPT:unroll_size=256 -OPT:unroll_level=2 -HP:bdt=2m:heap=2m
 -GRA:unspill=on -CG:cmp_peep=on -TENV:frame_pointer=off

450.soplex: -march=barcelona -fb_create fbdata(pass 1)
 -fb_opt fbdata(pass 2) -O3 -INLINE:aggressive=on
 -OPT:IEEE_arith=3 -OPT:IEEE_NaN_Inf=off
 -OPT:fold_unsigned_relops=on -OPT:malloc_alg=1
 -CG:load_exe=0 -fno-exceptions -m32 -HP:bdt=2m

453.povray: -march=barcelona -fb_create fbdata(pass 1)
 -fb_opt fbdata(pass 2) -Ofast -INLINE:aggressive=on
 -HP:bdt=2m:heap=2m

Fortran benchmarks:

410.bwaves: -fastsse -Msmartalloc -Mprefetch=nta -Mfprelaxed
 -Mipa=fast -Mipa=inline -tp shanghai-64 -Bstatic_pgi

416.gamess: -march=barcelona -fb_create fbdata(pass 1)
 -fb_opt fbdata(pass 2) -O2 -OPT:Ofast -OPT:ro=3
 -OPT:unroll_size=256 -HP:bdt=2m:heap=2m

434.zeusmp: -fastsse -Mfprelaxed -Mprefetch=distance:8 -Mprefetch=t0
 -Msmartalloc=huge -Msmartalloc=hugebss -Mipa=fast
 -Mipa=inline -tp shanghai-64 -Bstatic_pgi

437.leslie3d: -Mpfi=indirect(pass 1) -Mpfo=indirect(pass 2)
 -Mipa=fast(pass 2) -Mipa=inline(pass 2) -fastsse
 -Mvect=fuse -Msmartalloc=huge -Mprefetch=distance:8
 -Mprefetch=t0 -Mfprelaxed -tp shanghai-64 -Bstatic_pgi

459.GemsFDTD: -march=barcelona -Ofast -LNO:fission=2 -LNO:simd=2
 -LNO:prefetch_ahead=1 -CG:load_exe=0 -HP

465.tonto: -march=barcelona -Ofast -OPT:alias=no_f90_pointer_alias
 -LNO:blocking=off -CG:load_exe=1 -IPA:plimit=525 -HP

Benchmarks using both Fortran and C:

435.gromacs: -march=barcelona -Ofast -OPT:rsqrt=2 -HP:bdt=2m:heap=2m

Continued on next page



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Peak Optimization Flags (Continued)

436.cactusADM: -fastsse -Mconcur -Msmartalloc=huge -Mfprelaxed -Mipa=fast
-Mipa=inline -tp shanghai-64 -Bstatic_pgi

454.calculix: -Mpfi=indirect(pass 1) -Mpfo=indirect(pass 2)
-Mipa=fast(pass 2) -Mipa=inline(pass 2) -fastsse
-Mvect=short -Msmartalloc=huge -Mprefetch=t0 -Mpre
-Mfprelaxed -tp shanghai-64 -Bstatic_pgi

481.wrf: -fastsse -Mvect=noaltcode -Msmartalloc=huge
-Mprefetch=distance:8 -Mfprelaxed -tp shanghai-64
-Bstatic_pgi

Peak Other Flags

C benchmarks:

-Mipa=jobs:4(pass 2)

C++ benchmarks:

444.namd: -Mipa=jobs:4(pass 2)

Fortran benchmarks:

410.bwaves: -Mipa=jobs:4

434.zeusmp: -Mipa=jobs:4

437.leslie3d: -Mipa=jobs:4(pass 2)

Benchmarks using both Fortran and C:

436.cactusADM: -Mipa=jobs:4

454.calculix: -Mipa=jobs:4(pass 2)

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/amd-platform.20090710.html>

http://www.spec.org/cpu2006/flags/pgi80_linux_flags.20090914.html

<http://www.spec.org/cpu2006/flags/x86-open64-4.2.2-flags-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/amd-platform.20090710.xml>

http://www.spec.org/cpu2006/flags/pgi80_linux_flags.20090914.xml

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