



SPEC[®] CFP2006 Result

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Fujitsu

SPECfp[®]2006 = 38.0

CELSIUS R570, Intel Xeon X5550

SPECfp_base2006 = 35.7

CPU2006 license: 19

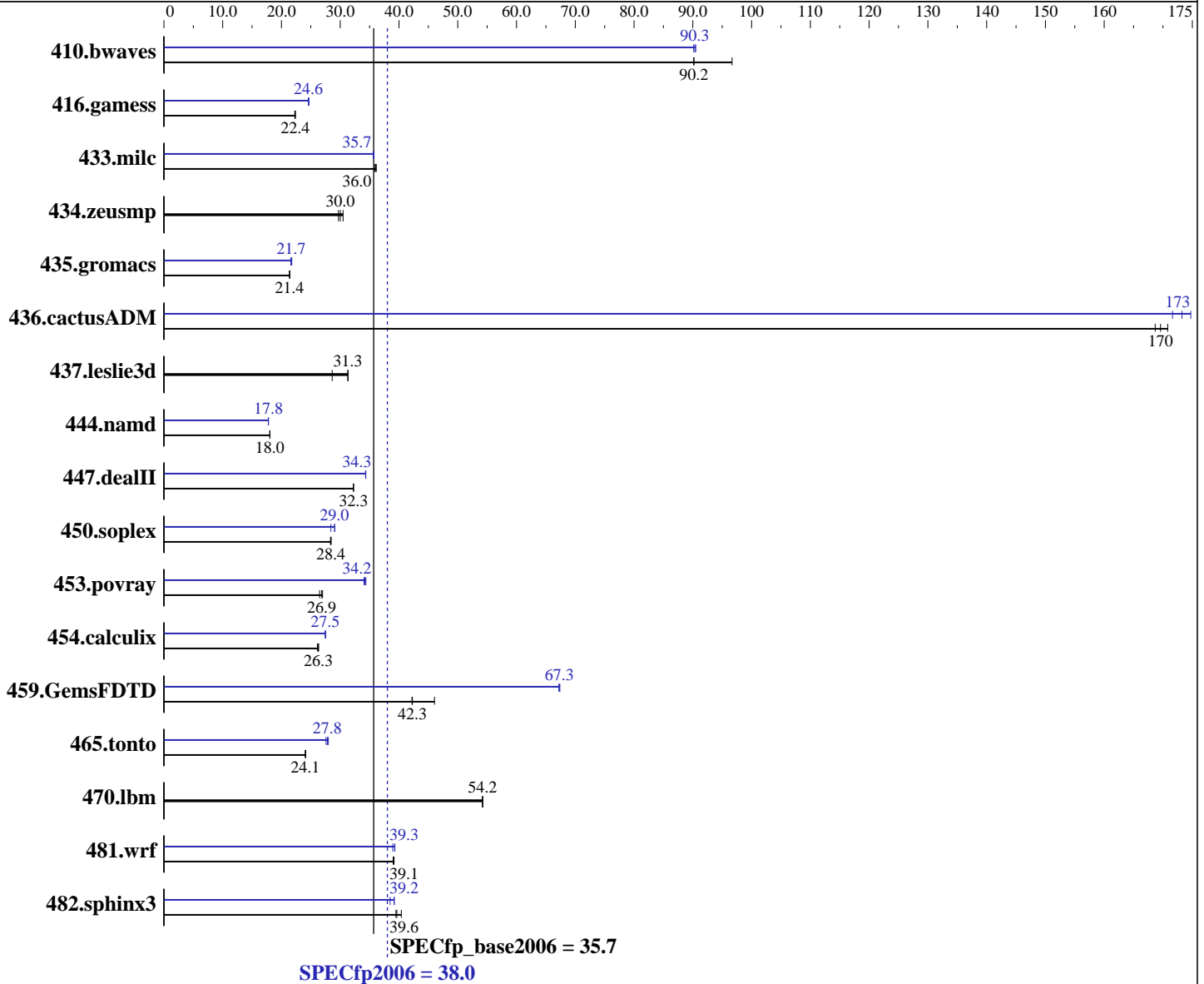
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Oct-2009

Hardware Availability: Apr-2009

Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon X5550
 CPU Characteristics: Intel Turbo Boost Technology up to 3.06 GHz
 CPU MHz: 2667
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SuSe Linux Enterprise Server 10 (x86_64) SP2, kernel 2.6.16.60-0.21-smp
 Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20090131 Package ID: I_cproc_p_11.0.080, I_cprof_p_11.0.080
 Auto Parallel: Yes
 File System: ext3
 System State: Multi-User Run Level 3
 Base Pointers: 64-bit

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L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 24 GB (6x4 GB PC3 10600R, 2 rank, ECC)
Disk Subsystem: 1 x SATA II, 400 GB, 7200 rpm
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	141	96.7	151	90.1	<u>151</u>	<u>90.2</u>	151	90.1	150	90.5	<u>150</u>	<u>90.3</u>
416.gamess	876	22.4	<u>876</u>	<u>22.4</u>	879	22.3	797	24.6	<u>796</u>	<u>24.6</u>	793	24.7
433.milc	254	36.1	<u>255</u>	<u>36.0</u>	256	35.9	<u>257</u>	<u>35.7</u>	258	35.6	257	35.7
434.zeusmp	306	29.7	299	30.5	<u>303</u>	<u>30.0</u>	306	29.7	299	30.5	<u>303</u>	<u>30.0</u>
435.gromacs	334	21.4	333	21.4	<u>334</u>	<u>21.4</u>	328	21.8	331	21.6	<u>329</u>	<u>21.7</u>
436.cactusADM	70.0	171	70.8	169	<u>70.5</u>	<u>170</u>	<u>69.0</u>	<u>173</u>	69.6	172	68.4	175
437.leslie3d	300	31.3	<u>301</u>	<u>31.3</u>	328	28.6	300	31.3	<u>301</u>	<u>31.3</u>	328	28.6
444.namd	<u>445</u>	<u>18.0</u>	445	18.0	445	18.0	451	17.8	451	17.8	<u>451</u>	<u>17.8</u>
447.dealII	<u>355</u>	<u>32.3</u>	355	32.2	355	32.3	333	34.3	<u>333</u>	<u>34.3</u>	333	34.3
450.soplex	293	28.4	<u>293</u>	<u>28.4</u>	294	28.3	<u>287</u>	<u>29.0</u>	293	28.4	287	29.1
453.povray	201	26.5	<u>198</u>	<u>26.9</u>	197	27.0	<u>156</u>	<u>34.2</u>	156	34.1	155	34.4
454.calculix	314	26.3	316	26.1	<u>314</u>	<u>26.3</u>	<u>300</u>	<u>27.5</u>	300	27.5	300	27.5
459.GemsFDTD	230	46.0	251	42.2	<u>251</u>	<u>42.3</u>	157	67.4	158	67.2	<u>158</u>	<u>67.3</u>
465.tonto	<u>409</u>	<u>24.1</u>	409	24.1	408	24.1	352	28.0	<u>354</u>	<u>27.8</u>	357	27.6
470.lbm	253	54.2	<u>253</u>	<u>54.2</u>	253	54.2	253	54.2	<u>253</u>	<u>54.2</u>	253	54.2
481.wrf	286	39.0	<u>285</u>	<u>39.1</u>	285	39.2	<u>284</u>	<u>39.3</u>	284	39.3	287	38.9
482.sphinx3	494	39.5	482	40.4	<u>492</u>	<u>39.6</u>	506	38.5	497	39.2	<u>498</u>	<u>39.2</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS configuration:
Hyper-Threading Technology set to Disabled.

General Notes

OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
KMP_STACKSIZE set to 200M

For information about Fujitsu please visit:

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General Notes (Continued)

<http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

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Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Peak Compiler Invocation

C benchmarks (except as noted below):

`icc`

`482.sphinx3: icc -m32`

C++ benchmarks (except as noted below):

`icpc`

`450.soplex: icpc -m32`

Fortran benchmarks:

`ifort`

Benchmarks using both Fortran and C:

`icc ifort`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.deallI: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
470.lbm: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

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Peak Optimization Flags (Continued)

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias -scalar-rep- -opt-prefetch

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
-parallel

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -auto

Benchmarks using both Fortran and C:

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Peak Optimization Flags (Continued)

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
-parallel -auto-ilp32

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.17.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.17.xml>

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