SPEC® CINT2006 Result

Supermicro
Motherboard X8SIE-F

$SPECint^®_rate2006 = $ 128

$SPECint_rate_base2006 = $ 120

CPU2006 license: 001176　　　　　　　　　　　　　　　　　　　　**Test date:** Jan-2010
Test sponsor: Supermicro　　　　　　　　　　　　　　　　　　**Hardware Availability:** Sep-2009
Tested by: Supermicro　　　　　　　　　　　　　　　　　　　**Software Availability:** Sep-2009

Benchmark	Copies	Base	Peak
400.perlbench	8	105	125
401.bzip2	8	73.8	78.8
403.gcc	8	103	102
429.mcf	4 / 8	129	138
445.gobmk	8	122	131
456.hmmer	4 / 8	165	183
458.sjeng	8	114	126
462.libquantum	8	310	
464.h264ref	8	157	162
471.omnetpp	8	84.5	88.7
473.astar	8	71.7	79.0
483.xalancbmk	8	131	

SPECint_rate_base2006 = 120
SPECint_rate2006 = 128

Hardware

CPU Name:	Intel Xeon X3470
CPU Characteristics:	Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz:	2933
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	16 GB (4 x 4GB DDR3-1333 RDIMM, ECC, CL9)
Disk Subsystem:	1 x 160 GB SATA II, 7200 RPM
Other Hardware:	None

Software

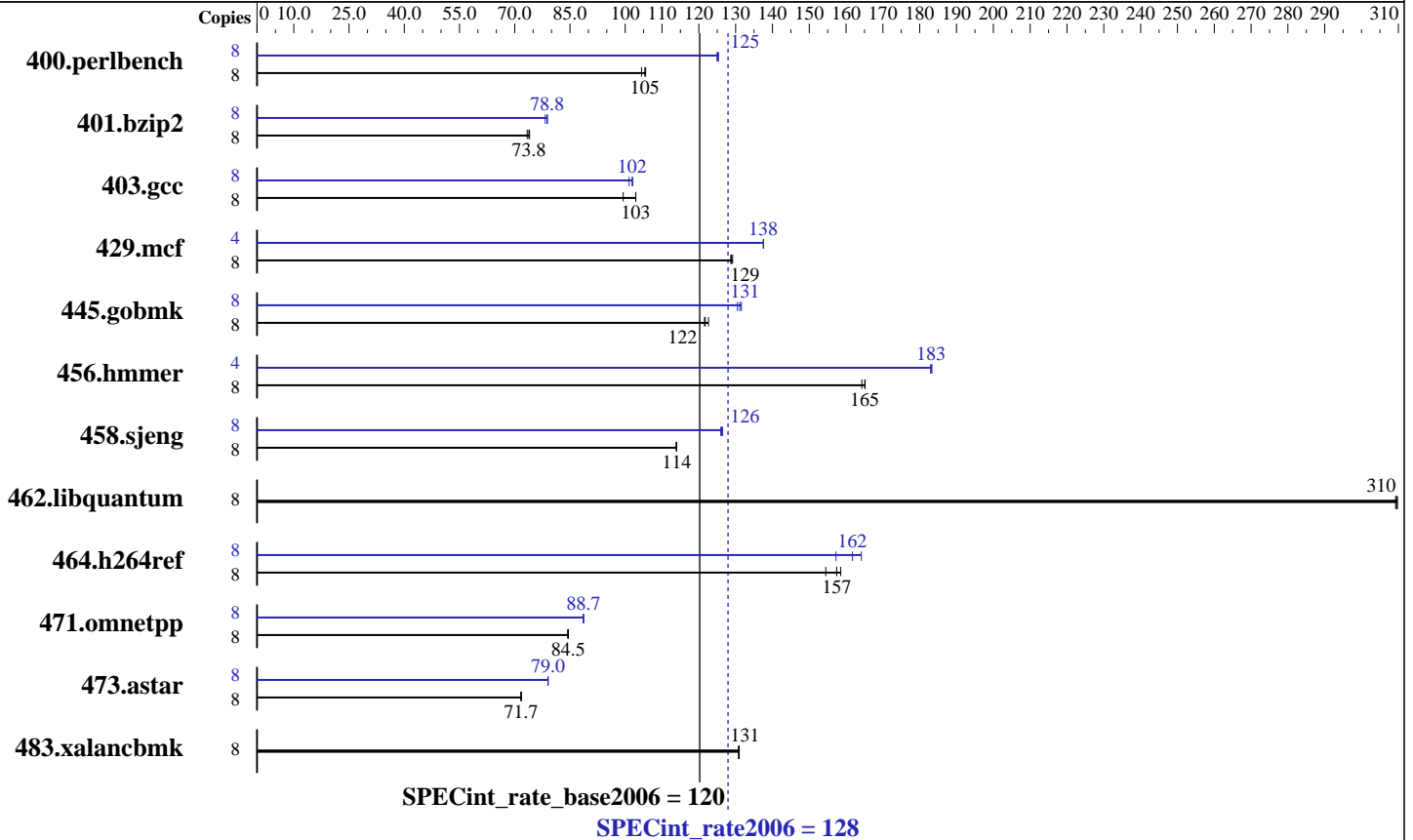|	
Operating System:	SUSE Linux Enterprise Server 11 (x86_64) Kernel 2.6.27.19-5-default
Compiler:	Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091012 Package ID: l_cproc_p_11.1.059
Auto Parallel:	No
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base Copies	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Peak Copies	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio
400.perlbench	8	748	104	**742**	**105**	740	106	8	626	125	623	125	**625**	**125**
401.bzip2	8	**1045**	**73.8**	1052	73.4	1043	74.0	8	**980**	**78.8**	979	78.9	986	78.3
403.gcc	8	647	99.5	626	103	**626**	**103**	8	637	101	631	102	**632**	**102**
429.mcf	8	565	129	**566**	**129**	567	129	4	265	138	265	138	**265**	**138**
445.gobmk	8	691	122	**689**	**122**	684	123	8	**640**	**131**	638	132	643	130
456.hmmer	8	454	164	**452**	**165**	452	165	4	**204**	**183**	204	183	204	183
458.sjeng	8	850	114	**849**	**114**	849	114	8	768	126	765	126	**767**	**126**
462.libquantum	8	535	310	**536**	**310**	536	309	8	535	310	**536**	**310**	536	309
464.h264ref	8	1117	159	**1124**	**157**	1146	154	8	1079	164	1126	157	**1095**	**162**
471.omnetpp	8	**592**	**84.5**	591	84.6	593	84.4	8	**564**	**88.7**	564	88.6	564	88.7
473.astar	8	784	71.7	**783**	**71.7**	782	71.8	8	711	79.0	**710**	**79.0**	710	79.1
483.xalancbmk	8	422	131	421	131	**422**	**131**	8	422	131	421	131	**422**	**131**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Platform Notes

```
Default BIOS Settings
```

Base Compiler Invocation

C benchmarks:
```
icc  -m32
```

C++ benchmarks:
```
icpc -m32
```

Base Portability Flags

```
    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX
```

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Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap
```

Base Other Flags

C benchmarks:

    403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc  -m32
```

    401.bzip2: `icc -m64`

    456.hmmer: `icc -m64`

    458.sjeng: `icc -m64`

C++ benchmarks (except as noted below):
```
icpc -m32
```

    473.astar: `icpc -m64`

Peak Portability Flags

    400.perlbench: `-DSPEC_CPU_LINUX_IA32`
    401.bzip2: `-DSPEC_CPU_LP64`
    456.hmmer: `-DSPEC_CPU_LP64`
    458.sjeng: `-DSPEC_CPU_LP64`
    462.libquantum: `-DSPEC_CPU_LINUX`
    473.astar: `-DSPEC_CPU_LP64`
    483.xalancbmk: `-DSPEC_CPU_LINUX`

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Peak Optimization Flags

C benchmarks:

    400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
                `-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
                `-prof-use(pass 2) -ansi-alias`

       401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
                `-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
                `-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32`

         403.gcc: `-xSSE4.2 -ipo -O3 -no-prec-div -static`

         429.mcf: `-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch`

       445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2`
                `-ipo -no-prec-div -ansi-alias`

       456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2`
                `-ansi-alias -auto-ilp32`

       458.sjeng: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
                `-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
                `-prof-use(pass 2) -unroll4 -auto-ilp32`

    462.libquantum: `basepeak = yes`

      464.h264ref: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
                `-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
                `-prof-use(pass 2) -unroll2 -ansi-alias`

C++ benchmarks:

      471.omnetpp: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
                `-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
                `-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs`
                `-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap`

        473.astar: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
                `-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
                `-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs`
                `-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64`

    483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

Continued on next page

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Peak Other Flags (Continued)

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revE.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revE.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
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