



SPEC® CINT2006 Result

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Acer Incorporated

SPECint®_rate2006 = 123

Acer AT310 F1 (Intel Xeon X3470)

SPECint_rate_base2006 = 116

CPU2006 license: 97

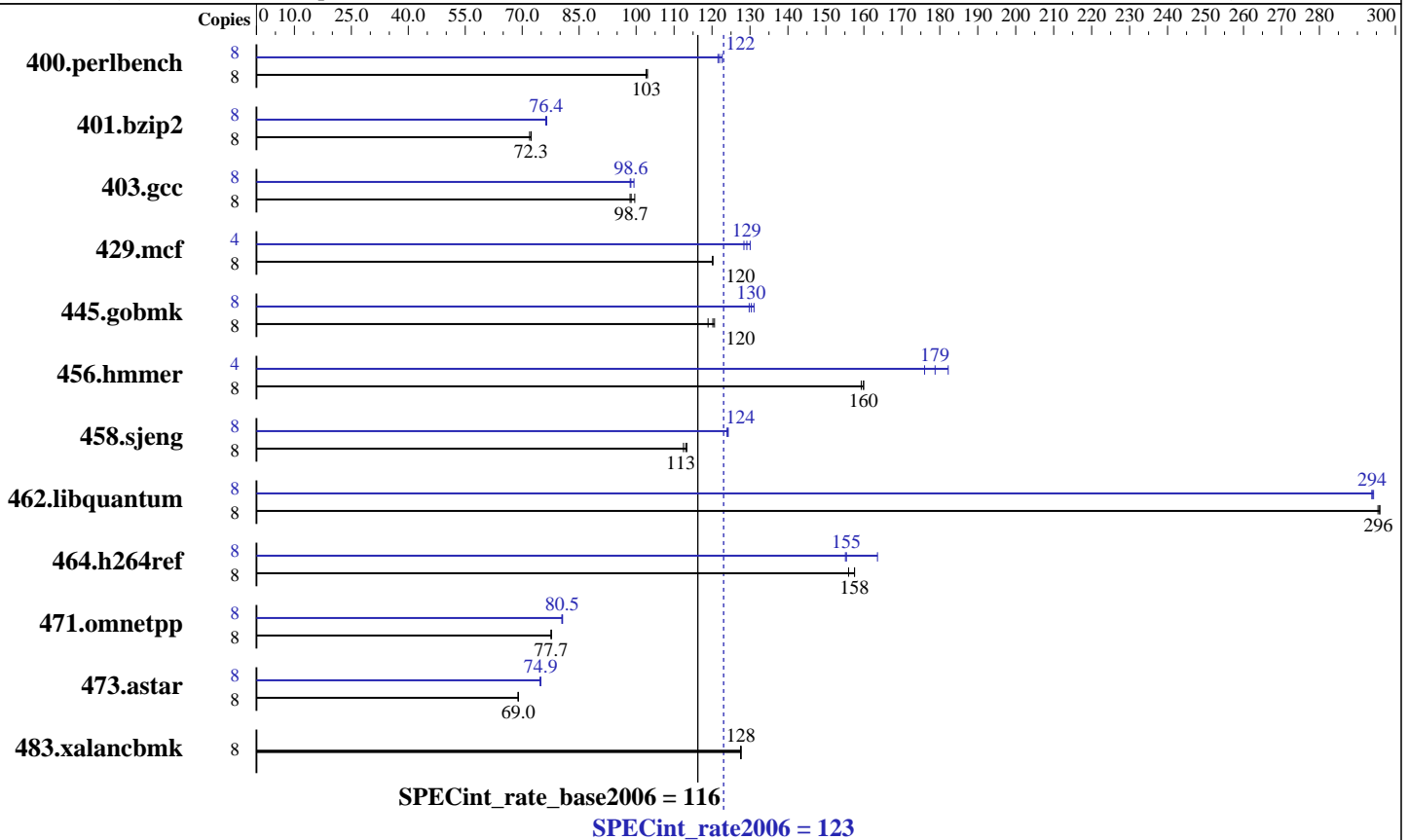
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Mar-2010

Hardware Availability: Mar-2010

Software Availability: Jan-2010



Hardware

CPU Name: Intel Xeon X3470
 CPU Characteristics: Intel Turbo Boost Technology up to 3.6 GHz
 CPU MHz: 2933
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 16 GB (4 x 4096 MB ECC Unbuffered DDR3-1333)
 Disk Subsystem: 1 x 1000 GB 7200RPM SATA HDD
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Sserver 11 (x86_64)
 Kernel 2.6.27.19-5
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1
 Build 20091130 Package ID: 1_cproc_p_11.1.064
 Auto Parallel: No
 File System: ReiserFS
 System State: Multi-user, level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1
 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	759	103	761	103	<u>761</u>	<u>103</u>	8	637	123	643	122	<u>641</u>	<u>122</u>
401.bzip2	8	1067	72.4	<u>1067</u>	<u>72.3</u>	1073	72.0	8	1010	76.5	<u>1011</u>	<u>76.4</u>	1013	76.2
403.gcc	8	<u>652</u>	<u>98.7</u>	654	98.4	646	99.6	8	654	98.4	648	99.4	<u>653</u>	<u>98.6</u>
429.mcf	8	607	120	607	120	<u>607</u>	<u>120</u>	4	284	128	280	130	<u>282</u>	<u>129</u>
445.gobmk	8	705	119	<u>698</u>	<u>120</u>	696	121	8	647	130	640	131	<u>644</u>	<u>130</u>
456.hammer	8	469	159	467	160	<u>467</u>	<u>160</u>	4	<u>209</u>	<u>179</u>	205	182	212	176
458.sjeng	8	861	112	<u>856</u>	<u>113</u>	854	113	8	781	124	779	124	<u>780</u>	<u>124</u>
462.libquantum	8	<u>560</u>	<u>296</u>	560	296	561	295	8	563	294	<u>564</u>	<u>294</u>	564	294
464.h264ref	8	<u>1124</u>	<u>158</u>	1124	158	1135	156	8	1082	164	1141	155	<u>1139</u>	<u>155</u>
471.omnetpp	8	644	77.6	644	77.7	<u>644</u>	<u>77.7</u>	8	621	80.5	620	80.6	<u>621</u>	<u>80.5</u>
473.astar	8	814	69.0	<u>814</u>	<u>69.0</u>	815	68.9	8	750	74.9	752	74.7	<u>750</u>	<u>74.9</u>
483.xalancbmk	8	432	128	<u>433</u>	<u>128</u>	433	128	8	432	128	<u>433</u>	<u>128</u>	433	128

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was set for stacksize unlimited

General Notes

This result was measured on the Gateway GT310 F1.
These 4 systems are electronically equivalent:
Acer AR320 F1, Acer AT310 F1, Gateway GR320 F1, and Gateway GT310 F1.

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.icl1.1/libic1.1-32bit -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

462.libquantum: icc -m64

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

456.hmmcr: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
 -ipo -no-prec-div -ansi-alias

456.hmmcr: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
 -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs

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Peak Optimization Flags (Continued)

473.astar (continued):

`-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64`

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

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