



# SPEC® CINT2006 Result

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## IBM Corporation

**SPECint®2006 = 38.2**

IBM System x iDataPlex dx360 M3 (Intel Xeon X5660)

**SPECint\_base2006 = 35.4**

CPU2006 license: 11

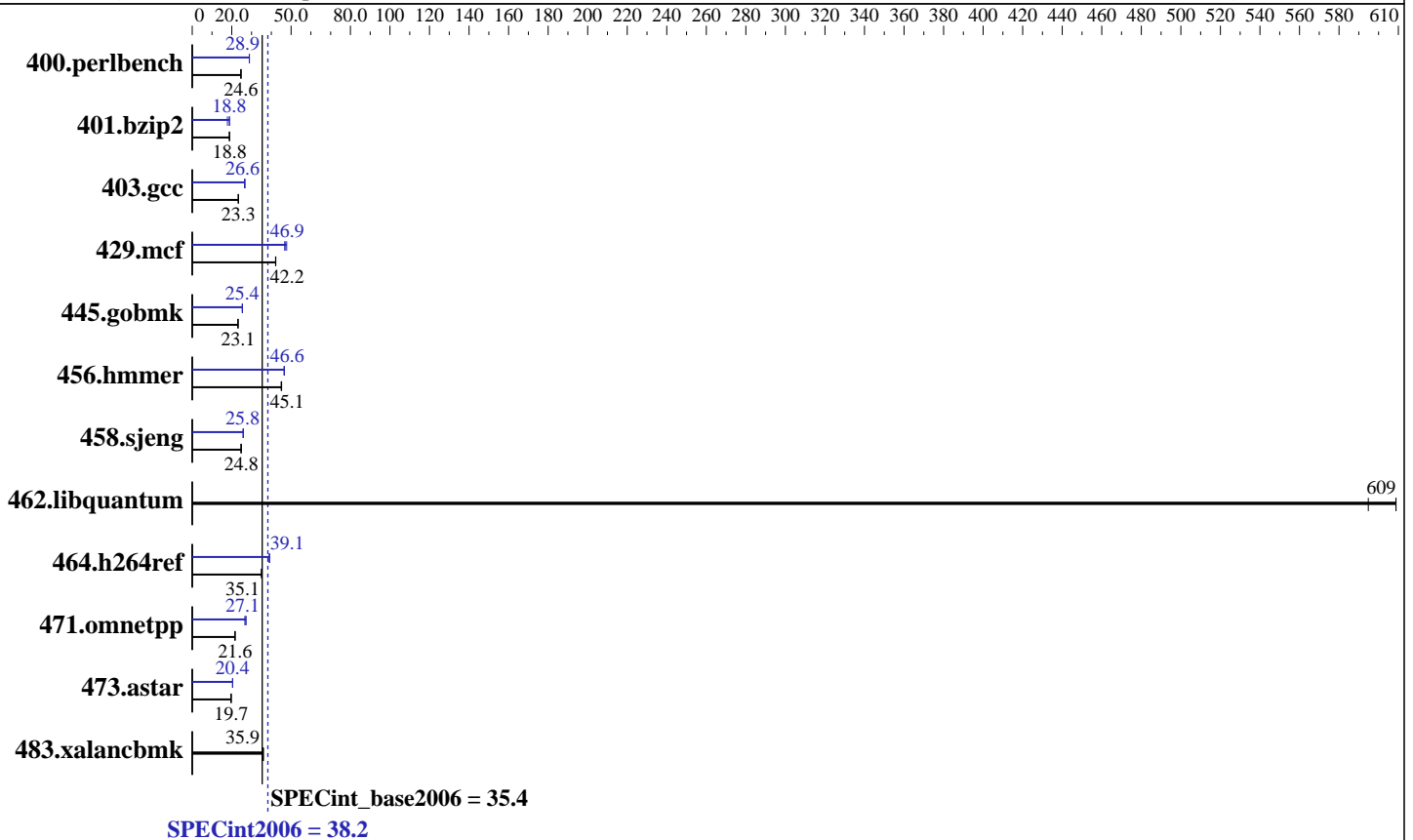
Test date: Apr-2010

Test sponsor: IBM Corporation

Hardware Availability: Jun-2010

Tested by: IBM Corporation

Software Availability: Jan-2010



### Hardware

CPU Name: Intel Xeon X5660  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz  
 CPU MHz: 2800  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (12 x 4 GB PC3-10600R-ECC, CL9)  
 Disk Subsystem: 1 x 73 GB SAS, 15000RPM  
 Other Hardware: None

### Software

Operating System: SuSe Linux Enterprise Server 11 (x86\_64)  
 Kernel 2.6.27.19-5-default  
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1  
 Build 20091130 Package ID: l\_cproc\_p\_11.1.064  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



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### Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	397	24.6	396	24.7	<b><u>397</u></b>	<b><u>24.6</u></b>	338	28.9	<b><u>338</u></b>	<b><u>28.9</u></b>	339	28.8
401.bzip2	512	18.8	<b><u>512</u></b>	<b><u>18.8</u></b>	513	18.8	512	18.8	542	17.8	<b><u>513</u></b>	<b><u>18.8</u></b>
403.gcc	345	23.4	<b><u>345</u></b>	<b><u>23.3</u></b>	346	23.3	304	26.5	302	26.7	<b><u>303</u></b>	<b><u>26.6</u></b>
429.mcf	216	42.3	216	42.2	<b><u>216</u></b>	<b><u>42.2</u></b>	195	46.8	191	47.8	<b><u>194</u></b>	<b><u>46.9</u></b>
445.gobmk	<b><u>453</u></b>	<b><u>23.1</u></b>	453	23.1	453	23.2	414	25.3	414	25.4	<b><u>414</u></b>	<b><u>25.4</u></b>
456.hammer	206	45.2	<b><u>207</u></b>	<b><u>45.1</u></b>	207	45.0	201	46.5	<b><u>200</u></b>	<b><u>46.6</u></b>	200	46.6
458.sjeng	490	24.7	<b><u>489</u></b>	<b><u>24.8</u></b>	488	24.8	469	25.8	<b><u>469</u></b>	<b><u>25.8</u></b>	469	25.8
462.libquantum	34.8	595	<b><u>34.0</u></b>	<b><u>609</u></b>	34.0	609	34.8	595	<b><u>34.0</u></b>	<b><u>609</u></b>	34.0	609
464.h264ref	<b><u>631</u></b>	<b><u>35.1</u></b>	632	35.0	629	35.2	567	39.0	567	39.1	<b><u>567</u></b>	<b><u>39.1</u></b>
471.omnetpp	289	21.7	289	21.6	<b><u>289</u></b>	<b><u>21.6</u></b>	<b><u>230</u></b>	<b><u>27.1</u></b>	230	27.2	235	26.6
473.astar	357	19.7	<b><u>357</u></b>	<b><u>19.7</u></b>	357	19.7	344	20.4	<b><u>344</u></b>	<b><u>20.4</u></b>	343	20.5
483.xalancbmk	193	35.8	<b><u>192</u></b>	<b><u>35.9</u></b>	192	35.9	193	35.8	<b><u>192</u></b>	<b><u>35.9</u></b>	192	35.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Platform Notes

Turbo Mode Enable  
Turbo Boost set to Traditional  
CPU C State Enable  
Data Reuse Disable

### General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502  
'ulimit -s unlimited' was used to set the stack size to unlimited prior to run  
OMP\_NUM\_THREADS set to number of cores  
KMP\_AFFINITY set to granularity=fine,scatter

### Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64



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## Base Portability Flags

```

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

```

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32

429.mcf: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

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## Peak Compiler Invocation (Continued)

471.omnetpp: icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
 401.bzip2: -DSPEC\_CPU\_LP64  
 403.gcc: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
 473.astar: -DSPEC\_CPU\_LP64  
 483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div -static(pass 2) -prof-use(pass 2)  
 -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
 -opt-malloc-options=3 -auto-ilp32

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -unroll2 -ansi-alias

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## Peak Optimization Flags (Continued)

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100601.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100601.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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