SPEC® CINT2006 Result
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Fujitsu

PRIMERGY RX600 S5, Intel Xeon E7540, 2.0 GHz

SPECint®_rate2006 =   548

SPECint_rate_base2006 = 508

CPU2006 license: 19	**Test date:**	Jun-2010
Test sponsor: Fujitsu	**Hardware Availability:**	Jun-2010
Tested by: Fujitsu	**Software Availability:**	Jan-2010

Copies

Benchmark	Copies	Peak	Base
400.perlbench	48	495	418
401.bzip2	48	334	316
403.gcc	48		419
429.mcf	48	594	594
445.gobmk	48	534	490
456.hmmer	48	877	680
458.sjeng	48	486	435
462.libquantum	48		1590
464.h264ref	48	655	649
471.omnetpp	48	382	344
473.astar	48	360	322
483.xalancbmk	48		548

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Hardware

CPU Name:	Intel Xeon E7540
CPU Characteristics:	Intel Turbo Boost Technology up to 2.26 GHz
CPU MHz:	2000
FPU:	Integrated
CPU(s) enabled:	24 cores, 4 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable:	2,3,4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	18 MB I+D on chip per chip
Other Cache:	None
Memory:	256 GB (64x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC, see add'l detail in notes)
Disk Subsystem:	1 x SAS, 300 GB, 10000 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
Compiler:	Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064
Auto Parallel:	No
File System:	ext3
System State:	Multi-User Run Level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1

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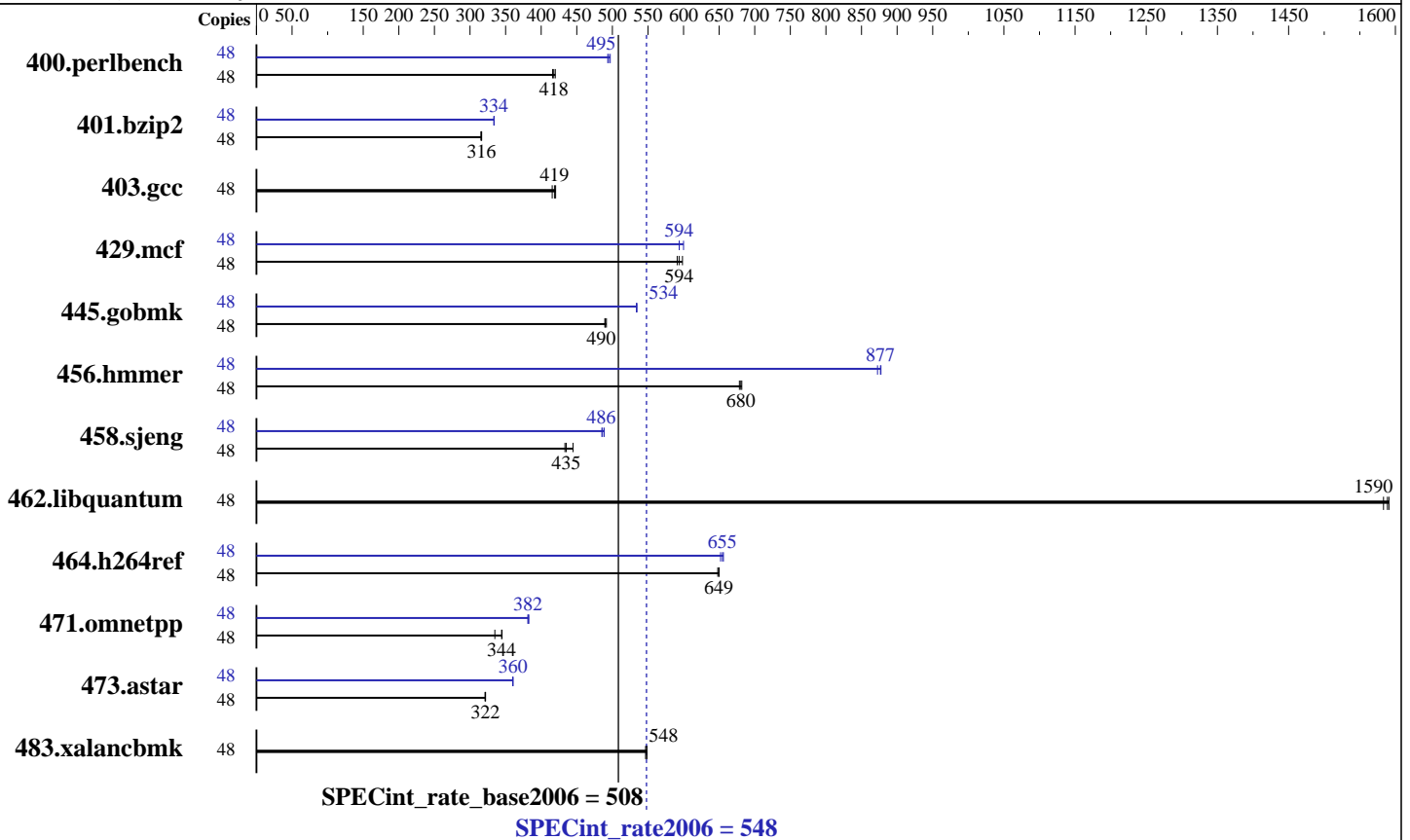$SPECint\_rate\_base2006 =$ 508

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Results Table

Benchmark	Base Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Peak Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	48	**1123**	**418**	1127	416	1117	420	48	**948**	**495**	944	497	950	494
401.bzip2	48	1469	315	**1465**	**316**	1464	316	48	**1388**	**334**	1389	334	1387	334
403.gcc	48	919	420	930	415	**923**	**419**	48	919	420	930	415	**923**	**419**
429.mcf	48	731	598	**737**	**594**	741	591	48	**737**	**594**	730	600	737	594
445.gobmk	48	1024	492	**1027**	**490**	1029	489	48	**943**	**534**	942	535	943	534
456.hmmer	48	657	682	660	678	**658**	**680**	48	513	872	**511**	**877**	510	877
458.sjeng	48	1340	433	**1335**	**435**	1306	445	48	1197	485	**1194**	**486**	1189	489
462.libquantum	48	625	1590	**626**	**1590**	628	1580	48	625	1590	**626**	**1590**	628	1580
464.h264ref	48	1634	650	**1636**	**649**	1639	648	48	1619	656	1629	652	**1623**	**655**
471.omnetpp	48	870	345	896	335	**871**	**344**	48	787	381	**786**	**382**	784	383
473.astar	48	**1047**	**322**	1050	321	1047	322	48	935	360	936	360	**935**	**360**
483.xalancbmk	48	**604**	**548**	604	548	606	547	48	**604**	**548**	604	548	606	547

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

        The config file option 'submit' was used.
         numactl was used to bind copies to the cores

Operating System Notes

        'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

        The system automatically configures the memory to run at 1066 MHz.

General Notes

        For information about Fujitsu please visit: http://www.fujitsu.com
        Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

Base Compiler Invocation

C benchmarks:
        icc -m32

C++ benchmarks:
        icpc -m32

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Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap
```

Base Other Flags

C benchmarks:
```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc  -m32
```

```
401.bzip2: icc -m64
```

```
456.hmmer: icc -m64
```

```
458.sjeng: icc -m64
```

C++ benchmarks (except as noted below):
```
icpc -m32
```

```
473.astar: icpc -m64
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
```

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Peak Portability Flags (Continued)

```
  462.libquantum: -DSPEC_CPU_LINUX
        473.astar: -DSPEC_CPU_LP64
  483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
  400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                 -prof-use(pass 2) -ansi-alias

      401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                 -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

        403.gcc: basepeak = yes

        429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

      445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
                 -ipo -no-prec-div -ansi-alias

      456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
                 -ansi-alias -auto-ilp32

      458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                 -prof-use(pass 2) -unroll4 -auto-ilp32

  462.libquantum: basepeak = yes

     464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                 -prof-use(pass 2) -unroll2 -ansi-alias
```

C++ benchmarks:

```
    471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
                 -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

      473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
                 -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64
```

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Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.02.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.02.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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