



# SPEC® CFP2006 Result

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## NEC Corporation

Express5800/R120b-2  
(Intel Xeon E5506)

SPECfp®\_rate2006 = 62.3

SPECfp\_rate\_base2006 = 60.5

CPU2006 license: 9006

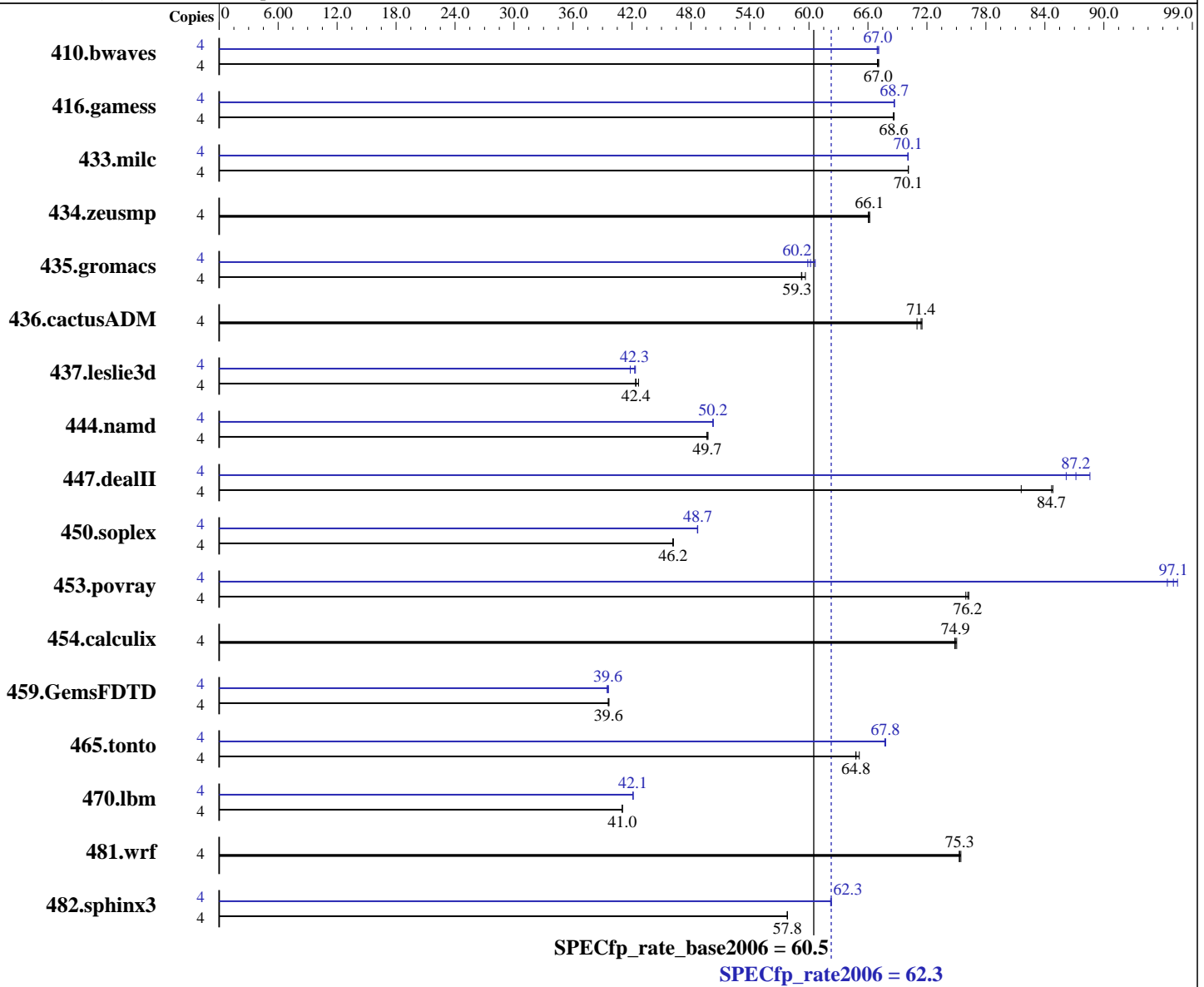
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Jul-2010

Hardware Availability: Sep-2010

Software Availability: Dec-2009



### Hardware

CPU Name: Intel Xeon E5506  
 CPU Characteristics:  
 CPU MHz: 2133  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64),  
Kernel 2.6.27.19-5-default  
 Compiler: Intel C++ and Fortran Professional Compiler for  
IA32 and Intel 64, Version 11.1  
Build 20091130 Package ID: I\_cproc\_p\_11.1.064,  
L\_cprof\_p\_11.1.064  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)

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L3 Cache: 4 MB I+D on chip per chip  
Other Cache: None  
Memory: 48 GB (6 x 8 GB PC3L-10600R, 2 rank, CL9, ECC, running at 800 MHz)  
Disk Subsystem: 1x160 GB SATA, 7200 RPM  
Other Hardware: None

Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	812	67.0	810	67.1	<b>811</b>	<b>67.0</b>	4	<b>811</b>	<b>67.0</b>	810	67.1	812	66.9
416.gamess	4	<b>1142</b>	<b>68.6</b>	1141	68.7	1142	68.6	4	<b>1140</b>	<b>68.7</b>	1141	68.7	1140	68.7
433.milc	4	524	70.1	<b>524</b>	<b>70.1</b>	524	70.1	4	<b>524</b>	<b>70.1</b>	524	70.0	524	70.1
434.zeusmp	4	550	66.2	<b>550</b>	<b>66.1</b>	551	66.0	4	550	66.2	<b>550</b>	<b>66.1</b>	551	66.0
435.gromacs	4	482	59.2	<b>482</b>	<b>59.3</b>	479	59.6	4	471	60.6	477	59.9	<b>475</b>	<b>60.2</b>
436.cactusADM	4	669	71.5	673	71.0	<b>669</b>	<b>71.4</b>	4	669	71.5	673	71.0	<b>669</b>	<b>71.4</b>
437.leslie3d	4	<b>887</b>	<b>42.4</b>	888	42.4	881	42.7	4	899	41.8	<b>890</b>	<b>42.3</b>	888	42.3
444.namd	4	<b>646</b>	<b>49.7</b>	647	49.6	645	49.7	4	639	50.2	638	50.3	<b>639</b>	<b>50.2</b>
447.dealII	4	<b>540</b>	<b>84.7</b>	540	84.8	561	81.6	4	517	88.6	531	86.2	<b>525</b>	<b>87.2</b>
450.soplex	4	<b>723</b>	<b>46.2</b>	722	46.2	723	46.2	4	<b>685</b>	<b>48.7</b>	685	48.7	686	48.7
453.povray	4	<b>279</b>	<b>76.2</b>	280	76.0	279	76.3	4	<b>219</b>	<b>97.1</b>	221	96.5	218	97.5
454.calculix	4	440	75.0	441	74.8	<b>441</b>	<b>74.9</b>	4	440	75.0	441	74.8	<b>441</b>	<b>74.9</b>
459.GemsFDTD	4	1072	39.6	1070	39.7	<b>1072</b>	<b>39.6</b>	4	<b>1073</b>	<b>39.6</b>	1072	39.6	1075	39.5
465.tonto	4	608	64.8	604	65.1	<b>608</b>	<b>64.8</b>	4	<b>581</b>	<b>67.8</b>	581	67.7	580	67.8
470.ibm	4	1340	41.0	<b>1340</b>	<b>41.0</b>	1340	41.0	4	1306	42.1	<b>1305</b>	<b>42.1</b>	1305	42.1
481.wrf	4	594	75.3	592	75.5	<b>593</b>	<b>75.3</b>	4	594	75.3	592	75.5	<b>593</b>	<b>75.3</b>
482.sphinx3	4	<b>1349</b>	<b>57.8</b>	1348	57.8	1349	57.8	4	<b>1252</b>	<b>62.3</b>	1252	62.3	1253	62.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

Default BIOS settings were used.



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## General Notes

The Express5800/R120b-1 and the Express5800/R120b-2 models are electronically equivalent. The results have been measured on the Express5800/R120b-2 model.

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

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## Base Optimization Flags (Continued)

Fortran benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static`

Benchmarks using both Fortran and C:

`-xSSE4.2 -ipo -O3 -no-prec-div -static`

## Peak Compiler Invocation

C benchmarks (except as noted below):

`icc -m64`

482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):

`icpc -m64`

450.soplex: `icpc -m32`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

## Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`  
 416.gamess: `-DSPEC_CPU_LP64`  
 433.milc: `-DSPEC_CPU_LP64`  
 434.zeusmp: `-DSPEC_CPU_LP64`  
 435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`  
 436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`  
 437.leslie3d: `-DSPEC_CPU_LP64`  
 444.namd: `-DSPEC_CPU_LP64`  
 447.dealII: `-DSPEC_CPU_LP64`  
 453.povray: `-DSPEC_CPU_LP64`  
 454.calculix: `-DSPEC_CPU_LP64 -nofor_main`  
 459.GemsFDTD: `-DSPEC_CPU_LP64`  
 465.tonto: `-DSPEC_CPU_LP64`  
 470.lbm: `-DSPEC_CPU_LP64`  
 481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`



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## Peak Optimization Flags

### C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-fno-alias -opt-prefetch

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-malloc-options=3 -ansi-alias -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

### C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-fno-alias -auto-ilp32

447.dealIII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias -scalar-rep-

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -Ob0

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll4 -auto -inline-calloc -opt-malloc-options=3

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## Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100721.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100721.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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