



SPEC[®] CFP2006 Result

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Fujitsu

SPECfp[®]_rate2006 = 54.5

PRIMERGY TX100 S2, Intel Core i3-560, 3.33 GHz

SPECfp_rate_base2006 = 52.6

CPU2006 license: 19

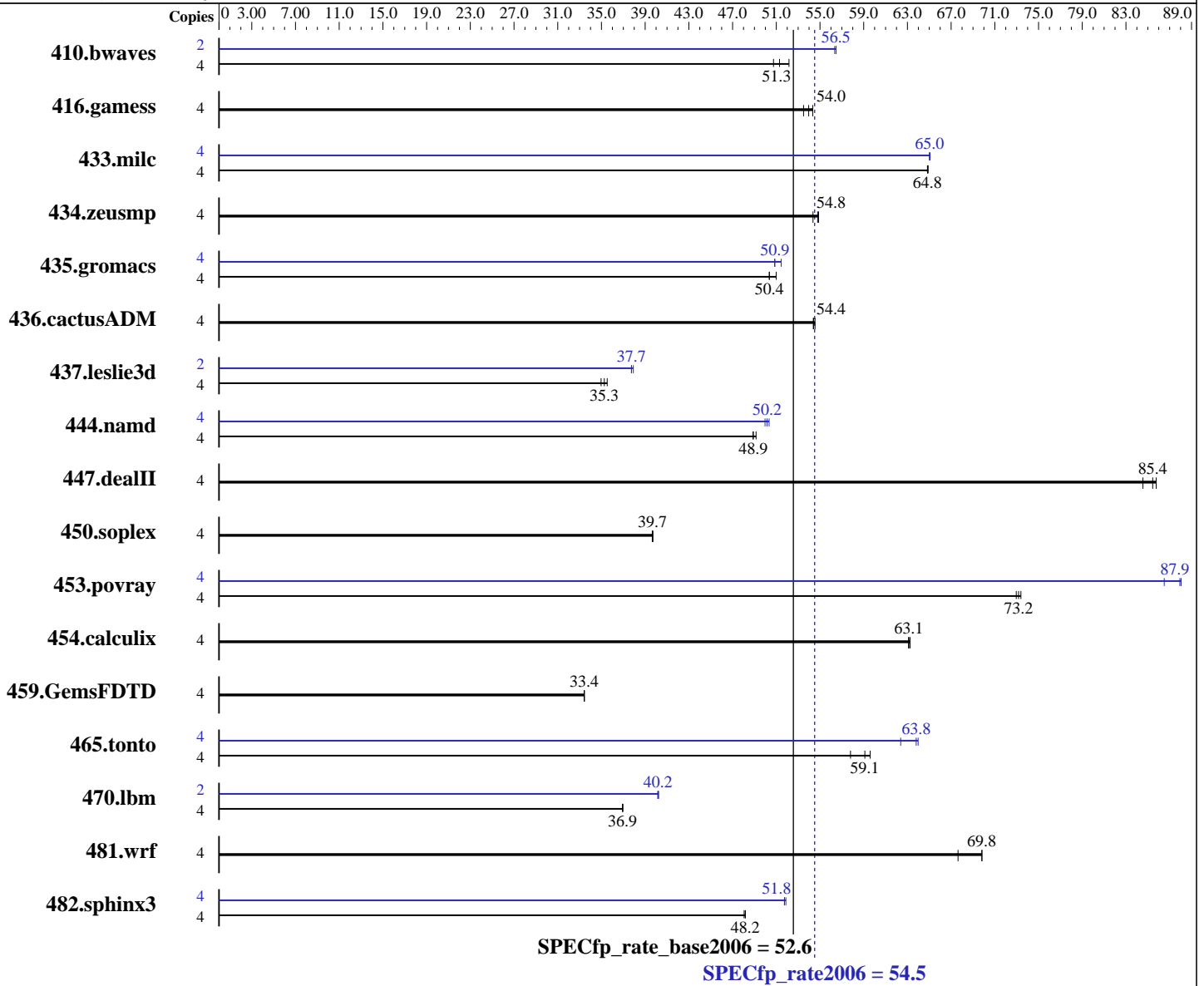
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Aug-2010

Hardware Availability: Aug-2010

Software Availability: Jan-2010



Hardware

CPU Name: Intel Core i3-560
 CPU Characteristics: 3333
 CPU MHz: 3333
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) with SP1, Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)

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L3 Cache: 4 MB I+D on chip per chip
Other Cache: None
Memory: 16 GB (4x4 GB PC3-10600E, 2 rank, CL9-9-9, ECC)
Disk Subsystem: 1 x SAS, 300 GB, 10000 RPM
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---------------|--------|-------------|-------------|-------------|-------------|-------------|-------------|--------|------------|-------------|------------|-------------|-------------|-------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 4 | 1060 | 51.3 | 1042 | 52.2 | 1071 | 50.7 | 2 | 481 | 56.5 | 481 | 56.5 | 482 | 56.3 |
| 416.gamess | 4 | 1464 | 53.5 | 1441 | 54.3 | 1451 | 54.0 | 4 | 1464 | 53.5 | 1441 | 54.3 | 1451 | 54.0 |
| 433.milc | 4 | 566 | 64.8 | 566 | 64.8 | 566 | 64.9 | 4 | 565 | 65.0 | 564 | 65.1 | 565 | 65.0 |
| 434.zeusmp | 4 | 670 | 54.4 | 664 | 54.8 | 663 | 54.9 | 4 | 670 | 54.4 | 664 | 54.8 | 663 | 54.9 |
| 435.gromacs | 4 | 567 | 50.4 | 567 | 50.4 | 560 | 51.0 | 4 | 561 | 50.9 | 555 | 51.5 | 562 | 50.9 |
| 436.cactusADM | 4 | 876 | 54.5 | 879 | 54.4 | 878 | 54.4 | 4 | 876 | 54.5 | 879 | 54.4 | 878 | 54.4 |
| 437.leslie3d | 4 | 1058 | 35.5 | 1075 | 35.0 | 1066 | 35.3 | 2 | 498 | 37.7 | 498 | 37.7 | 496 | 37.9 |
| 444.namd | 4 | 656 | 48.9 | 653 | 49.1 | 656 | 48.9 | 4 | 637 | 50.3 | 642 | 50.0 | 640 | 50.2 |
| 447.dealII | 4 | 541 | 84.6 | 536 | 85.4 | 534 | 85.8 | 4 | 541 | 84.6 | 536 | 85.4 | 534 | 85.8 |
| 450.soplex | 4 | 840 | 39.7 | 841 | 39.7 | 841 | 39.6 | 4 | 840 | 39.7 | 841 | 39.7 | 841 | 39.6 |
| 453.povray | 4 | 291 | 73.2 | 290 | 73.4 | 292 | 73.0 | 4 | 242 | 88.0 | 246 | 86.5 | 242 | 87.9 |
| 454.calculix | 4 | 522 | 63.2 | 523 | 63.1 | 523 | 63.1 | 4 | 522 | 63.2 | 523 | 63.1 | 523 | 63.1 |
| 459.GemsFDTD | 4 | 1270 | 33.4 | 1269 | 33.4 | 1270 | 33.4 | 4 | 1270 | 33.4 | 1269 | 33.4 | 1270 | 33.4 |
| 465.tonto | 4 | 666 | 59.1 | 660 | 59.6 | 681 | 57.8 | 4 | 617 | 63.8 | 631 | 62.4 | 615 | 64.0 |
| 470.lbm | 4 | 1487 | 37.0 | 1488 | 36.9 | 1488 | 36.9 | 2 | 683 | 40.2 | 684 | 40.2 | 684 | 40.2 |
| 481.wrf | 4 | 640 | 69.8 | 661 | 67.6 | 640 | 69.8 | 4 | 640 | 69.8 | 661 | 67.6 | 640 | 69.8 |
| 482.sphinx3 | 4 | 1618 | 48.2 | 1619 | 48.2 | 1622 | 48.1 | 4 | 1507 | 51.7 | 1503 | 51.9 | 1506 | 51.8 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502



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Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.lelie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:
-xSSE4.2 -ipo -O3 -no-prec-div -static



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Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deallI: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -opt-prefetch

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -ansi-alias -auto-ilp32

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Peak Optimization Flags (Continued)

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.deallI: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static

459.GemsFDTD: basepeak = yes

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.xml>



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