SPEC® CFP2006 Result

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Acer Incorporated

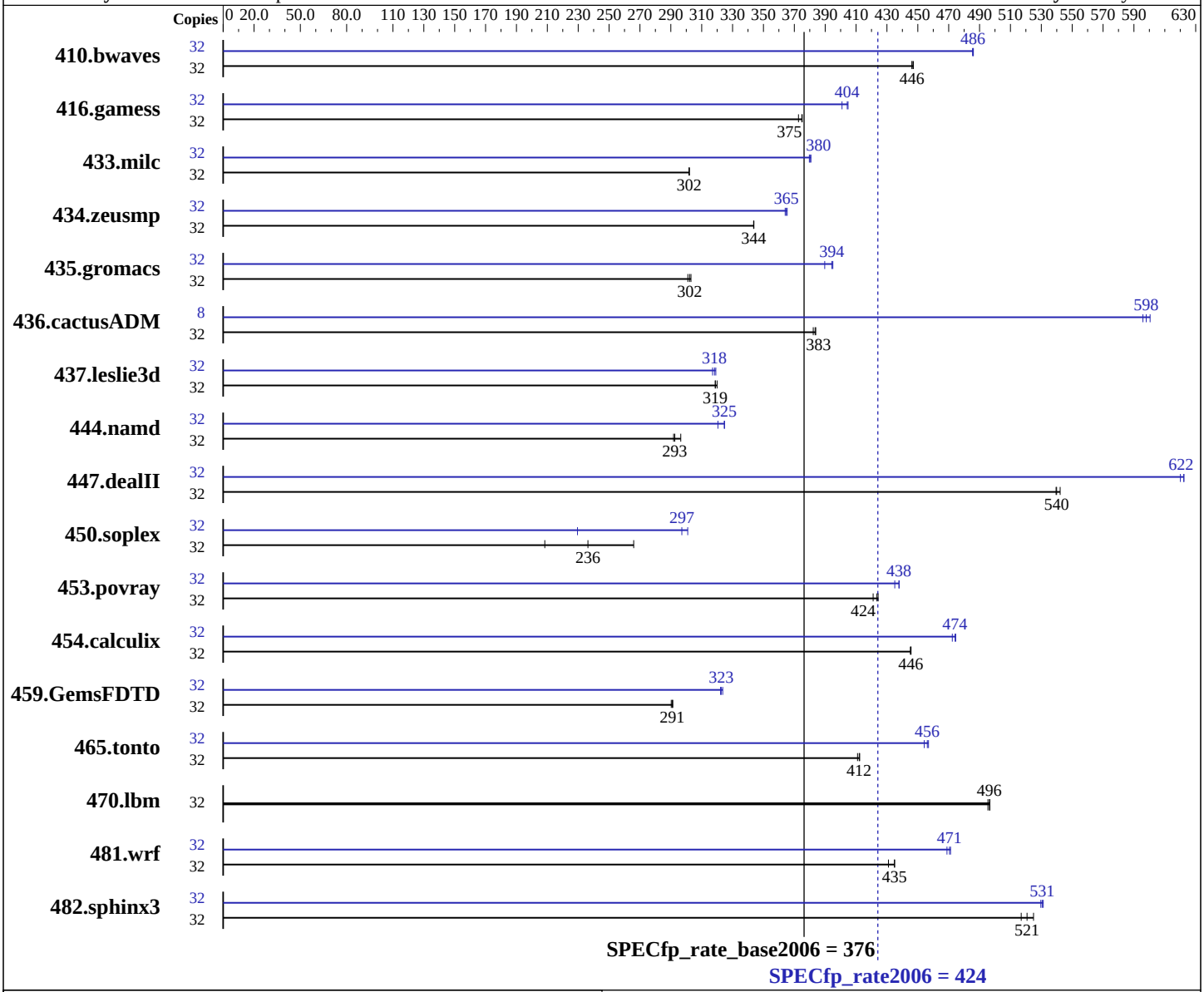
Gateway GR585 F1 (AMD Opteron 6124 HE)

SPECfp®_rate2006 = 424

SPECfp_rate_base2006 = 376

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Jul-2010
Hardware Availability: Jul-2010
Software Availability: May-2010



Hardware

CPU Name:	AMD Opteron 6124 HE
CPU Characteristics:	
CPU MHz:	1800
FPU:	Integrated
CPU(s) enabled:	32 cores, 4 chips, 8 cores/chip
CPU(s) orderable:	2,4 chips
Primary Cache:	64 KB I + 64 KB D on chip per core
Secondary Cache:	512 KB I+D on chip per core

Continued on next page

Software

Operating System:	Red Hat Enterprise Linux Server release 5.5, Kernel 2.6.18-194.el5
Compiler:	x86 Open64 4.2.3.2 Compiler Suite (from AMD)
Auto Parallel:	Yes
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	32/64-bit
Other Software:	binutils 2.18

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L3 Cache: 12 MB I+D on chip per chip, 6 MB shared / 4 cores
Other Cache: None
Memory: 128 GB (32 x 4 GB DDR3-1333 RDIMM, CL9, Dual Rank)
Disk Subsystem: 1 x 1000 GB SATA, 7200 RPM
Other Hardware: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	32	973	447	**974**	**446**	975	446	32	896	485	**895**	**486**	895	486
416.gamess	32	1681	373	**1671**	**375**	1670	375	32	**1550**	**404**	1563	401	1548	405
433.milc	32	973	302	**973**	**302**	973	302	32	771	381	774	380	**773**	**380**
434.zeusmp	32	847	344	847	344	**847**	**344**	32	**798**	**365**	797	365	800	364
435.gromacs	32	759	301	**756**	**302**	754	303	32	586	390	578	395	**579**	**394**
436.cactusADM	32	996	384	1000	382	**997**	**383**	8	159	600	**160**	**598**	160	596
437.leslie3d	32	944	319	**943**	**319**	940	320	32	949	317	**945**	**318**	942	319
444.namd	32	880	292	**877**	**293**	866	296	32	801	321	**791**	**325**	790	325
447.dealII	32	675	542	**678**	**540**	678	540	32	588	622	590	620	**588**	**622**
450.soplex	32	**1129**	**236**	1281	208	1004	266	32	1163	230	**898**	**297**	887	301
453.povray	32	**402**	**424**	404	421	401	424	32	391	435	389	438	**389**	**438**
454.calculix	32	593	445	592	446	**593**	**446**	32	559	472	556	475	**557**	**474**
459.GemsFDTD	32	**1167**	**291**	1170	290	1166	291	32	**1052**	**323**	1054	322	1049	324
465.tonto	32	**764**	**412**	764	412	766	411	32	689	457	693	454	**690**	**456**
470.lbm	32	**886**	**496**	885	497	887	495	32	**886**	**496**	885	497	887	495
481.wrf	32	829	431	**822**	**435**	822	435	32	762	469	759	471	**760**	**471**
482.sphinx3	32	**1198**	**521**	1206	517	1188	525	32	1174	531	1177	530	**1176**	**531**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152'  was used to set environment locked pages in memory limit

Set vm/nr_hugepages=28672 in /etc/sysctl.conf
mount -t hugetlbfs nodev /mnt/hugepages
```

General Notes

```
Environment variables set by runspec before the start of the run:
HUGETLB_LIMIT = "896"
LD_LIBRARY_PATH = "/spec/amd1002mc-rate-libs-revB/64:/spec/amd1002mc-rate-libs-revB/32"
OMP_NUM_THREADS = "4"

The x86 Open64 Compiler Suite is only available from (and supported by) AMD at
http://developer.amd.com/cpu/open64

This result was measured on the Gateway GR585 F1.
The Acer AR585 F1 and Gateway GR585 F1 are electronically equivalent.
```

Base Compiler Invocation

C benchmarks:
opencc

C++ benchmarks:
openCC

Fortran benchmarks:
openf95

Benchmarks using both Fortran and C:
opencc openf95

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64
436.cactusADM: -DSPEC_CPU_LP64 -fno-second-underscore
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX -DSPEC_CPU_CASE_FLAG
-fno-second-underscore
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
`-march=barcelona -mso -Ofast -OPT:malloc_alg=1 -HP:bdt=2m`

C++ benchmarks:
`-march=barcelona -mso -Ofast -static -INLINE:aggressive=on -OPT:malloc_alg=1 -HP:bdt=2m`

Fortran benchmarks:
`-march=barcelona -mso -Ofast -HP`

Benchmarks using both Fortran and C:
`-march=barcelona -mso -Ofast -OPT:malloc_alg=1 -HP:bdt=2m -HP`

Peak Compiler Invocation

C benchmarks:
`opencc`

C++ benchmarks:
`openCC`

Fortran benchmarks:
`openf95`

Benchmarks using both Fortran and C:
`opencc openf95`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64`
436.cactusADM: `-DSPEC_CPU_LP64 -fno-second-underscore`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
470.lbm: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX -DSPEC_CPU_CASE_FLAG -fno-second-underscore`
482.sphinx3: `-DSPEC_CPU_LP64`

Peak Optimization Flags

C benchmarks:

433.milc: -march=barcelona -mso -Ofast -CG:movnti=1
-CG:local_sched_alg=1 -CG:locs_shallow_depth=1
-HP:bdt=2m:heap=2m -LNO:prefetch=3

470.lbm: basepeak = yes

482.sphinx3: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -OPT:malloc_alg=2
-CG:sse_cse_regs=0 -CG:locs_shallow_depth=1 -CG:cmp_peep=on
-CG:local_sched_alg=1 -INLINE:aggressive=on

C++ benchmarks:

444.namd: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -LNO:ignore_feedback=off
-CG:local_sched_alg=2 -CG:load_exe=0 -CG:compute_to=on
-OPT:unroll_size=256 -fno-exceptions -HP:bdt=2m:heap=2m

447.dealII: -march=barcelona -mso -Ofast -static -INLINE:aggressive=on
-LNO:opt=0 -fno-emit-exceptions -m32
-OPT:unroll_times_max=8 -OPT:unroll_size=256
-OPT:unroll_level=2 -HP:bdt=2m:heap=2m -GRA:unspill=on
-CG:cmp_peep=on -TENV:frame_pointer=off

450.soplex: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -INLINE:aggressive=on
-OPT:IEEE_arith=3 -OPT:IEEE_NaN_Inf=off
-OPT:fold_unsigned_relops=on -OPT:malloc_alg=1
-CG:load_exe=0 -fno-exceptions -m32 -HP:bdt=2m

453.povray: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -INLINE:aggressive=on

Fortran benchmarks:

410.bwaves: -march=barcelona -mso -O3 -OPT:Ofast -OPT:treeheight=on
-LNO:blocking=off -LNO:prefetch_ahead=5
-LNO:ignore_feedback=off -WOPT:aggstr=0 -HP:bdt=2m:heap=2m
-CG:cmp_peep=on

416.gamess: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -LNO:fu=6 -LNO:blocking=0
-LNO:prefetch=0 -OPT:Ofast -OPT:ro=3 -OPT:unroll_size=256
-HP:bdt=2m:heap=2m

434.zeusmp: -march=barcelona -mso -Ofast -LNO:blocking=off
-LNO:interchange=off -OPT:treeheight=on -OPT:unroll_size=256
-CG:cmp_peep=on -GRA:prioritize_by_density=on -HP

Continued on next page

Peak Optimization Flags (Continued)

437.leslie3d: -march=barcelona -mso -Ofast -HP:bdt=2m:heap=2m

459.GemsFDTD: -march=barcelona -mso -Ofast -LNO:fission=2
-LNO:prefetch_ahead=1 -CG:load_exe=0 -CG:local_sched_alg=1
-HP

465.tonto: -march=barcelona -mso -Ofast
-OPT:alias=no_f90_pointer_alias -LNO:blocking=off
-CG:load_exe=1 -IPA:plimit=525 -HP

Benchmarks using both Fortran and C:

435.gromacs: -march=barcelona -mso -Ofast -OPT:rsqrt=2
-HP:bdt=2m:heap=2m

436.cactusADM: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -apo -LNO:prefetch_ahead=1
-HP:bdt=2m:heap=2m -LANG:heap_allocation_threshold=100

454.calculix: -march=barcelona -mso -Ofast -CG:load_exe=0
-CG:ptr_load_use=0 -CG:local_sched_alg=2 -CG:compute_to=on
-LNO:prefetch_ahead=30 -WOPT:unroll=2
-GRA:optimize_boundary=on -HP:bdt=2m:heap=2m

481.wrf: -march=barcelona -mso -Ofast -LNO:blocking=off
-LNO:prefetch_ahead=10 -LANG:copyinout=off
-IPA:callee_limit=5000 -GRA:prioritize_by_density=on -m3dnow
-HP

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/amd-platform-revA.html
http://www.spec.org/cpu2006/flags/x86-open64-4.2.3-flags-revA.20100721.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/amd-platform-revA.xml
http://www.spec.org/cpu2006/flags/x86-open64-4.2.3-flags-revA.20100721.xml

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