



SPEC[®] CFP2006 Result

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NEC Corporation

Express5800/R120b-2
(Intel Xeon E5640)

SPECfp[®]_rate2006 = 92.1

SPECfp_rate_base2006 = 88.8

CPU2006 license: 9006

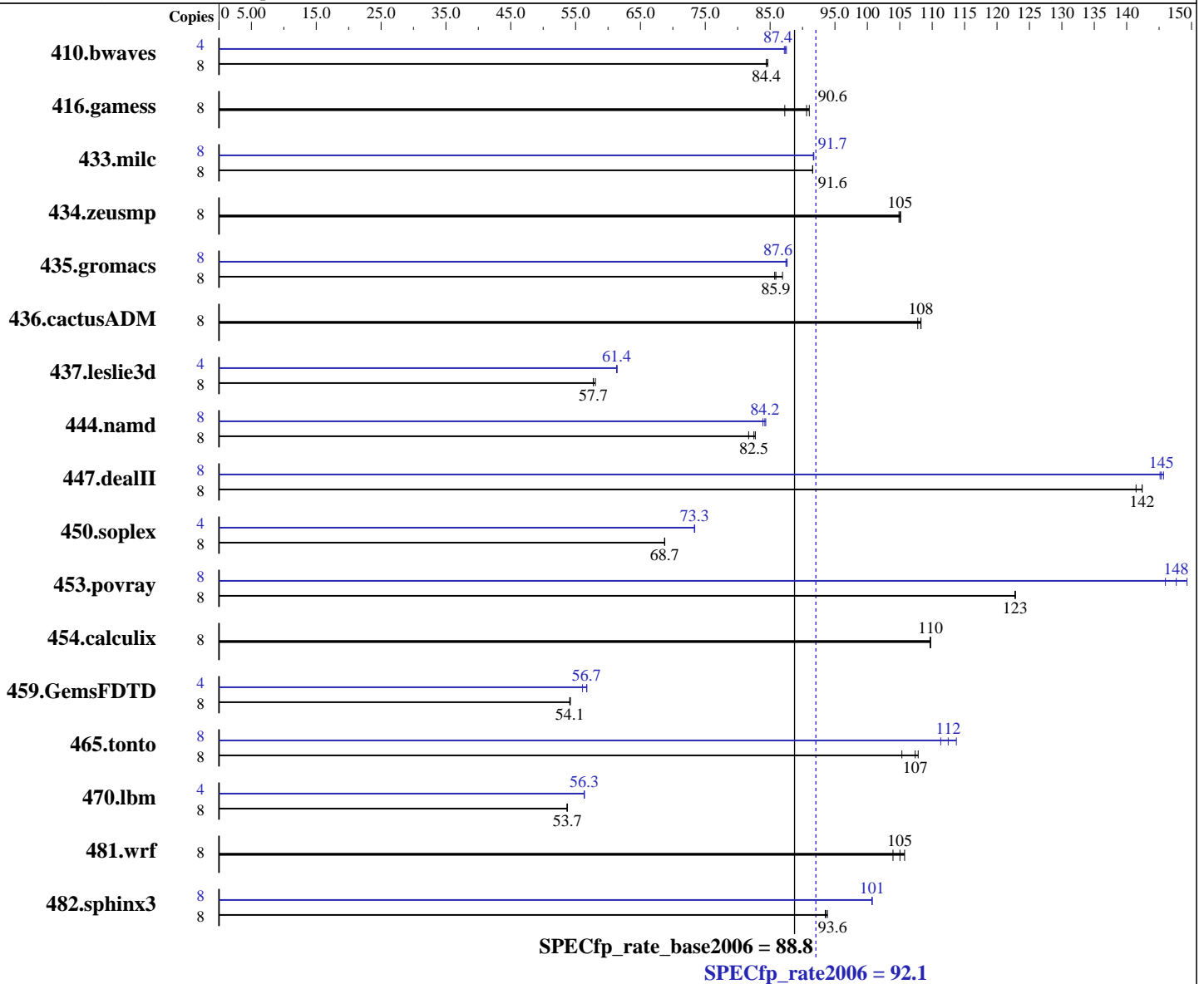
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Aug-2010

Hardware Availability: Sep-2010

Software Availability: Dec-2009



Hardware

CPU Name: Intel Xeon E5640
 CPU Characteristics: Intel Turbo Boost Technology up to 2.93 GHz
 CPU MHz: 2667
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
 Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: I_cproc_p_11.1.064, L_cprof_p_11.1.064
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)

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L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (6 x 8 GB PC3L-10600R, 2 rank, CL9, ECC, running at 1066 MHz)
Disk Subsystem: 1x160 GB SATA, 7200 RPM
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	1284	84.7	<u>1288</u>	<u>84.4</u>	1288	84.4	4	623	87.2	621	87.5	<u>622</u>	<u>87.4</u>
416.gamess	8	1720	91.0	1795	87.3	<u>1728</u>	<u>90.6</u>	8	1720	91.0	1795	87.3	<u>1728</u>	<u>90.6</u>
433.milc	8	802	91.6	<u>802</u>	<u>91.6</u>	802	91.6	8	<u>801</u>	<u>91.7</u>	801	91.7	800	91.8
434.zeusmp	8	<u>693</u>	<u>105</u>	694	105	692	105	8	<u>693</u>	<u>105</u>	694	105	692	105
435.gromacs	8	657	86.9	<u>665</u>	<u>85.9</u>	666	85.7	8	<u>652</u>	<u>87.6</u>	653	87.4	652	87.6
436.cactusADM	8	887	108	883	108	<u>883</u>	<u>108</u>	8	887	108	883	108	<u>883</u>	<u>108</u>
437.leslie3d	8	1295	58.1	1303	57.7	<u>1302</u>	<u>57.7</u>	4	<u>613</u>	<u>61.4</u>	612	61.4	613	61.3
444.namd	8	<u>778</u>	<u>82.5</u>	785	81.7	776	82.7	8	<u>762</u>	<u>84.2</u>	761	84.3	765	83.9
447.dealII	8	<u>643</u>	<u>142</u>	647	141	643	142	8	628	146	<u>630</u>	<u>145</u>	631	145
450.soplex	8	970	68.8	<u>971</u>	<u>68.7</u>	971	68.7	4	455	73.3	455	73.4	<u>455</u>	<u>73.3</u>
453.povray	8	346	123	<u>347</u>	<u>123</u>	347	123	8	<u>288</u>	<u>148</u>	285	149	292	146
454.calculix	8	602	110	601	110	<u>601</u>	<u>110</u>	8	602	110	601	110	<u>601</u>	<u>110</u>
459.GemsFDTD	8	1566	54.2	1569	54.1	<u>1568</u>	<u>54.1</u>	4	757	56.1	<u>749</u>	<u>56.7</u>	748	56.7
465.tonto	8	730	108	<u>733</u>	<u>107</u>	747	105	8	<u>700</u>	<u>112</u>	692	114	707	111
470.lbm	8	2047	53.7	2046	53.7	<u>2047</u>	<u>53.7</u>	4	<u>975</u>	<u>56.3</u>	976	56.3	975	56.4
481.wrf	8	845	106	<u>851</u>	<u>105</u>	860	104	8	845	106	<u>851</u>	<u>105</u>	860	104
482.sphinx3	8	<u>1666</u>	<u>93.6</u>	1667	93.5	1661	93.9	8	<u>1548</u>	<u>101</u>	1549	101	1548	101

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS setting:
Performance/Watt: Traditional
Server Class: Custom
Data Reuse Optimization: Disabled



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General Notes

The Express5800/R120b-1 and the Express5800/R120b-2 models are electronically equivalent. The results have been measured on the Express5800/R120b-1 model.

Base Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

Base Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
450.soplex: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
470.lbm: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`
482.sphinx3: `-DSPEC_CPU_LP64`

Base Optimization Flags

C benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static`

C++ benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static`

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Base Optimization Flags (Continued)

Fortran benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static`

Benchmarks using both Fortran and C:

`-xSSE4.2 -ipo -O3 -no-prec-div -static`

Peak Compiler Invocation

C benchmarks (except as noted below):

`icc -m64`

482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):

`icpc -m64`

450.soplex: `icpc -m32`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
 416.gamess: `-DSPEC_CPU_LP64`
 433.milc: `-DSPEC_CPU_LP64`
 434.zeusmp: `-DSPEC_CPU_LP64`
 435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
 436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
 437.leslie3d: `-DSPEC_CPU_LP64`
 444.namd: `-DSPEC_CPU_LP64`
 447.deallI: `-DSPEC_CPU_LP64`
 453.povray: `-DSPEC_CPU_LP64`
 454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
 459.GemsFDTD: `-DSPEC_CPU_LP64`
 465.tonto: `-DSPEC_CPU_LP64`
 470.lbm: `-DSPEC_CPU_LP64`
 481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`



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Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -opt-prefetch

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -ansi-alias -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealIII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias -scalar-rep-

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

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Peak Optimization Flags (Continued)

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE-R120b.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE-R120b.xml>

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For other inquiries, please contact webmaster@spec.org.

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