SPEC® CINT2006 Result
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Inspur Corporation
TS850

$SPECint^{®}\_rate2006 =$ 1400

$SPECint\_rate\_base2006 =$ 1320
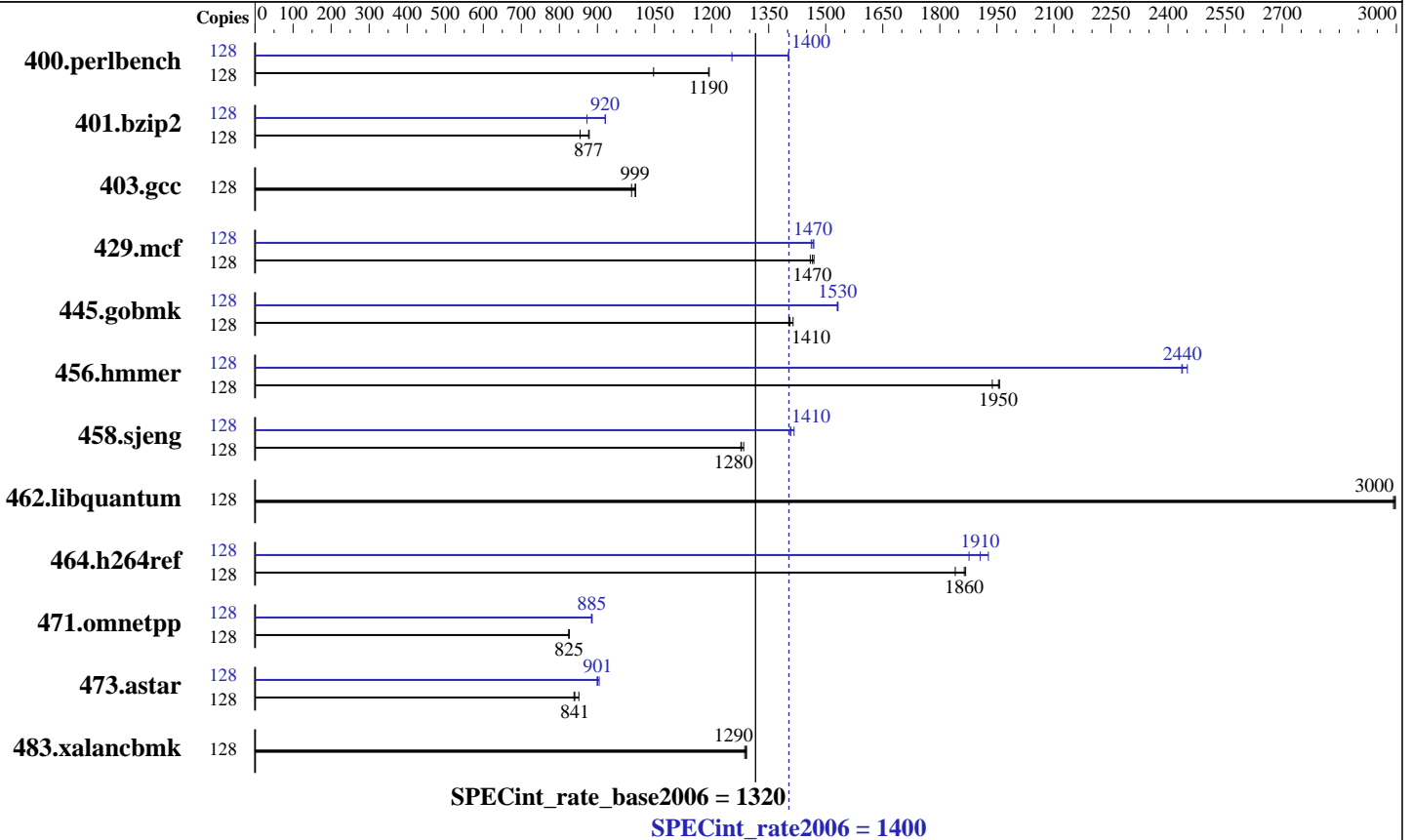
CPU2006 license: 3358
Test sponsor: Inspur Corporation
Tested by: Inspur Corporation

Test date: Sep-2010
Hardware Availability: Apr-2010
Software Availability: Feb-2010

SPECint_rate_base2006 = 1320

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Hardware

CPU Name: Intel Xeon X7560
CPU Characteristics: Intel Turbo Boost Technology up to 2.67 GHz
CPU MHz: 2267
FPU: Integrated
CPU(s) enabled: 64 cores, 8 chips, 8 cores/chip, 2 threads/core
CPU(s) orderable: 4,6,8 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 24 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (64 x 4 GB 4Rx8 PC3-8500R-7,ECC)
Disk Subsystem: 2 x 300 GB (SAS, 10000 RPM, RAID0)
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1
Build 20091130 Package ID: l_cproc_p_11.1.064
Auto Parallel: No
File System: ext3
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V8.1
Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	___Base___ Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	___Peak___ Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	128	1194	1050	1047	1190	**1049**	**1190**	128	998	1250	**892**	**1400**	892	1400
401.bzip2	128	1446	854	1406	879	**1409**	**877**	128	1416	873	**1342**	**920**	1341	921
403.gcc	128	1030	1000	1041	990	**1031**	**999**	128	1030	1000	1041	990	**1031**	**999**
429.mcf	128	800	1460	**797**	**1470**	795	1470	128	**796**	**1470**	795	1470	799	1460
445.gobmk	128	957	1400	949	1410	**955**	**1410**	128	877	1530	876	1530	**877**	**1530**
456.hmmer	128	616	1940	610	1960	**611**	**1950**	128	487	2450	**490**	**2440**	490	2440
458.sjeng	128	1213	1280	**1212**	**1280**	1206	1280	128	**1100**	**1410**	1093	1420	1101	1410
462.libquantum	128	886	2990	**885**	**3000**	885	3000	128	886	2990	**885**	**3000**	885	3000
464.h264ref	128	1516	1870	**1519**	**1860**	1539	1840	128	1470	1930	1509	1880	**1486**	**1910**
471.omnetpp	128	**969**	**825**	970	825	969	826	128	**904**	**885**	904	885	903	886
473.astar	128	1056	851	1072	839	**1069**	**841**	128	993	905	999	899	**998**	**901**
483.xalancbmk	128	**684**	**1290**	684	1290	686	1290	128	**684**	**1290**	684	1290	686	1290

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
numactl was used to bind copies to the cores
```

Platform Notes

```
Turbo Mode enabled
```

General Notes

```
'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
```

Base Compiler Invocation

```
C benchmarks:
     icc -m32

C++ benchmarks:
     icpc -m32
```

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
```

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Base Portability Flags (Continued)

    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

    -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:

    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
    -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

Base Other Flags

C benchmarks:

        403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc -m32

        401.bzip2: icc -m64

        456.hmmer: icc -m64

        458.sjeng: icc -m64

C++ benchmarks (except as noted below):
    icpc -m32

        473.astar: icpc -m64

Peak Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
        401.bzip2: -DSPEC_CPU_LP64
        456.hmmer: -DSPEC_CPU_LP64
        458.sjeng: -DSPEC_CPU_LP64
    462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2(pass 2)` `-prof-gen(pass 1)` `-ipo(pass 2)`
`-O3(pass 2)` `-no-prec-div(pass 2)` `-static(pass 2)`
`-prof-use(pass 2)` `-ansi-alias`

401.bzip2: `-xSSE4.2(pass 2)` `-prof-gen(pass 1)` `-ipo(pass 2)`
`-O3(pass 2)` `-no-prec-div(pass 2)` `-static(pass 2)`
`-prof-use(pass 2)` `-opt-prefetch` `-ansi-alias` `-auto-ilp32`

403.gcc: `basepeak = yes`

429.mcf: `-xSSE4.2` `-ipo` `-O3` `-no-prec-div` `-static` `-opt-prefetch`

445.gobmk: `-xSSE4.2(pass 2)` `-prof-gen(pass 1)` `-prof-use(pass 2)` `-O2`
`-ipo` `-no-prec-div` `-ansi-alias`

456.hmmer: `-xSSE4.2` `-ipo` `-O3` `-no-prec-div` `-static` `-unroll2`
`-ansi-alias` `-auto-ilp32`

458.sjeng: `-xSSE4.2(pass 2)` `-prof-gen(pass 1)` `-ipo(pass 2)`
`-O3(pass 2)` `-no-prec-div(pass 2)` `-static(pass 2)`
`-prof-use(pass 2)` `-unroll4` `-auto-ilp32`

462.libquantum: `basepeak = yes`

464.h264ref: `-xSSE4.2(pass 2)` `-prof-gen(pass 1)` `-ipo(pass 2)`
`-O3(pass 2)` `-no-prec-div(pass 2)` `-static(pass 2)`
`-prof-use(pass 2)` `-unroll2` `-ansi-alias`

C++ benchmarks:

471.omnetpp: `-xSSE4.2(pass 2)` `-prof-gen(pass 1)` `-ipo(pass 2)`
`-O3(pass 2)` `-no-prec-div(pass 2)` `-prof-use(pass 2)`
`-ansi-alias` `-opt-ra-region-strategy=block` `-Wl,-z,muldefs`
`-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit` `-lsmartheap`

473.astar: `-xSSE4.2(pass 2)` `-prof-gen(pass 1)` `-ipo(pass 2)`
`-O3(pass 2)` `-no-prec-div(pass 2)` `-prof-use(pass 2)`
`-ansi-alias` `-opt-ra-region-strategy=routine` `-Wl,-z,muldefs`
`-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit` `-lsmartheap64`

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Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.xml

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