



# SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## IBM Corporation

SPECfp®\_rate2006 = 369

### IBM BladeCenter HX5 (Intel Xeon E7-2870)

SPECfp\_rate\_base2006 = 356

CPU2006 license: 11

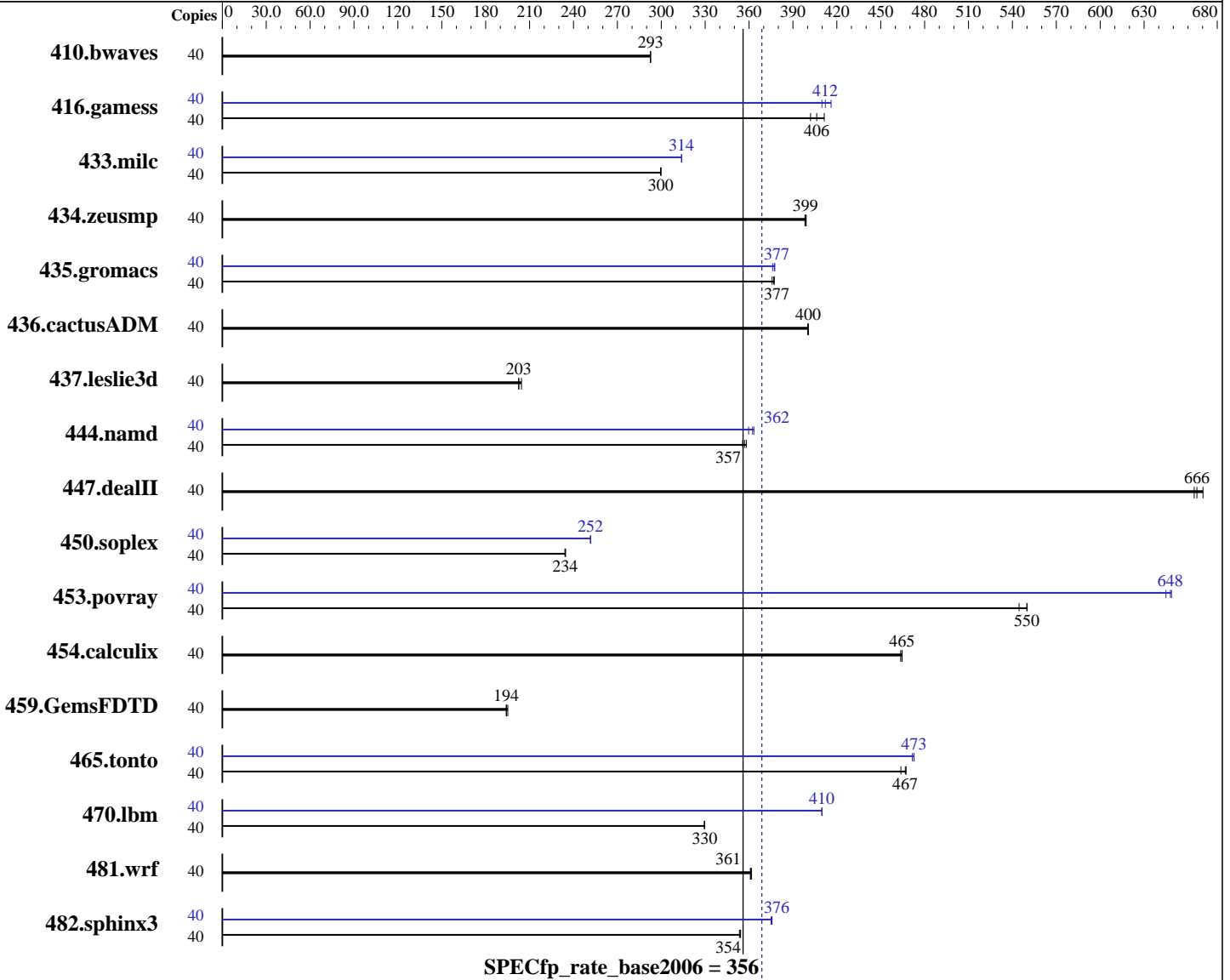
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Feb-2011

Hardware Availability: May-2011

Software Availability: Apr-2011



### Hardware

CPU Name: Intel Xeon E7-2870  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

### Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86\_64), Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## IBM Corporation

SPECfp\_rate2006 = 369

### IBM BladeCenter HX5 (Intel Xeon E7-2870)

SPECfp\_rate\_base2006 = 356

CPU2006 license: 11

Test date: Feb-2011

Test sponsor: IBM Corporation

Hardware Availability: May-2011

Tested by: IBM Corporation

Software Availability: Apr-2011

L3 Cache: 30 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 128 GB (16 x 8 GB 4Rx8 PC3-8500R-7, ECC)  
 Disk Subsystem: 2 x 50 GB SSD, RAID 0  
 Other Hardware: None

Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	40	1858	293	1857	293	<u>1857</u>	<u>293</u>	40	1858	293	1857	293	<u>1857</u>	<u>293</u>
416.gamess	40	1948	402	1904	411	<u>1928</u>	<u>406</u>	40	1882	416	1911	410	<u>1900</u>	<u>412</u>
433.milc	40	1226	300	1225	300	<u>1226</u>	<u>300</u>	40	1169	314	1170	314	<u>1170</u>	<u>314</u>
434.zeusmp	40	914	398	912	399	<u>913</u>	<u>399</u>	40	914	398	912	399	<u>913</u>	<u>399</u>
435.gromacs	40	757	377	760	376	<u>758</u>	<u>377</u>	40	759	376	<u>757</u>	<u>377</u>	756	378
436.cactusADM	40	1194	400	<u>1194</u>	<u>400</u>	1195	400	40	1194	400	<u>1194</u>	<u>400</u>	1195	400
437.leslie3d	40	<u>1855</u>	<u>203</u>	1856	203	1839	204	40	<u>1855</u>	<u>203</u>	1856	203	1839	204
444.namd	40	902	356	896	358	<u>899</u>	<u>357</u>	40	892	360	<u>885</u>	<u>362</u>	883	363
447.dealII	40	689	664	<u>687</u>	<u>666</u>	683	670	40	689	664	<u>687</u>	<u>666</u>	683	670
450.soplex	40	1422	235	<u>1423</u>	<u>234</u>	1425	234	40	1325	252	1326	252	<u>1325</u>	<u>252</u>
453.povray	40	<u>387</u>	<u>550</u>	391	545	387	550	40	330	645	<u>328</u>	<u>648</u>	328	649
454.calculix	40	710	465	<u>710</u>	<u>465</u>	712	464	40	710	465	<u>710</u>	<u>465</u>	712	464
459.GemsFDTD	40	2186	194	<u>2186</u>	<u>194</u>	2175	195	40	2186	194	<u>2186</u>	<u>194</u>	2175	195
465.tonto	40	849	464	<u>843</u>	<u>467</u>	842	467	40	<u>833</u>	<u>473</u>	832	473	835	472
470.lbm	40	1668	329	<u>1667</u>	<u>330</u>	1667	330	40	1341	410	1341	410	<u>1341</u>	<u>410</u>
481.wrf	40	1238	361	<u>1238</u>	<u>361</u>	1235	362	40	1238	361	<u>1238</u>	<u>361</u>	1235	362
482.sphinx3	40	2201	354	<u>2202</u>	<u>354</u>	2206	353	40	2075	376	2079	375	<u>2075</u>	<u>376</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
 numactl was used to bind copies to the cores

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
echo 1 > /proc/sys/vm/zone_reclaim_mode
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 18000 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp\_rate2006 = 369

IBM BladeCenter HX5 (Intel Xeon E7-2870)

SPECfp\_rate\_base2006 = 356

CPU2006 license: 11

Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Feb-2011

Hardware Availability: May-2011

Software Availability: Apr-2011

## Platform Notes

BIOS Settings:  
Turbo Boost Power Optimization set to Traditional

## General Notes

Binaries were compiled on RHEL5.5

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp\_rate2006 = 369

IBM BladeCenter HX5 (Intel Xeon E7-2870)

SPECfp\_rate\_base2006 = 356

CPU2006 license: 11

Test date: Feb-2011

Test sponsor: IBM Corporation

Hardware Availability: May-2011

Tested by: IBM Corporation

Software Availability: Apr-2011

## Base Optimization Flags (Continued)

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.deallI: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp\_rate2006 = 369

IBM BladeCenter HX5 (Intel Xeon E7-2870)

SPECfp\_rate\_base2006 = 356

CPU2006 license: 11

Test date: Feb-2011

Test sponsor: IBM Corporation

Hardware Availability: May-2011

Tested by: IBM Corporation

Software Availability: Apr-2011

## Peak Optimization Flags

### C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3  
-ansi-alias -opt-prefetch -static -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2

### C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

### Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto  
-inline-calloc -opt-malloc-options=3  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

### Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch  
-static -auto-ilp32

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp\_rate2006 = 369

IBM BladeCenter HX5 (Intel Xeon E7-2870)

SPECfp\_rate\_base2006 = 356

CPU2006 license: 11

Test date: Feb-2011

Test sponsor: IBM Corporation

Hardware Availability: May-2011

Tested by: IBM Corporation

Software Availability: Apr-2011

## Peak Optimization Flags (Continued)

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revA.html>

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revA.xml>

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.  
Report generated on Wed Jul 23 18:53:00 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 26 April 2011.