



# SPEC<sup>®</sup> CFP2006 Result

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## Intel Corporation

SPECfp<sup>®</sup>\_rate2006 = 104

Intel DH67BLB3 Motherboard (Intel Core i5-2500)

SPECfp\_rate\_base2006 = 103

CPU2006 license: 13

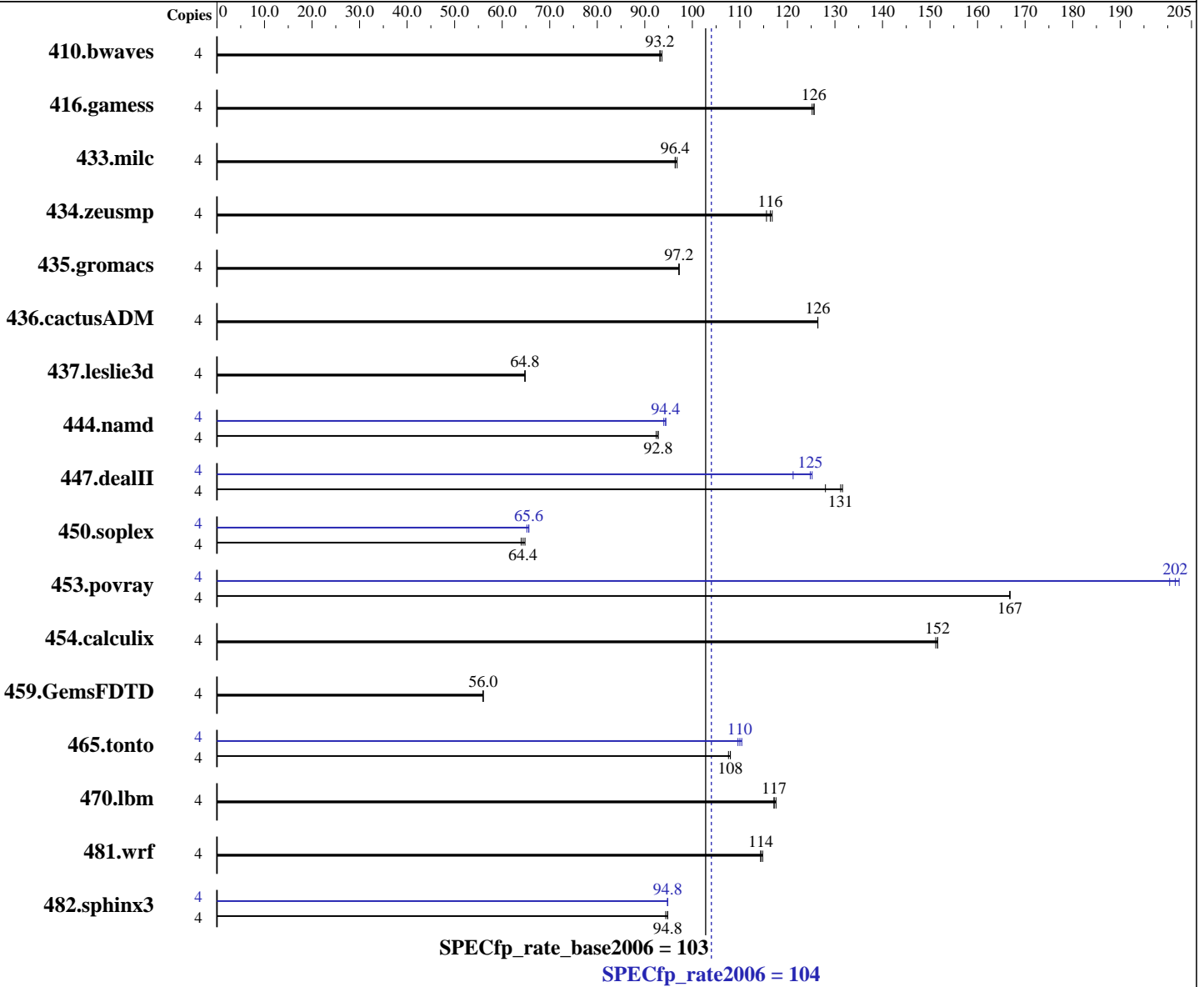
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Apr-2011

Hardware Availability: Mar-2011

Software Availability: Apr-2011



### Hardware

CPU Name: Intel Core i5-2500  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.7 GHz  
 CPU MHz: 3300  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

### Software

Operating System: Windows 7 Ultimate (64-bit)  
 Compiler: Intel C++ Compiler XE for Intel64  
 Version 12.0.3.163 Build 20110217  
 Intel Visual Fortran Compiler XE for Intel64  
 Version 12.0.0.104 Build 20101006  
 Microsoft Visual Studio 2008 Professional SP1  
 (for libraries)  
 Auto Parallel: No  
 File System: NTFS

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L3 Cache: 6 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600U-9)  
 Disk Subsystem: Seagate 1 TB SATA, 7200 RPM  
 Other Hardware: None

System State: Default  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: SmartHeap Library Version 9.01 from <http://www.microquill.com/>

## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	4	584	93.2	<u>583</u>	<u>93.2</u>	582	93.6	4	584	93.2	<u>583</u>	<u>93.2</u>	582	93.6		
416.gamess	4	625	125	<u>624</u>	<u>126</u>	624	126	4	625	125	<u>624</u>	<u>126</u>	624	126		
433.milc	4	<u>380</u>	<u>96.4</u>	380	96.4	380	96.8	4	<u>380</u>	<u>96.4</u>	380	96.4	380	96.8		
434.zeusmp	4	312	117	314	116	<u>313</u>	<u>116</u>	4	312	117	314	116	<u>313</u>	<u>116</u>		
435.gromacs	4	293	97.2	<u>294</u>	<u>97.2</u>	294	97.2	4	293	97.2	<u>294</u>	<u>97.2</u>	294	97.2		
436.cactusADM	4	378	126	379	126	<u>378</u>	<u>126</u>	4	378	126	379	126	<u>378</u>	<u>126</u>		
437.leslie3d	4	<u>580</u>	<u>64.8</u>	580	64.8	579	64.8	4	<u>580</u>	<u>64.8</u>	580	64.8	579	64.8		
444.namd	4	347	92.4	346	92.8	<u>346</u>	<u>92.8</u>	4	340	94.4	<u>341</u>	<u>94.4</u>	341	94.0		
447.dealII	4	348	132	<u>349</u>	<u>131</u>	357	128	4	366	125	<u>366</u>	<u>125</u>	378	121		
450.soplex	4	520	64.0	<u>517</u>	<u>64.4</u>	516	64.8	4	511	65.2	<u>508</u>	<u>65.6</u>	507	65.6		
453.povray	4	<u>128</u>	<u>167</u>	128	167	128	167	4	<u>106</u>	<u>202</u>	105	202	106	200		
454.calculix	4	<u>218</u>	<u>152</u>	218	151	218	152	4	<u>218</u>	<u>152</u>	218	151	218	152		
459.GemsFDTD	4	760	56.0	759	56.0	<u>759</u>	<u>56.0</u>	4	760	56.0	759	56.0	<u>759</u>	<u>56.0</u>		
465.tonto	4	365	108	366	108	<u>365</u>	<u>108</u>	4	356	110	<u>358</u>	<u>110</u>	359	110		
470.lbm	4	468	117	<u>468</u>	<u>117</u>	467	118	4	468	117	<u>468</u>	<u>117</u>	467	118		
481.wrf	4	390	114	<u>390</u>	<u>114</u>	390	115	4	390	114	<u>390</u>	<u>114</u>	390	115		
482.sphinx3	4	825	94.4	<u>823</u>	<u>94.8</u>	821	94.8	4	822	94.8	821	94.8	<u>821</u>	<u>94.8</u>		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
The start command with the /affinity switch was used to bind processes to cores

## General Notes

Tested systems can be used with Shin-G ATX case,  
PC Power and Cooling 1200W power supply

## Base Compiler Invocation

C benchmarks:  
icl -Qvc9 -Qstd=c99

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## Base Compiler Invocation (Continued)

C++ benchmarks:

icl -Qvc9

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icl -Qvc9 -Qstd=c99 ifort

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_P64 -names:lowercase  
 416.gamess: -DSPEC\_CPU\_P64  
 433.milc: -DSPEC\_CPU\_P64  
 434.zeusmp: -DSPEC\_CPU\_P64  
 435.gromacs: -DSPEC\_CPU\_P64  
 436.cactusADM: -DSPEC\_CPU\_P64 /names:lowercase /assume:underscore  
 437.leslie3d: -DSPEC\_CPU\_P64  
 444.namd: -DSPEC\_CPU\_P64 /TP  
 447.dealII: -DSPEC\_CPU\_P64 -DDEAL\_II\_MEMBER\_VAR\_SPECIALIZATION\_BUG  
 450.soplex: -DSPEC\_CPU\_P64  
 453.povray: -DSPEC\_CPU\_P64 -DSPEC\_CPU\_WINDOWS\_ICL  
 454.calculix: -DSPEC\_CPU\_P64 -DSPEC\_CPU\_NOZMODIFIER /names:lowercase  
 459.GemsFDTD: -DSPEC\_CPU\_P64  
 465.tonto: -DSPEC\_CPU\_P64  
 470.lbm: -DSPEC\_CPU\_P64  
 481.wrf: -DSPEC\_CPU\_P64 -DSPEC\_CPU\_WINDOWS\_ICL  
 482.sphinx3: -DSPEC\_CPU\_P64

## Base Optimization Flags

C benchmarks:

-QxAVX -Qipo -O3 -Qprec-div- -Qansi-alias -Qauto-ilp32 /F1000000000  
-link /FORCE:MULTIPLE

C++ benchmarks:

-QxAVX -Qipo -O3 -Qprec-div- -Qansi-alias -Qcxx-features  
-Qauto-ilp32 /F1000000000 shlw64M.lib -link /FORCE:MULTIPLE

Fortran benchmarks:

-QxAVX -Qipo -O3 -Qprec-div- -Qansi-alias /F1000000000  
-link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:

-QxAVX -Qipo -O3 -Qprec-div- -Qansi-alias -Qauto-ilp32 /F1000000000  
-link /FORCE:MULTIPLE



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## Peak Compiler Invocation

C benchmarks:

icl -Qvc9 -Qstd=c99

C++ benchmarks:

icl -Qvc9

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icl -Qvc9 -Qstd=c99 ifort

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: -QxAVX -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias  
-Qauto-ilp32 /F1000000000 -link /FORCE:MULTIPLE

C++ benchmarks:

444.namd: -QxAVX(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -Qipo  
-O3 -Qprec-div- -Oa -Qauto-ilp32 /F1000000000 sh1W64M.lib  
-link /FORCE:MULTIPLE

447.dealII: -QxAVX(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -Qipo  
-O3 -Qprec-div- -Qunroll2 -Qansi-alias -Qscalar-rep-  
-Qauto-ilp32 /F1000000000 sh1W64M.lib  
-link /FORCE:MULTIPLE

450.soplex: -QxAVX(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -Qipo  
-O3 -Qauto-ilp32 /F1000000000 sh1W64M.lib  
-link /FORCE:MULTIPLE

453.povray: -QxAVX(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -Qipo  
-O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32 /F1000000000  
sh1W64M.lib -link /FORCE:MULTIPLE

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## Peak Optimization Flags (Continued)

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -QxAVX(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -Qipo  
-O3 -Qprec-div- -Qunroll4 -Qauto /F1000000000  
-link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revB.html>

<http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revB.xml>

<http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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