SPEC® CFP2006 Result
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Intel Corporation

Intel DH67BLB3 Motherboard (Intel Core i5-2390T)

$SPECfp®\_rate2006 = \quad 69.6$

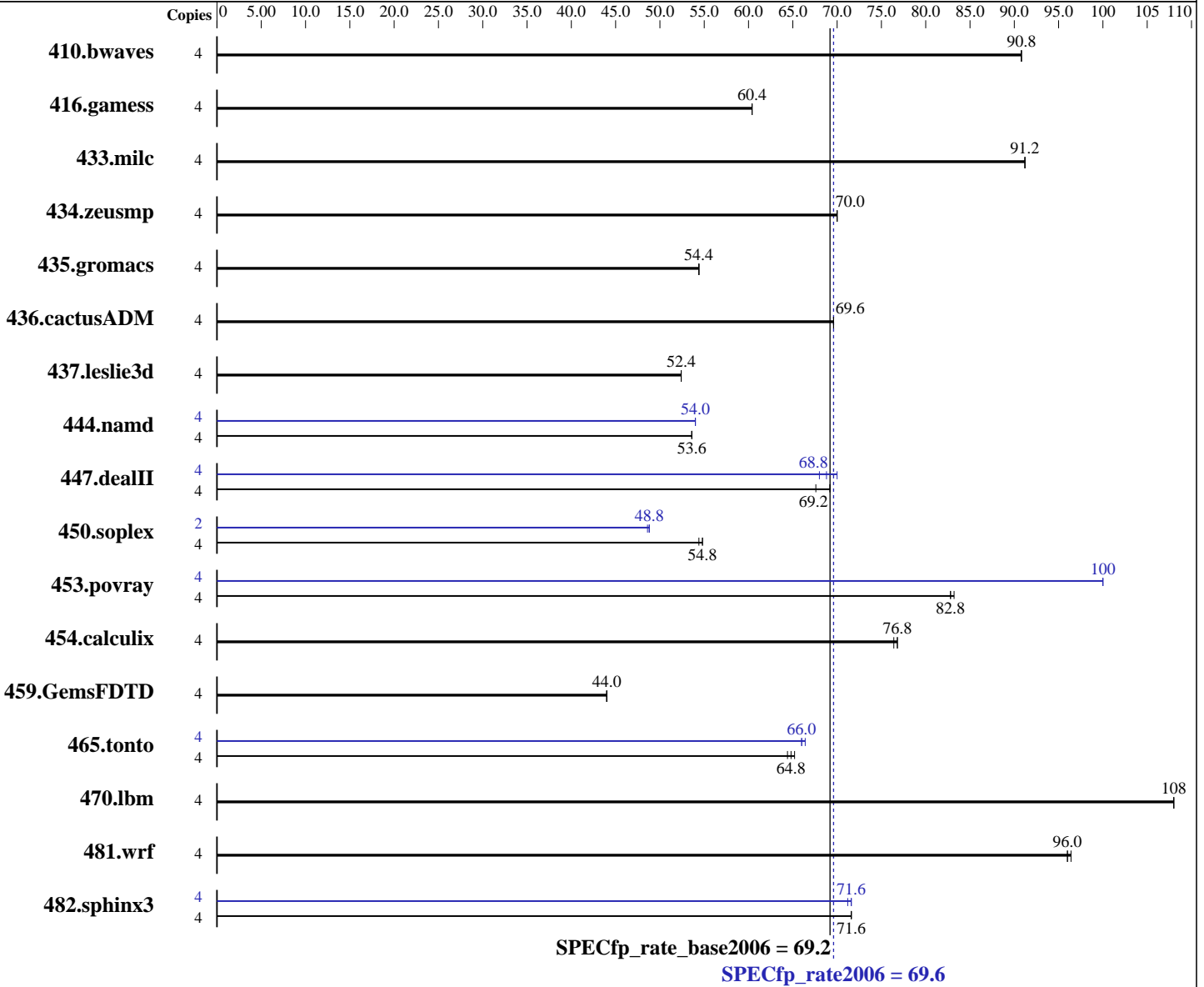$SPECfp\_rate\_base2006 = \quad 69.2$

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Apr-2011
Hardware Availability: Mar-2011
Software Availability: Apr-2011

Benchmark	Copies	Value
410.bwaves	4	90.8
416.gamess	4	60.4
433.milc	4	91.2
434.zeusmp	4	70.0
435.gromacs	4	54.4
436.cactusADM	4	69.6
437.leslie3d	4	52.4
444.namd	4	54.0
444.namd	4	53.6
447.dealII	4	68.8
447.dealII	4	69.2
450.soplex	2	48.8
450.soplex	4	54.8
453.povray	4	100
453.povray	4	82.8
454.calculix	4	76.8
459.GemsFDTD	4	44.0
465.tonto	4	66.0
465.tonto	4	64.8
470.lbm	4	108
481.wrf	4	96.0
482.sphinx3	4	71.6
482.sphinx3	4	71.6

SPECfp_rate_base2006 = 69.2

SPECfp_rate2006 = 69.6

Hardware

CPU Name: Intel Core i5-2390T
CPU Characteristics: Intel Turbo Boost Technology up to 3.5 GHz
CPU MHz: 2700
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip, 2 threads/core
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Windows 7 Ultimate (64-bit)
Compiler: Intel C++ Compiler XE for Intel64
Version 12.0.3.163 Build 20110217
Intel Visual Fortran Compiler XE for Intel64
Version 12.0.3.163 Build 20110217
Microsoft Visual Studio 2008 Professional SP1
(for libraries)
Auto Parallel: No
File System: NTFS

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L3 Cache:	3 MB I+D on chip per chip	System State:	Default
Other Cache:	None	Base Pointers:	32/64-bit
Memory:	8 GB (2 x 4 GB 2Rx8 PC3-10600U-9)	Peak Pointers:	32/64-bit
Disk Subsystem:	Seagate 1 TB SATA, 7200 RPM	Other Software:	SmartHeap Library Version 9.01 from
Other Hardware:	None		http://www.microquill.com/

Results Table

Benchmark	Base Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Peak Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	599	90.8	600	90.8	**600**	**90.8**	4	599	90.8	600	90.8	**600**	**90.8**
416.gamess	4	1296	60.4	**1295**	**60.4**	1294	60.4	4	1296	60.4	**1295**	**60.4**	1294	60.4
433.milc	4	**402**	**91.2**	403	91.2	402	91.2	4	**402**	**91.2**	403	91.2	402	91.2
434.zeusmp	4	**520**	**70.0**	520	70.0	519	70.0	4	**520**	**70.0**	520	70.0	519	70.0
435.gromacs	4	524	54.4	**524**	**54.4**	523	54.4	4	524	54.4	**524**	**54.4**	523	54.4
436.cactusADM	4	686	69.6	**687**	**69.6**	687	69.6	4	686	69.6	**687**	**69.6**	687	69.6
437.leslie3d	4	719	52.4	716	52.4	**716**	**52.4**	4	719	52.4	716	52.4	**716**	**52.4**
444.namd	4	597	53.6	597	53.6	**597**	**53.6**	4	**592**	**54.0**	592	54.0	592	54.0
447.dealII	4	**663**	**69.2**	659	69.2	677	67.6	4	654	70.0	**665**	**68.8**	671	68.0
450.soplex	4	**610**	**54.8**	611	54.4	610	54.8	2	343	48.6	342	48.8	**342**	**48.8**
453.povray	4	257	82.8	256	83.2	**256**	**82.8**	4	213	100	**213**	**100**	212	100
454.calculix	4	430	76.8	431	76.4	**430**	**76.8**	4	430	76.8	431	76.4	**430**	**76.8**
459.GemsFDTD	4	**965**	**44.0**	965	44.0	964	44.0	4	**965**	**44.0**	965	44.0	964	44.0
465.tonto	4	611	64.4	605	65.2	**607**	**64.8**	4	**595**	**66.0**	596	66.0	594	66.4
470.lbm	4	509	108	**509**	**108**	509	108	4	509	108	**509**	**108**	509	108
481.wrf	4	463	96.4	466	96.0	**466**	**96.0**	4	463	96.4	466	96.0	**466**	**96.0**
482.sphinx3	4	1088	71.6	1087	71.6	**1087**	**71.6**	4	1093	71.2	**1092**	**71.6**	1092	71.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The config file option 'submit' was used.
    The start command with the /affinity switch was used to bind processes to cores

General Notes

    Tested systems can be used with Shin-G ATX case,
    PC Power and Cooling 1200W power supply

Base Compiler Invocation

    C benchmarks:
        icl -Qvc9 -Qstd=c99

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Base Compiler Invocation (Continued)

C++ benchmarks:
```
icl -Qvc9
```

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icl -Qvc9 -Qstd=c99 ifort
```

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_P64 -names:lowercase
    416.gamess: -DSPEC_CPU_P64
      433.milc: -DSPEC_CPU_P64
    434.zeusmp: -DSPEC_CPU_P64
    435.gromacs: -DSPEC_CPU_P64
  436.cactusADM: -DSPEC_CPU_P64 /names:lowercase /assume:underscore
   437.leslie3d: -DSPEC_CPU_P64
     444.namd: -DSPEC_CPU_P64 /TP
     447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
    450.soplex: -DSPEC_CPU_P64
    453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
   454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER /names:lowercase
  459.GemsFDTD: -DSPEC_CPU_P64
     465.tonto: -DSPEC_CPU_P64
      470.lbm: -DSPEC_CPU_P64
       481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
    482.sphinx3: -DSPEC_CPU_P64
```

Base Optimization Flags

C benchmarks:
```
-QxAVX -Qipo -O3 -Qprec-div- -Qansi-alias -Qauto-ilp32 /F1000000000
        -link /FORCE:MULTIPLE
```

C++ benchmarks:
```
-QxAVX -Qipo -O3 -Qprec-div- -Qansi-alias -Qcxx-features
-Qauto-ilp32 /F1000000000 shlW64M.lib          -link /FORCE:MULTIPLE
```

Fortran benchmarks:
```
-QxAVX -Qipo -O3 -Qprec-div- -Qansi-alias /F1000000000
        -link /FORCE:MULTIPLE
```

Benchmarks using both Fortran and C:
```
-QxAVX -Qipo -O3 -Qprec-div- -Qansi-alias -Qauto-ilp32 /F1000000000
        -link /FORCE:MULTIPLE
```

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Peak Compiler Invocation

C benchmarks:
    icl -Qvc9 -Qstd=c99

C++ benchmarks:
    icl -Qvc9

Fortran benchmarks:
    ifort

Benchmarks using both Fortran and C:
    icl -Qvc9 -Qstd=c99 ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

   433.milc: basepeak = yes

   470.lbm: basepeak = yes

   482.sphinx3: -QxAVX -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
                -Qauto-ilp32 /F1000000000   -link /FORCE:MULTIPLE

C++ benchmarks:

   444.namd: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
             -O3 -Qprec-div- -Oa -Qauto-ilp32 /F1000000000 shlW64M.lib
                        -link /FORCE:MULTIPLE

   447.dealII: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
               -O3 -Qprec-div- -Qunroll2 -Qansi-alias -Qscalar-rep-
               -Qauto-ilp32 /F1000000000 shlW64M.lib
                        -link /FORCE:MULTIPLE

   450.soplex: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
               -O3 -Qauto-ilp32 /F1000000000 shlW64M.lib
                        -link /FORCE:MULTIPLE

   453.povray: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
               -O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32 /F1000000000
               shlW64M.lib   -link /FORCE:MULTIPLE

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Peak Optimization Flags (Continued)

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qunroll4 -Qauto /F1000000000
-link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revB.html
http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revB.xml
http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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