



# SPEC® CFP2006 Result

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## Intel Corporation

### SPECfp®\_rate2006 = 61.9

### Intel DH67BLB3 Motherboard (Intel Core i3-2100T)

### SPECfp\_rate\_base2006 = 61.9

CPU2006 license: 13

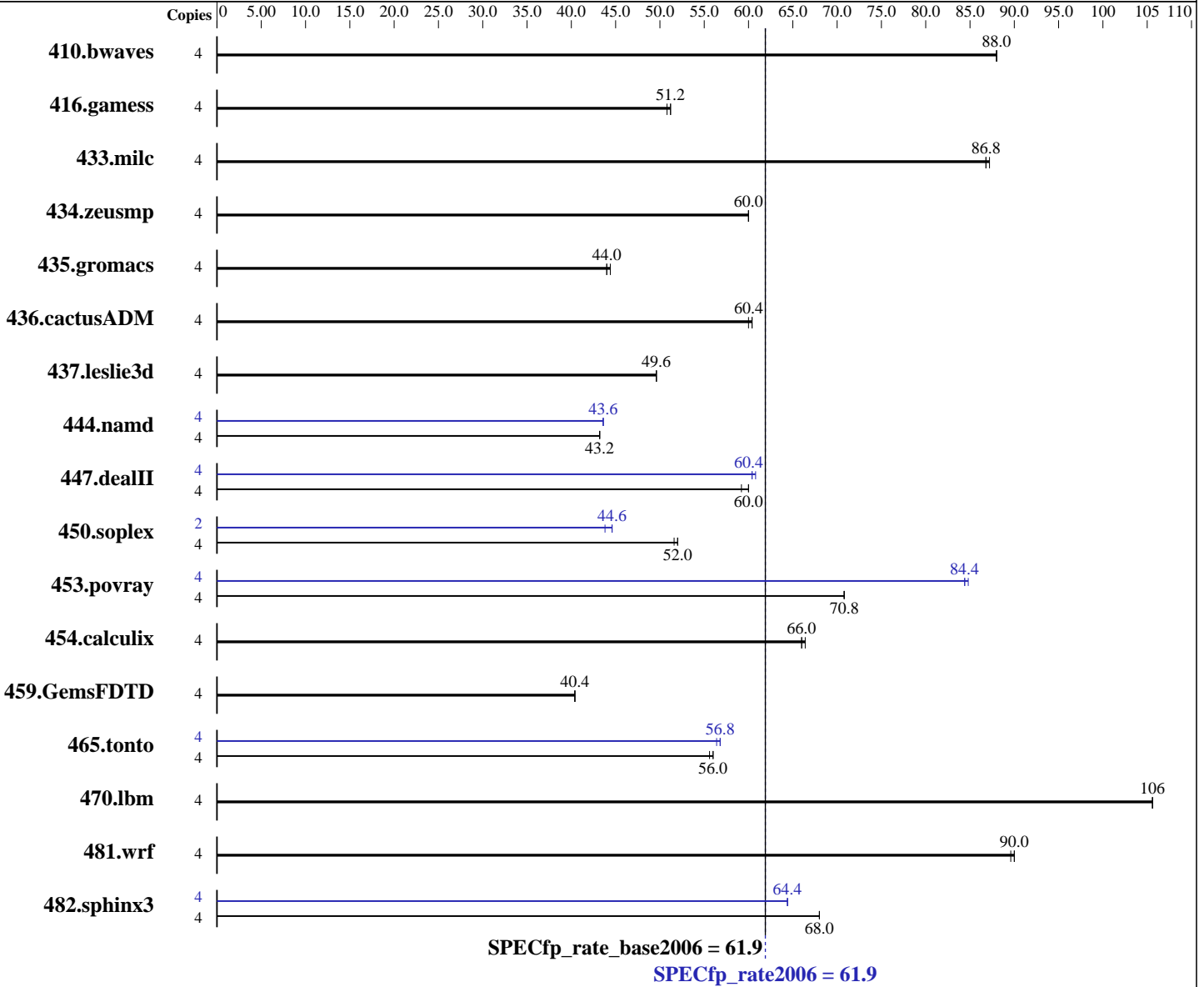
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Apr-2011

Hardware Availability: Mar-2011

Software Availability: Apr-2011



### Hardware

CPU Name: Intel Core i3-2100T  
 CPU Characteristics:  
 CPU MHz: 2500  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Windows 7 Ultimate (64-bit)  
 Compiler: Intel C++ Compiler XE for Intel64  
 Version 12.0.3.163 Build 20110217  
 Intel Visual Fortran Compiler XE for Intel64  
 Version 12.0.3.163 Build 20110217  
 Microsoft Visual Studio 2008 Professional SP1  
 (for libraries)

Auto Parallel: No  
 File System: NTFS

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L3 Cache: 3 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600U-9)  
 Disk Subsystem: Seagate 1 TB SATA, 7200 RPM  
 Other Hardware: None

System State: Default  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: SmartHeap Library Version 9.01 from <http://www.microquill.com/>

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	618	88.0	617	88.0	<b>617</b>	<b>88.0</b>	4	618	88.0	617	88.0	<b>617</b>	<b>88.0</b>
416.gamess	4	<b>1533</b>	<b>51.2</b>	1536	50.8	1533	51.2	4	<b>1533</b>	<b>51.2</b>	1536	50.8	1533	51.2
433.milc	4	<b>422</b>	<b>86.8</b>	422	86.8	422	87.2	4	<b>422</b>	<b>86.8</b>	422	86.8	422	87.2
434.zeusmp	4	606	60.0	<b>608</b>	<b>60.0</b>	608	60.0	4	606	60.0	<b>608</b>	<b>60.0</b>	608	60.0
435.gromacs	4	<b>647</b>	<b>44.0</b>	646	44.4	647	44.0	4	<b>647</b>	<b>44.0</b>	646	44.4	647	44.0
436.cactusADM	4	794	60.4	798	60.0	<b>794</b>	<b>60.4</b>	4	794	60.4	798	60.0	<b>794</b>	<b>60.4</b>
437.leslie3d	4	761	49.6	<b>760</b>	<b>49.6</b>	756	49.6	4	761	49.6	<b>760</b>	<b>49.6</b>	756	49.6
444.namd	4	742	43.2	740	43.2	<b>741</b>	<b>43.2</b>	4	734	43.6	735	43.6	<b>734</b>	<b>43.6</b>
447.dealII	4	760	60.0	<b>763</b>	<b>60.0</b>	775	59.2	4	<b>757</b>	<b>60.4</b>	760	60.4	753	60.8
450.soplex	4	644	51.6	644	52.0	<b>644</b>	<b>52.0</b>	2	374	44.6	381	43.8	<b>375</b>	<b>44.6</b>
453.povray	4	<b>301</b>	<b>70.8</b>	300	70.8	301	70.8	4	251	84.8	252	84.4	<b>252</b>	<b>84.4</b>
454.calculix	4	501	66.0	<b>499</b>	<b>66.0</b>	498	66.4	4	501	66.0	<b>499</b>	<b>66.0</b>	498	66.4
459.GemsFDTD	4	1051	40.4	<b>1051</b>	<b>40.4</b>	1050	40.4	4	1051	40.4	<b>1051</b>	<b>40.4</b>	1050	40.4
465.tonto	4	709	55.6	<b>704</b>	<b>56.0</b>	703	56.0	4	693	56.8	700	56.4	<b>693</b>	<b>56.8</b>
470.lbm	4	521	106	<b>521</b>	<b>106</b>	521	106	4	521	106	<b>521</b>	<b>106</b>	521	106
481.wrf	4	497	90.0	499	89.6	<b>497</b>	<b>90.0</b>	4	497	90.0	499	89.6	<b>497</b>	<b>90.0</b>
482.sphinx3	4	1148	68.0	1146	68.0	<b>1148</b>	<b>68.0</b>	4	<b>1214</b>	<b>64.4</b>	1213	64.4	1214	64.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
 The start command with the /affinity switch was used to bind processes to cores

## General Notes

Tested systems can be used with Shin-G ATX case,  
 PC Power and Cooling 1200W power supply

## Base Compiler Invocation

C benchmarks:  
 icl -Qvc9 -Qstd=c99

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## Base Compiler Invocation (Continued)

C++ benchmarks:

icl -Qvc9

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icl -Qvc9 -Qstd=c99 ifort

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_P64 -names:lowercase  
 416.gamess: -DSPEC\_CPU\_P64  
 433.milc: -DSPEC\_CPU\_P64  
 434.zeusmp: -DSPEC\_CPU\_P64  
 435.gromacs: -DSPEC\_CPU\_P64  
 436.cactusADM: -DSPEC\_CPU\_P64 /names:lowercase /assume:underscore  
 437.leslie3d: -DSPEC\_CPU\_P64  
 444.namd: -DSPEC\_CPU\_P64 /TP  
 447.dealII: -DSPEC\_CPU\_P64 -DDEAL\_II\_MEMBER\_VAR\_SPECIALIZATION\_BUG  
 450.soplex: -DSPEC\_CPU\_P64  
 453.povray: -DSPEC\_CPU\_P64 -DSPEC\_CPU\_WINDOWS\_ICL  
 454.calculix: -DSPEC\_CPU\_P64 -DSPEC\_CPU\_NOZMODIFIER /names:lowercase  
 459.GemsFDTD: -DSPEC\_CPU\_P64  
 465.tonto: -DSPEC\_CPU\_P64  
 470.lbm: -DSPEC\_CPU\_P64  
 481.wrf: -DSPEC\_CPU\_P64 -DSPEC\_CPU\_WINDOWS\_ICL  
 482.sphinx3: -DSPEC\_CPU\_P64

## Base Optimization Flags

C benchmarks:

-QxAVX -Qipo -O3 -Qprec-div- -Qansi-alias -Qauto-ilp32 /F1000000000  
-link /FORCE:MULTIPLE

C++ benchmarks:

-QxAVX -Qipo -O3 -Qprec-div- -Qansi-alias -Qcxx-features  
-Qauto-ilp32 /F1000000000 shlw64M.lib -link /FORCE:MULTIPLE

Fortran benchmarks:

-QxAVX -Qipo -O3 -Qprec-div- -Qansi-alias /F1000000000  
-link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:

-QxAVX -Qipo -O3 -Qprec-div- -Qansi-alias -Qauto-ilp32 /F1000000000  
-link /FORCE:MULTIPLE



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## Peak Compiler Invocation

C benchmarks:

icl -Qvc9 -Qstd=c99

C++ benchmarks:

icl -Qvc9

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icl -Qvc9 -Qstd=c99 ifort

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: -QxAVX -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias  
-Qauto-ilp32 /F1000000000 -link /FORCE:MULTIPLE

C++ benchmarks:

444.namd: -QxAVX(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -Qipo  
-O3 -Qprec-div- -Oa -Qauto-ilp32 /F1000000000 sh1W64M.lib  
-link /FORCE:MULTIPLE

447.dealII: -QxAVX(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -Qipo  
-O3 -Qprec-div- -Qunroll2 -Qansi-alias -Qscalar-rep-  
-Qauto-ilp32 /F1000000000 sh1W64M.lib  
-link /FORCE:MULTIPLE

450.soplex: -QxAVX(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -Qipo  
-O3 -Qauto-ilp32 /F1000000000 sh1W64M.lib  
-link /FORCE:MULTIPLE

453.povray: -QxAVX(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -Qipo  
-O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32 /F1000000000  
sh1W64M.lib -link /FORCE:MULTIPLE

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## Peak Optimization Flags (Continued)

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -QxAVX(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -Qipo  
-O3 -Qprec-div- -Qunroll4 -Qauto /F1000000000  
-link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revB.html>

<http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revB.xml>

<http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.

Report generated on Wed Jul 23 20:50:14 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 10 May 2011.