



# SPEC<sup>®</sup> CFP2006 Result

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Dell Inc.

SPECfp<sup>®</sup>\_rate2006 = 556

PowerEdge R910 (Intel Xeon X7550, 2.00 GHz)

SPECfp\_rate\_base2006 = 534

CPU2006 license: 55

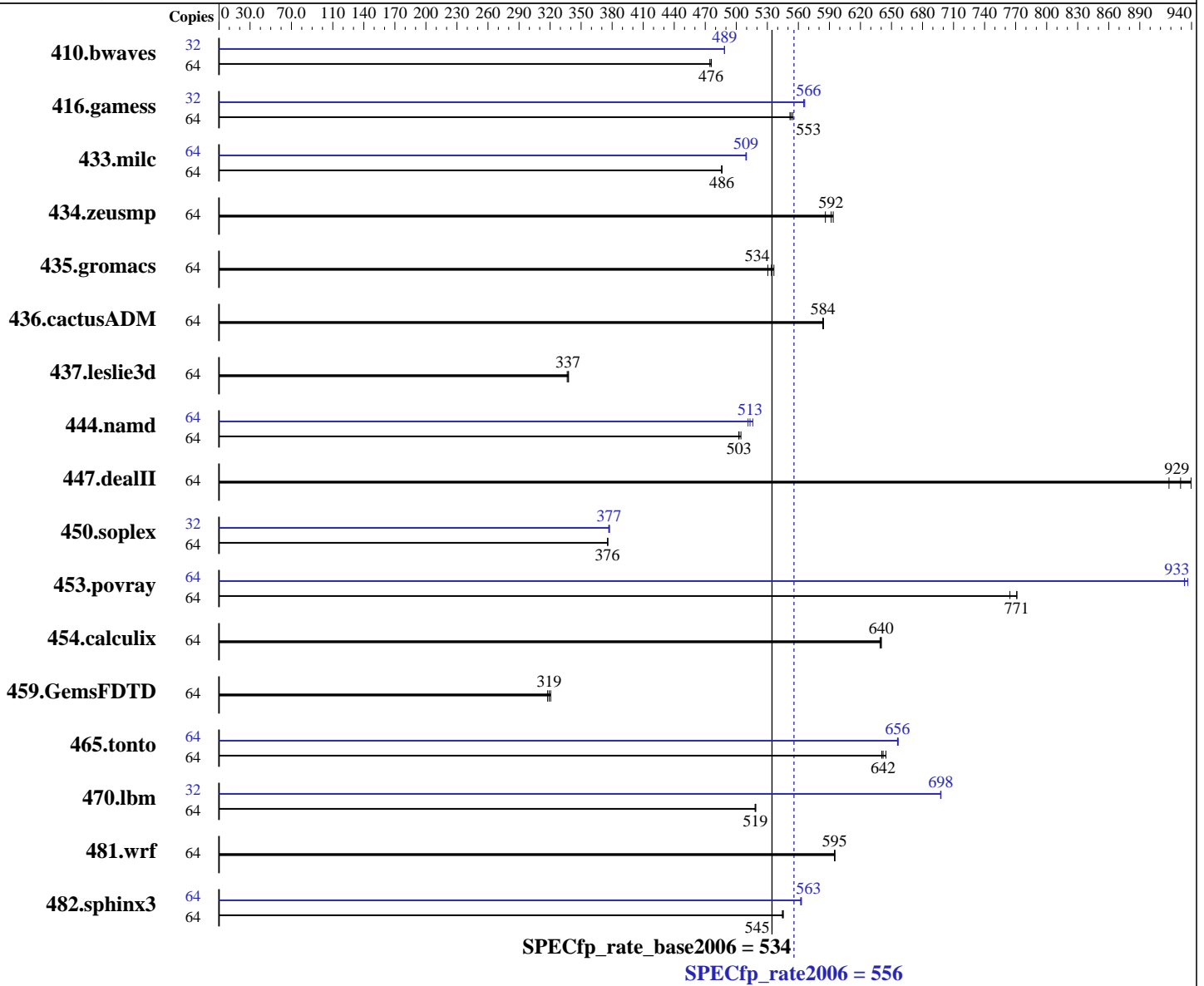
Test date: Apr-2011

Test sponsor: Dell Inc.

Hardware Availability: Jul-2011

Tested by: Dell Inc.

Software Availability: Jan-2011



### Hardware

CPU Name: Intel Xeon X7550  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.40 GHz  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 32 cores, 4 chips, 8 cores/chip, 2 threads/core  
 CPU(s) orderable: 2,4 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86\_64), Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit

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L3 Cache: 18 MB I+D on chip per chip  
Other Cache: None  
Memory: 512 GB (64 x 8 GB 4Rx8 PC3-8500R-7, ECC)  
Disk Subsystem: 1 x 500 GB 7200 RPM SAS 6Gb  
Other Hardware: None

Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	64	<b>1829</b>	<b>476</b>	1833	474	1827	476	32	<b>890</b>	<b>489</b>	890	489	890	489
416.gamess	64	<b>2266</b>	<b>553</b>	2271	552	2260	555	32	<b>1108</b>	<b>566</b>	1109	565	1107	566
433.milc	64	1209	486	<b>1209</b>	<b>486</b>	1209	486	64	1152	510	<b>1153</b>	<b>509</b>	1153	509
434.zeusmp	64	<b>984</b>	<b>592</b>	981	594	994	586	64	<b>984</b>	<b>592</b>	981	594	994	586
435.gromacs	64	<b>856</b>	<b>534</b>	852	536	861	530	64	<b>856</b>	<b>534</b>	852	536	861	530
436.cactusADM	64	1310	584	<b>1310</b>	<b>584</b>	1309	584	64	1310	584	<b>1310</b>	<b>584</b>	1309	584
437.leslie3d	64	1780	338	1787	337	<b>1783</b>	<b>337</b>	64	1780	338	1787	337	<b>1783</b>	<b>337</b>
444.namd	64	1017	504	<b>1021</b>	<b>503</b>	1022	502	64	995	516	<b>1000</b>	<b>513</b>	1004	511
447.dealII	64	779	940	<b>788</b>	<b>929</b>	797	918	64	779	940	<b>788</b>	<b>929</b>	797	918
450.soplex	64	1420	376	<b>1421</b>	<b>376</b>	1421	375	32	708	377	707	378	<b>708</b>	<b>377</b>
453.povray	64	445	764	441	771	<b>442</b>	<b>771</b>	64	<b>365</b>	<b>933</b>	364	936	365	933
454.calculix	64	825	640	826	639	<b>825</b>	<b>640</b>	64	825	640	826	639	<b>825</b>	<b>640</b>
459.GemsFDTD	64	2138	318	2118	321	<b>2128</b>	<b>319</b>	64	2138	318	2118	321	<b>2128</b>	<b>319</b>
465.tonto	64	977	645	<b>981</b>	<b>642</b>	983	641	64	959	657	<b>960</b>	<b>656</b>	960	656
470.lbm	64	<b>1696</b>	<b>519</b>	1695	519	1697	518	32	630	698	630	698	<b>630</b>	<b>698</b>
481.wrf	64	1201	595	<b>1201</b>	<b>595</b>	1202	595	64	1201	595	<b>1201</b>	<b>595</b>	1202	595
482.sphinx3	64	2291	545	<b>2289</b>	<b>545</b>	2287	545	64	2216	563	2219	562	<b>2217</b>	<b>563</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 57600 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```



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## Platform Notes

BIOS Settings:

Power Management = Maximum Performance (Default = Active Power Controller)

## General Notes

The Dell PowerEdge R910 and the Bull NovaScale R480 F2 models are electronically equivalent. The results have been measured on a Dell PowerEdge R910 model. Binaries were compiled on RHEL5.5

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64



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## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

470.lbm: -DSPEC\_CPU\_LP64

481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3  
-ansi-alias -opt-prefetch -static -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

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## Peak Optimization Flags (Continued)

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto  
-inline-calloc -opt-malloc-options=3  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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