



# SPEC<sup>®</sup> CFP2006 Result

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## Bull SAS

SPECfp<sup>®</sup>\_rate2006 = 463

NovaScale R480 F2 (Intel Xeon X7542, 2.67 GHz)

SPECfp\_rate\_base2006 = 428

CPU2006 license: 20

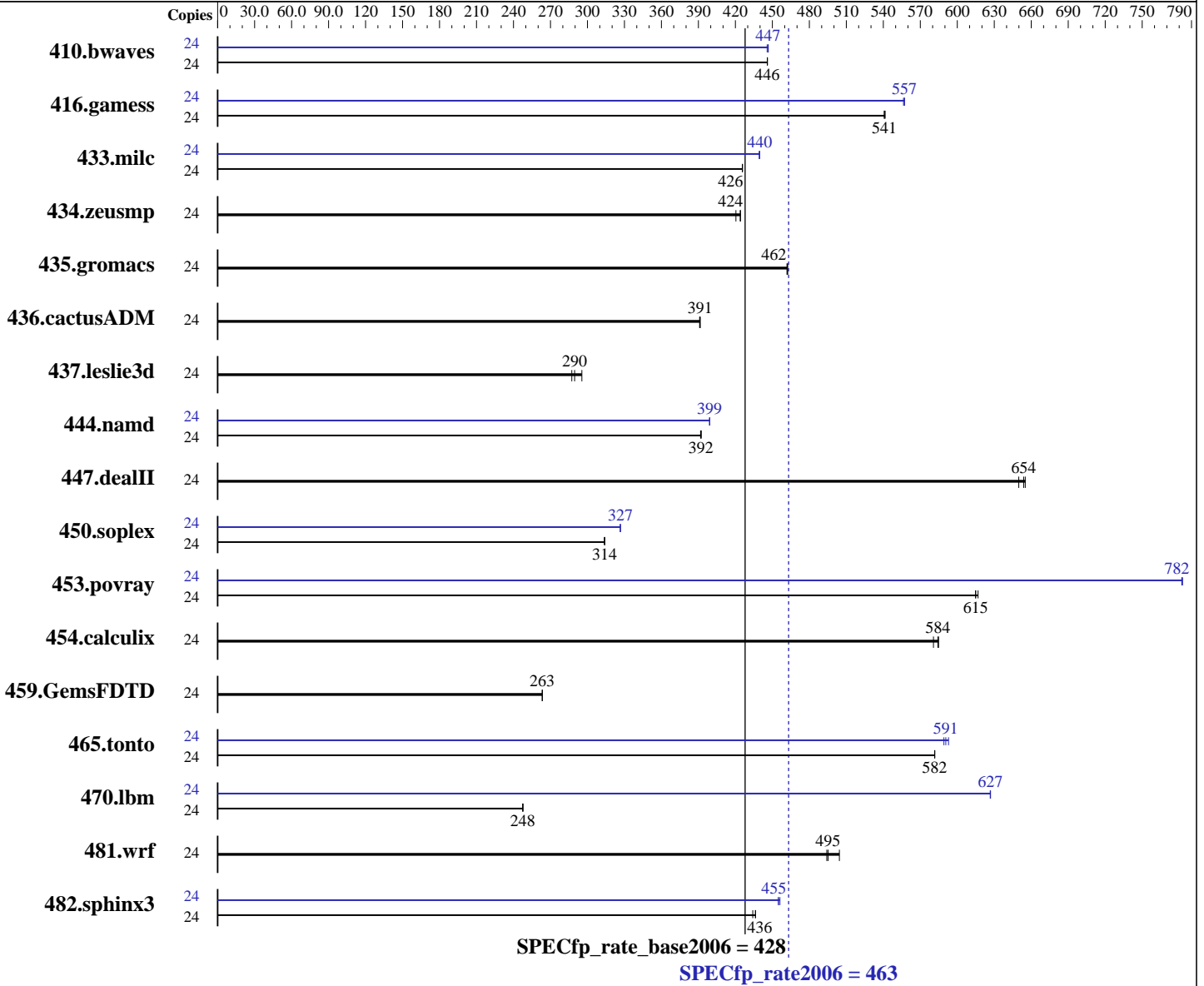
Test sponsor: Bull SAS

Tested by: Dell Inc.

Test date: Apr-2011

Hardware Availability: Jul-2011

Software Availability: Jan-2011



### Hardware

CPU Name: Intel Xeon X7542  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz  
 CPU MHz: 2666  
 FPU: Integrated  
 CPU(s) enabled: 24 cores, 4 chips, 6 cores/chip  
 CPU(s) orderable: 2,4 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86\_64), Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit

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L3 Cache: 18 MB I+D on chip per chip  
Other Cache: None  
Memory: 512 GB (64 x 8 GB 4Rx8 PC3-8500R-7, ECC, running at 978 MHz)  
Disk Subsystem: 1 x 500 GB 7200 RPM SAS 6Gb  
Other Hardware: None

Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	24	732	446	731	446	<b>731</b>	<b>446</b>	24	730	447	732	446	<b>730</b>	<b>447</b>		
416.gamess	24	868	541	869	541	<b>869</b>	<b>541</b>	24	845	556	843	557	<b>844</b>	<b>557</b>		
433.milc	24	518	426	<b>518</b>	<b>426</b>	517	426	24	501	440	<b>501</b>	<b>440</b>	502	439		
434.zeusmp	24	519	420	515	424	<b>515</b>	<b>424</b>	24	519	420	515	424	<b>515</b>	<b>424</b>		
435.gromacs	24	371	462	<b>371</b>	<b>462</b>	371	462	24	371	462	<b>371</b>	<b>462</b>	371	462		
436.cactusADM	24	733	391	<b>733</b>	<b>391</b>	734	391	24	733	391	<b>733</b>	<b>391</b>	734	391		
437.leslie3d	24	764	295	785	287	<b>779</b>	<b>290</b>	24	764	295	785	287	<b>779</b>	<b>290</b>		
444.namd	24	491	392	<b>491</b>	<b>392</b>	491	392	24	482	399	<b>482</b>	<b>399</b>	482	399		
447.dealII	24	422	650	419	655	<b>420</b>	<b>654</b>	24	422	650	419	655	<b>420</b>	<b>654</b>		
450.soplex	24	638	314	637	314	<b>637</b>	<b>314</b>	24	<b>613</b>	<b>327</b>	613	327	613	327		
453.povray	24	207	617	<b>208</b>	<b>615</b>	208	615	24	163	782	<b>163</b>	<b>782</b>	163	783		
454.calculix	24	<b>339</b>	<b>584</b>	341	581	339	585	24	<b>339</b>	<b>584</b>	341	581	339	585		
459.GemsFDTD	24	967	263	967	263	<b>967</b>	<b>263</b>	24	967	263	967	263	<b>967</b>	<b>263</b>		
465.tonto	24	406	582	406	582	<b>406</b>	<b>582</b>	24	401	589	<b>400</b>	<b>591</b>	398	593		
470.lbm	24	1332	248	<b>1332</b>	<b>248</b>	1331	248	24	526	627	526	627	<b>526</b>	<b>627</b>		
481.wrf	24	532	504	543	494	<b>541</b>	<b>495</b>	24	532	504	543	494	<b>541</b>	<b>495</b>		
482.sphinx3	24	1072	436	1078	434	<b>1072</b>	<b>436</b>	24	1028	455	<b>1028</b>	<b>455</b>	1025	456		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 21600 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```



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### Platform Notes

BIOS Settings:  
Power Management = Maximum Performance (Default = Active Power Controller)

### General Notes

The Dell PowerEdge R910 and the Bull NovaScale R480 F2 models are electronically equivalent. The results have been measured on a Dell PowerEdge R910 model. Binaries were compiled on RHEL5.5

### Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

### Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64



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## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

Fortran benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):  
icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

470.lbm: -DSPEC\_CPU\_LP64

481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3  
-ansi-alias -opt-prefetch -static -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

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## Peak Optimization Flags (Continued)

```
465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT
```

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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