



# SPEC® CINT2006 Result

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## Fujitsu

PRIMERGY RX900 S2, Intel Xeon E7-8830, 2.13 GHz

**SPECint\_rate2006 = 1410**

**SPECint\_rate\_base2006 = 1320**

CPU2006 license: 19

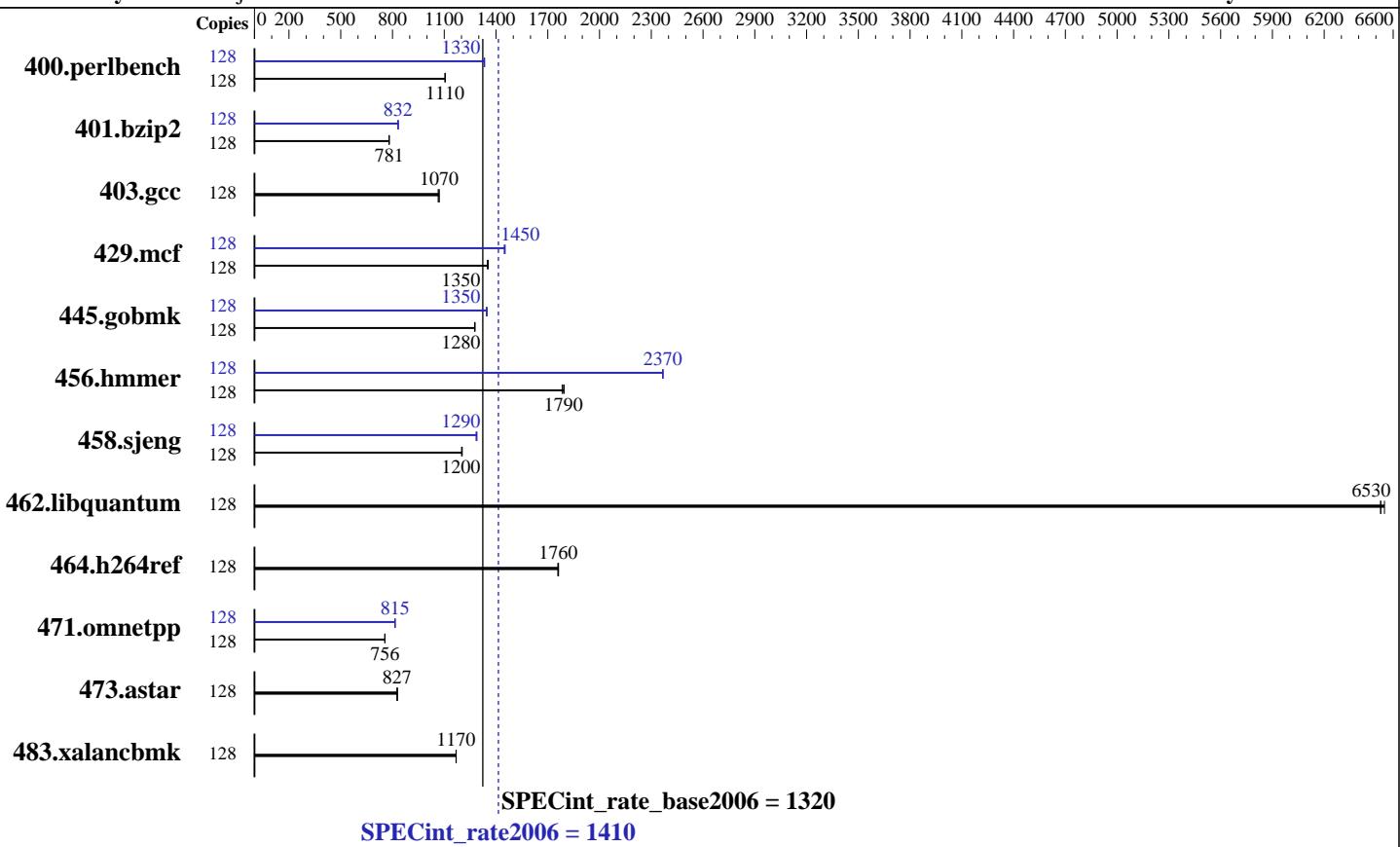
Test sponsor: Fujitsu

Tested by: Fujitsu

**Test date:** May-2011

**Hardware Availability:** Jun-2011

**Software Availability:** Jan-2011



## Hardware

CPU Name:	Intel Xeon E7-8830
CPU Characteristics:	Intel Turbo Boost Technology up to 2.40 GHz
CPU MHz:	2133
FPU:	Integrated
CPU(s) enabled:	64 cores, 8 chips, 8 cores/chip, 2 threads/core
CPU(s) orderable:	4,6,8 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	24 MB I+D on chip per chip
Other Cache:	None
Memory:	1 TB (128 x 8 GB 4Rx8 PC3-10600R-9, ECC, running at 1066 MHz)
Disk Subsystem:	2 x 147 GB (SAS, 15000RPM, RAID0)
Other Hardware:	None

## Software

Operating System:	SUSE Linux Enterprise Server 11 SP1(x86_64), Kernel 2.6.32.12-0.7-default
Compiler:	Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116
Auto Parallel:	No
File System:	ext2
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	128	1131	1110	<b>1131</b>	<b>1110</b>	1132	1100	128	<b>938</b>	<b>1330</b>	939	1330	938	1330
401.bzip2	128	1583	780	<b>1582</b>	<b>781</b>	1582	781	128	<b>1484</b>	<b>832</b>	1482	834	1485	832
403.gcc	128	968	1060	<b>963</b>	<b>1070</b>	963	1070	128	968	1060	<b>963</b>	<b>1070</b>	963	1070
429.mcf	128	<b>863</b>	<b>1350</b>	862	1350	865	1350	128	806	1450	<b>805</b>	<b>1450</b>	803	1450
445.gobmk	128	<b>1052</b>	<b>1280</b>	1050	1280	1053	1280	128	996	1350	<b>997</b>	<b>1350</b>	999	1340
456.hammer	128	665	1790	<b>667</b>	<b>1790</b>	670	1780	128	<b>504</b>	<b>2370</b>	505	2370	504	2370
458.sjeng	128	1290	1200	1286	1200	<b>1289</b>	<b>1200</b>	128	<b>1204</b>	<b>1290</b>	1201	1290	1205	1280
462.libquantum	128	406	6530	<b>406</b>	<b>6530</b>	405	6550	128	406	6530	<b>406</b>	<b>6530</b>	405	6550
464.h264ref	128	1611	1760	1608	1760	<b>1608</b>	<b>1760</b>	128	1611	1760	1608	1760	<b>1608</b>	<b>1760</b>
471.omnetpp	128	1059	756	1058	756	<b>1058</b>	<b>756</b>	128	981	815	<b>982</b>	<b>815</b>	983	814
473.astar	128	1084	829	1087	827	<b>1087</b>	<b>827</b>	128	1084	829	1087	827	<b>1087</b>	<b>827</b>
483.xalancbmk	128	<b>756</b>	<b>1170</b>	756	1170	755	1170	128	<b>756</b>	<b>1170</b>	756	1170	755	1170

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

Large pages were not enabled for this run

The following command was used prior to run  
ulimit -s unlimited  
echo 1 > /proc/sys/vm/zone\_reclaim\_mode

## General Notes

Binaries were compiled on RHEL5.5

For information about Fujitsu please visit: <http://www.fujitsu.com>

## Base Compiler Invocation

C benchmarks:  
icc -m32

C++ benchmarks:  
icpc -m32



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch  
-B /usr/share/libhugetlbfsl -Wl,-hugetlbfsl-link=BDT
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/smartheap -lsmartheap  
-B /usr/share/libhugetlbfsl -Wl,-hugetlbfsl-link=BDT
```

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

456.hmmr: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT  
  
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT  
  
403.gcc: basepeak = yes  
  
429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -auto-ilp32  
  
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -auto-ilp32  
  
456.hmmr: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT  
  
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll14 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT  
  
462.libquantum: basepeak = yes  
  
464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/smartheap -lsmartheap  
  
473.astar: basepeak = yes

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## Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.xml>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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