



SPEC® CFP2006 Result

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Oracle Corporation

SPECfp®_rate2006 = 219

Sun Fire X4170 M2 (Intel Xeon X5649 2.53 GHz)

SPECfp_rate_base2006 = 212

CPU2006 license: 6

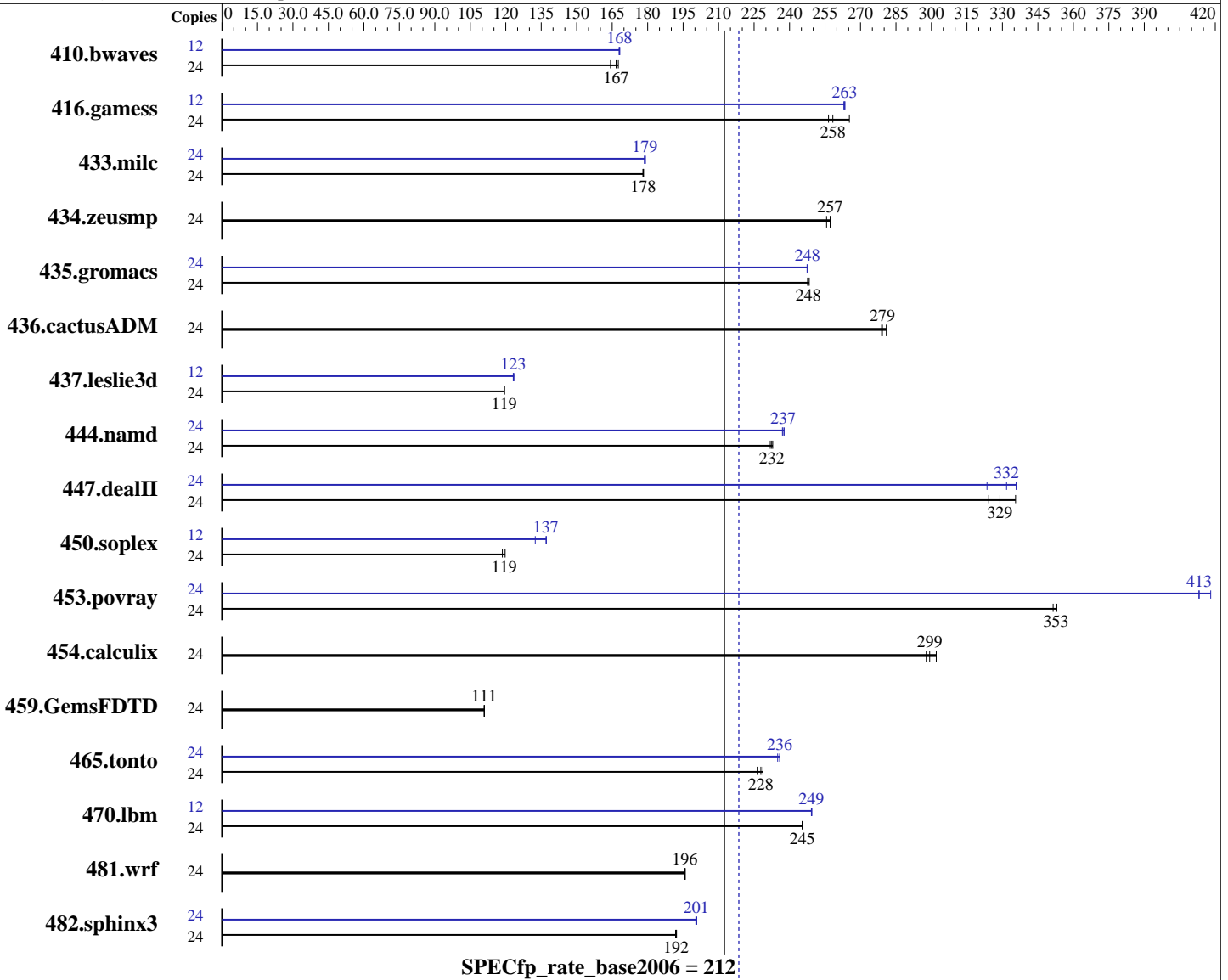
Test sponsor: Oracle Corporation

Tested by: Oracle Corporation

Test date: May-2011

Hardware Availability: Mar-2011

Software Availability: Nov-2010



Hardware

CPU Name: Intel Xeon E5649
 CPU Characteristics: Intel Turbo Boost Technology up to 2.93 GHz
 CPU MHz: 2533
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: Oracle Linux 5.5
 kernel 2.6.18-194.el5
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE
 for applications running on Intel 64
 Version 12.0.1.116 Build 20101116
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit

Continued on next page



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L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem: 1 x 300 GB 10000 RPM SAS2
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: None

Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---------------|--------|-------------|------------|-------------|------------|-------------|------------|--------|-------------|------------|-------------|------------|-------------|------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 24 | 1985 | 164 | 1946 | 168 | <u>1957</u> | <u>167</u> | 12 | 969 | 168 | 971 | 168 | <u>970</u> | <u>168</u> |
| 416.gamess | 24 | 1771 | 265 | 1832 | 256 | <u>1819</u> | <u>258</u> | 12 | <u>893</u> | <u>263</u> | 894 | 263 | 892 | 263 |
| 433.milc | 24 | 1236 | 178 | 1238 | 178 | <u>1237</u> | <u>178</u> | 24 | <u>1232</u> | <u>179</u> | 1230 | 179 | 1234 | 179 |
| 434.zeusmp | 24 | <u>849</u> | <u>257</u> | 854 | 256 | 849 | 257 | 24 | <u>849</u> | <u>257</u> | 854 | 256 | 849 | 257 |
| 435.gromacs | 24 | 690 | 248 | <u>691</u> | <u>248</u> | 692 | 248 | 24 | 692 | 248 | 693 | 247 | <u>692</u> | <u>248</u> |
| 436.cactusADM | 24 | 1021 | 281 | <u>1027</u> | <u>279</u> | 1028 | 279 | 24 | 1021 | 281 | <u>1027</u> | <u>279</u> | 1028 | 279 |
| 437.leslie3d | 24 | 1890 | 119 | 1889 | 119 | <u>1890</u> | <u>119</u> | 12 | <u>915</u> | <u>123</u> | 916 | 123 | 914 | 123 |
| 444.namd | 24 | 826 | 233 | <u>828</u> | <u>232</u> | 830 | 232 | 24 | 812 | 237 | 810 | 238 | <u>811</u> | <u>237</u> |
| 447.dealII | 24 | 847 | 324 | <u>835</u> | <u>329</u> | 818 | 336 | 24 | 849 | 323 | <u>828</u> | <u>332</u> | 818 | 336 |
| 450.soplex | 24 | 1688 | 119 | 1672 | 120 | <u>1677</u> | <u>119</u> | 12 | 755 | 132 | 730 | 137 | <u>730</u> | <u>137</u> |
| 453.povray | 24 | <u>362</u> | <u>353</u> | 362 | 353 | 363 | 351 | 24 | 309 | 413 | 305 | 418 | <u>309</u> | <u>413</u> |
| 454.calculix | 24 | 656 | 302 | <u>662</u> | <u>299</u> | 665 | 298 | 24 | 656 | 302 | <u>662</u> | <u>299</u> | 665 | 298 |
| 459.GemsFDTD | 24 | 2296 | 111 | 2295 | 111 | <u>2296</u> | <u>111</u> | 24 | 2296 | 111 | 2295 | 111 | <u>2296</u> | <u>111</u> |
| 465.tonto | 24 | 1033 | 229 | 1043 | 226 | <u>1036</u> | <u>228</u> | 24 | <u>1002</u> | <u>236</u> | 1005 | 235 | 1001 | 236 |
| 470.lbm | 24 | <u>1344</u> | <u>245</u> | 1344 | 245 | 1344 | 245 | 12 | 662 | 249 | <u>662</u> | <u>249</u> | 661 | 249 |
| 481.wrf | 24 | 1368 | 196 | 1370 | 196 | <u>1369</u> | <u>196</u> | 24 | 1368 | 196 | 1370 | 196 | <u>1369</u> | <u>196</u> |
| 482.sphinx3 | 24 | 2434 | 192 | <u>2435</u> | <u>192</u> | 2439 | 192 | 24 | 2334 | 200 | 2330 | 201 | <u>2332</u> | <u>201</u> |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
Hugepages was enabled with the following:
'nodev /mnt/hugepages hugetlbfs defaults 0 0' added to /etc/fstab
echo 10800 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so



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Platform Notes

Load Default BIOS Settings and then change the following
Hardware Prefetch Enabled
Adjacent Cache Line Prefetch Enabled
L1 Data Prefetch Enabled
Data Reuse Optimization Disabled

General Notes

Binaries were compiled on RHEL5.5 with Binutils binutils-2.17.50.0.6-14.el5

This result is measured on Sun Fire X4170 M2 server.
Note that the Sun Fire X4170 M2 server and Sun Fire X4270 M2 server are electrically equivalent.

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Continued on next page



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Base Portability Flags (Continued)

482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64

416.gamess: -DSPEC_CPU_LP64

433.milc: -DSPEC_CPU_LP64

434.zeusmp: -DSPEC_CPU_LP64

435.gromacs: -DSPEC_CPU_LP64 -nofor_main

436.cactusADM: -DSPEC_CPU_LP64 -nofor_main

437.leslie3d: -DSPEC_CPU_LP64

Continued on next page

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Page 4



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Peak Portability Flags (Continued)

```

444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

```

Peak Optimization Flags

C benchmarks:

```

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
         -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
         -no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
         -ansi-alias -opt-prefetch -static -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2

```

C++ benchmarks:

```

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
         -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
         -auto-ilp32

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
         -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
         -no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
         -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
         -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias
         -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

```

Fortran benchmarks:

```

410.bwaves: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
         -no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
         -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
         -inline-level=0 -scalar-rep- -static

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Peak Optimization Flags (Continued)

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

459.GemsFDTD: basepeak = yes

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
-static -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>
http://www.spec.org/cpu2006/flags/Oracle-platform-x86_64.20110622.html

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>
http://www.spec.org/cpu2006/flags/Oracle-platform-x86_64.20110622.xml

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