# SPEC® CFP2006 Result

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## Dell Inc.

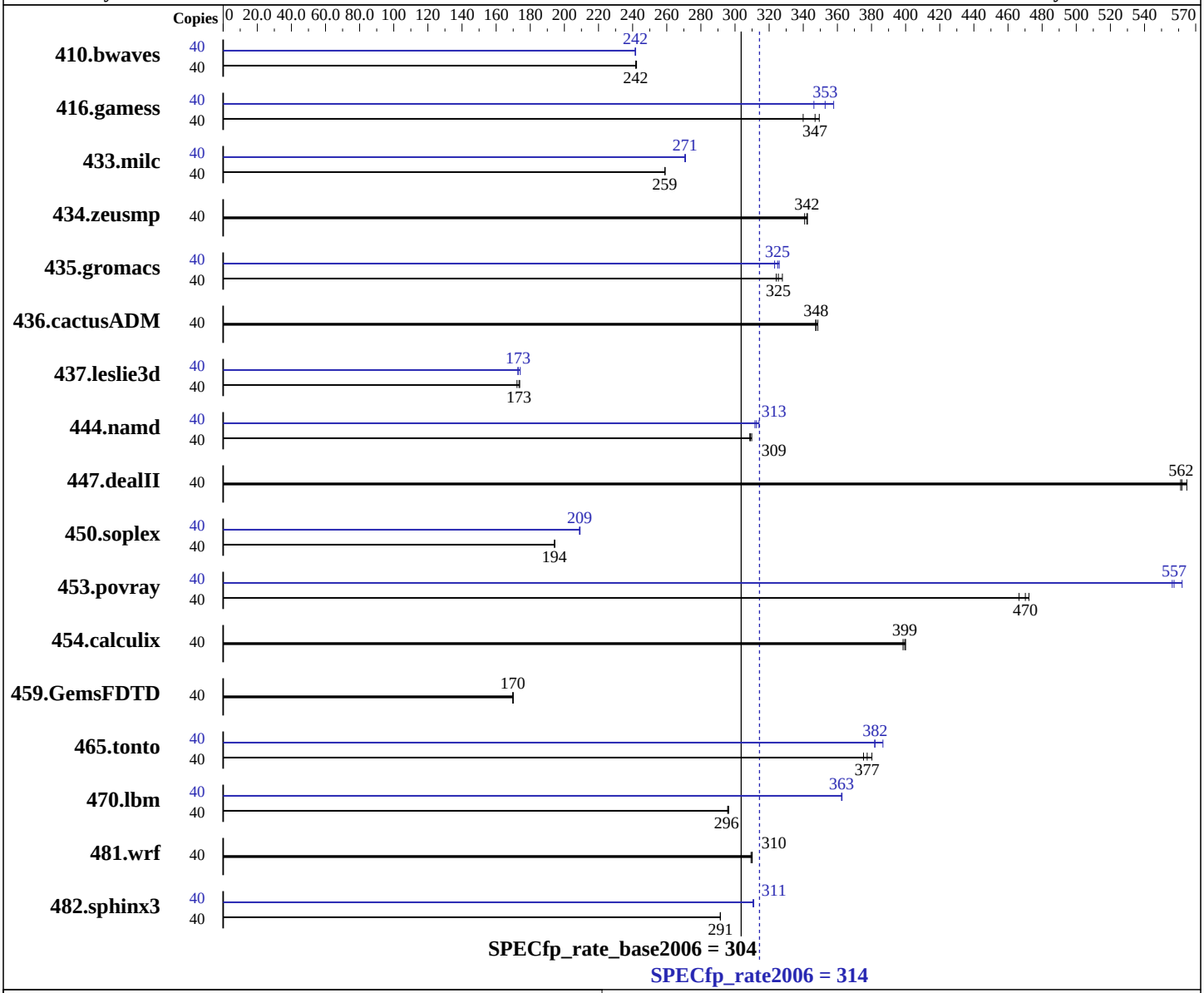
PowerEdge R810 (Intel Xeon E7-2850, 2.00 GHz)

SPECfp®_rate2006 = 314

SPECfp_rate_base2006 = 304

**CPU2006 license:** 55
**Test sponsor:** Dell Inc.
**Tested by:** Dell Inc.

**Test date:** Mar-2011
**Hardware Availability:** Apr-2011
**Software Availability:** Jan-2011




## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E7-2850 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 2.40 GHz |
| CPU MHz: | 2000 |
| FPU: | Integrated |
| CPU(s) enabled: | 20 cores, 2 chips, 10 cores/chip, 2 threads/core |
| CPU(s) orderable: | 2,4 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |

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## Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default |
| Compiler: | Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116 |
| Auto Parallel: | No |
| File System: | ext3 |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 64-bit |

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L3 Cache: 24 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (32 x 8 GB 4Rx8 PC3L-8500R-7, ECC)
Disk Subsystem: 2 x 300 GB 10000 RPM SAS, RAID0
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: None

## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 40 | 2243 | 242 | 2248 | 242 | **2248** | **242** | 40 | 2249 | 242 | **2250** | **242** | 2250 | 242 |
| 416.gamess | 40 | 2242 | 349 | **2258** | **347** | 2304 | 340 | 40 | **2220** | **353** | 2189 | 358 | 2262 | 346 |
| 433.milc | 40 | 1417 | 259 | **1418** | **259** | 1419 | 259 | 40 | 1355 | 271 | 1357 | 271 | **1356** | **271** |
| 434.zeusmp | 40 | 1063 | 342 | 1068 | 341 | **1064** | **342** | 40 | 1063 | 342 | 1068 | 341 | **1064** | **342** |
| 435.gromacs | 40 | 871 | 328 | 881 | 324 | **878** | **325** | 40 | **879** | **325** | 876 | 326 | 884 | 323 |
| 436.cactusADM | 40 | **1376** | **348** | 1376 | 347 | 1371 | 349 | 40 | **1376** | **348** | 1376 | 347 | 1371 | 349 |
| 437.leslie3d | 40 | **2168** | **173** | 2163 | 174 | 2183 | 172 | 40 | **2174** | **173** | 2159 | 174 | 2177 | 173 |
| 444.namd | 40 | 1036 | 310 | **1038** | **309** | 1039 | 309 | 40 | 1021 | 314 | **1026** | **313** | 1029 | 312 |
| 447.dealII | 40 | 815 | 561 | **814** | **562** | 810 | 565 | 40 | 815 | 561 | **814** | **562** | 810 | 565 |
| 450.soplex | 40 | **1717** | **194** | 1717 | 194 | 1718 | 194 | 40 | 1595 | 209 | 1597 | 209 | **1596** | **209** |
| 453.povray | 40 | 451 | 472 | 456 | 466 | **453** | **470** | 40 | 379 | 562 | **382** | **557** | 383 | 556 |
| 454.calculix | 40 | **826** | **399** | 825 | 400 | 828 | 398 | 40 | **826** | **399** | 825 | 400 | 828 | 398 |
| 459.GemsFDTD | 40 | 2496 | 170 | 2501 | 170 | **2498** | **170** | 40 | 2496 | 170 | 2501 | 170 | **2498** | **170** |
| 465.tonto | 40 | **1043** | **377** | 1049 | 375 | 1035 | 380 | 40 | **1030** | **382** | 1018 | 387 | 1031 | 382 |
| 470.lbm | 40 | 1859 | 296 | **1857** | **296** | 1855 | 296 | 40 | 1515 | 363 | **1516** | **363** | 1516 | 363 |
| 481.wrf | 40 | **1441** | **310** | 1441 | 310 | 1444 | 309 | 40 | **1441** | **310** | 1441 | 310 | 1444 | 309 |
| 482.sphinx3 | 40 | **2675** | **291** | 2676 | 291 | 2675 | 291 | 40 | 2510 | 311 | **2509** | **311** | 2507 | 311 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

## Operating System Notes

```
 'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 18000> /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

## Platform Notes

```
BIOS Settings:
Power Management = Maximum Performance (Default = Active Power Controller)
```

## General Notes

```
Binaries were compiled on RHEL5.5
```

## Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

## Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
    435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
     444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
     465.tonto: -DSPEC_CPU_LP64
      470.lbm: -DSPEC_CPU_LP64
      481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
  482.sphinx3: -DSPEC_CPU_LP64
```

## Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias
```

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## Base Optimization Flags (Continued)

C++ benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias`

Fortran benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static`

Benchmarks using both Fortran and C:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias`

## Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m64`

482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):
`icpc -m64`

450.soplex: `icpc -m32`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

## Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
470.lbm: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3 -ansi-alias -opt-prefetch -static -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3 -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

459.GemsFDTD: basepeak = yes

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto -inline-calloc -opt-malloc-options=3 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Benchmarks using both Fortran and C:

Continued on next page

## Peak Optimization Flags (Continued)

435.gromacs: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-opt-prefetch` `-static -auto-ilp32`

436.cactusADM: `basepeak = yes`

454.calculix: `basepeak = yes`

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html`
`http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml`
`http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.xml`


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