



SPEC[®] CINT2006 Result

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Acer Incorporated

SPECint[®]_rate2006 = 314

Acer AR380 F1 (Intel Xeon E5645)

SPECint_rate_base2006 = 294

CPU2006 license: 97

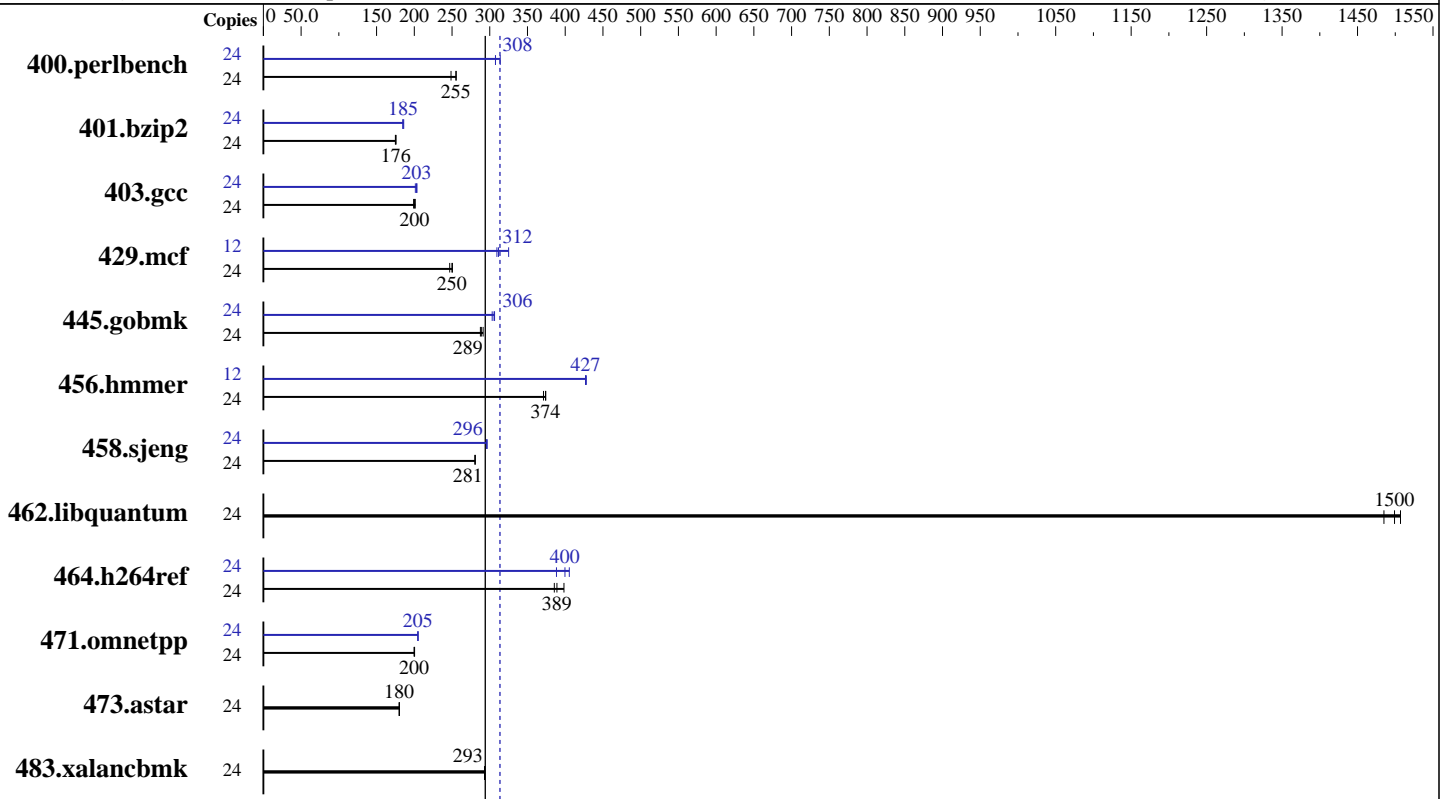
Test date: May-2011

Test sponsor: Acer Incorporated

Hardware Availability: Feb-2011

Tested by: Acer Incorporated

Software Availability: Jan-2011



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Hardware

CPU Name: Intel Xeon E5645
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (12 x 4 GB 2Rx8 PC3-10600R-9, ECC)
 Disk Subsystem: 1 x 300 GB SATA, 10000 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise server 11 (x86_64) SP1, Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



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Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|----------------|--------|-------------|------------|-------------|-------------|-------------|------------|--------|-------------|------------|------------|-------------|------------|------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 24 | 943 | 249 | 917 | 256 | <u>919</u> | <u>255</u> | 24 | <u>762</u> | <u>308</u> | 762 | 308 | 747 | 314 |
| 401.bzip2 | 24 | 1324 | 175 | <u>1319</u> | <u>176</u> | 1319 | 176 | 24 | <u>1251</u> | <u>185</u> | 1253 | 185 | 1245 | 186 |
| 403.gcc | 24 | 960 | 201 | 970 | 199 | <u>965</u> | <u>200</u> | 24 | 949 | 204 | <u>952</u> | <u>203</u> | 959 | 201 |
| 429.mcf | 24 | 886 | 247 | 874 | 251 | <u>876</u> | <u>250</u> | 12 | <u>351</u> | <u>312</u> | 354 | 309 | 337 | 325 |
| 445.gobmk | 24 | 865 | 291 | 875 | 288 | <u>872</u> | <u>289</u> | 24 | 822 | 306 | <u>824</u> | <u>306</u> | 830 | 303 |
| 456.hammer | 24 | 603 | 371 | <u>599</u> | <u>374</u> | 598 | 374 | 12 | 262 | 427 | 262 | 428 | <u>262</u> | <u>427</u> |
| 458.sjeng | 24 | <u>1035</u> | <u>281</u> | 1035 | 281 | 1035 | 281 | 24 | 980 | 296 | <u>980</u> | <u>296</u> | 982 | 296 |
| 462.libquantum | 24 | 330 | 1510 | <u>332</u> | <u>1500</u> | 335 | 1480 | 24 | 330 | 1510 | <u>332</u> | <u>1500</u> | 335 | 1480 |
| 464.h264ref | 24 | 1333 | 398 | 1378 | 386 | <u>1365</u> | <u>389</u> | 24 | <u>1329</u> | <u>400</u> | 1310 | 405 | 1367 | 388 |
| 471.omnetpp | 24 | 749 | 200 | <u>750</u> | <u>200</u> | 751 | 200 | 24 | 732 | 205 | 733 | 205 | <u>732</u> | <u>205</u> |
| 473.astar | 24 | <u>935</u> | <u>180</u> | 936 | 180 | 935 | 180 | 24 | <u>935</u> | <u>180</u> | 936 | 180 | 935 | 180 |
| 483.xalancbmk | 24 | <u>565</u> | <u>293</u> | 565 | 293 | 565 | 293 | 24 | <u>565</u> | <u>293</u> | 565 | 293 | 565 | 293 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size
Large pages were disabled for this run

Platform Notes

BIOS Settings:
Fan speed = full speed (Default = Balanced)
Data Reuse = Disabled (Default = Enabled)

General Notes

Binaries compiled on RHEL5.5
This result was measured on the Gateway GR380 F1.
The Acer AR360 F1, AR380 F1, Gateway GR360 F1 are electronically equivalent

Base Compiler Invocation

C benchmarks:
icc -m32

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/smartheap -lsmartheap
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32



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Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
 401.bzip2: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LINUX
 483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -opt-prefetch -auto-ilp32 -ansi-alias
 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div
 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
 -ansi-alias -auto-ilp32

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -unroll4 -auto-ilp32
 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs

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Peak Optimization Flags (Continued)

471.omnetpp (continued):
-L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.20110608.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.20110608.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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