



SPEC[®] CINT2006 Result

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Itautec

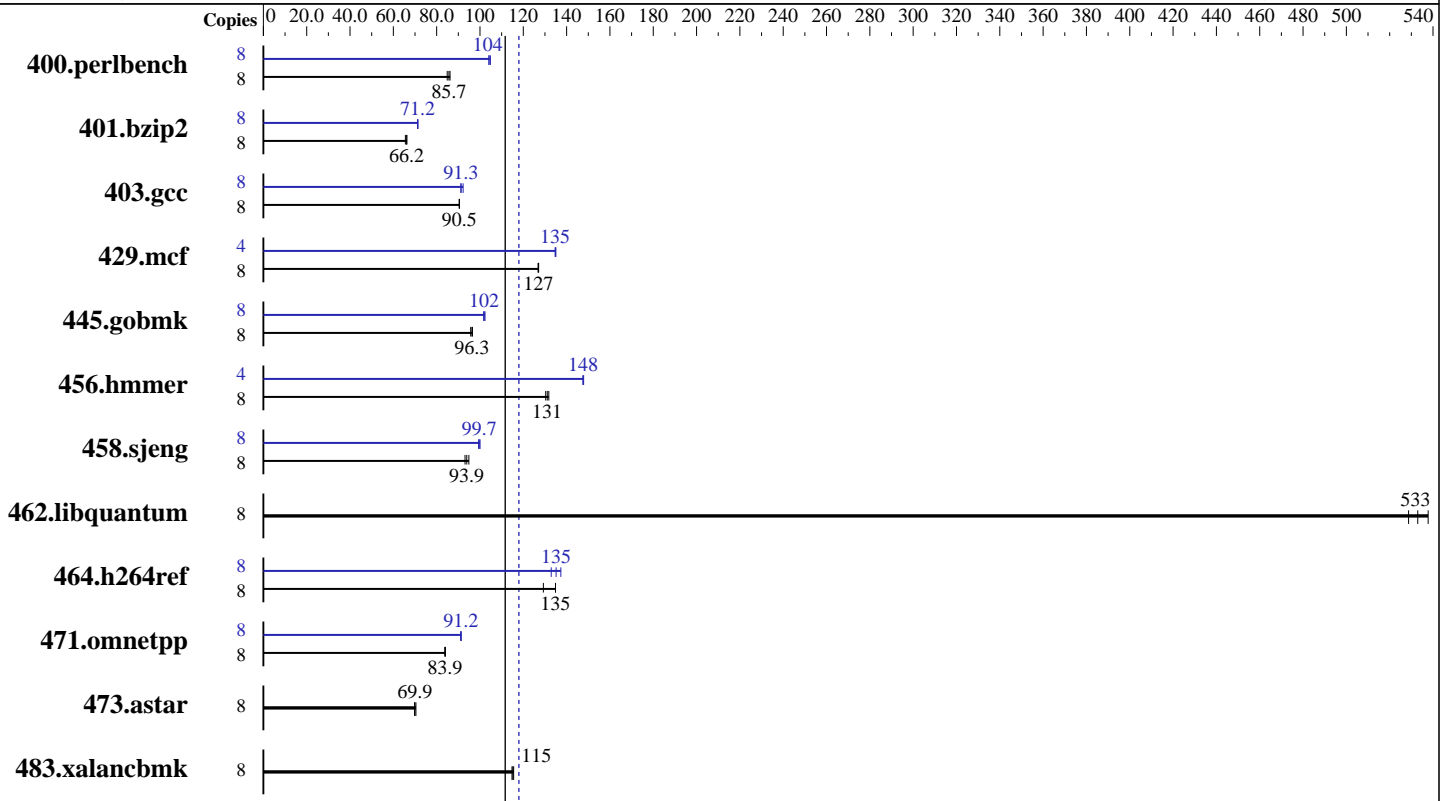
SPECint[®]_rate2006 = 118

Servidor Itautec MX223 (Intel Xeon E5620)

SPECint_rate_base2006 = 112

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Jul-2011
Hardware Availability: Apr-2010
Software Availability: Jan-2011



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Hardware

CPU Name: Intel Xeon E5620
 CPU Characteristics: Intel Turbo Boost Technology up to 2.67 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 24 GB (6 x 4 GB 2Rx4 PC3-8500R-7, ECC)
 Disk Subsystem: 1 x 500 GB SATA-2, 7200 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ Compiler XE for applications running on IA-32, Version 12.0.2 Build 20110112
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	907	86.2	<u>912</u>	<u>85.7</u>	921	84.9	8	<u>750</u>	<u>104</u>	746	105	751	104
401.bzip2	8	<u>1166</u>	<u>66.2</u>	1166	66.2	1177	65.6	8	1082	71.4	<u>1084</u>	<u>71.2</u>	1085	71.2
403.gcc	8	712	90.5	712	90.5	<u>712</u>	<u>90.5</u>	8	706	91.2	698	92.2	<u>705</u>	<u>91.3</u>
429.mcf	8	574	127	<u>574</u>	<u>127</u>	575	127	4	<u>271</u>	<u>135</u>	271	135	270	135
445.gobmk	8	<u>872</u>	<u>96.3</u>	869	96.5	877	95.7	8	<u>823</u>	<u>102</u>	820	102	825	102
456.hammer	8	566	132	<u>569</u>	<u>131</u>	573	130	4	252	148	253	148	<u>253</u>	<u>148</u>
458.sjeng	8	1020	94.9	<u>1031</u>	<u>93.9</u>	1040	93.1	8	<u>971</u>	<u>99.7</u>	968	100	974	99.3
462.libquantum	8	308	538	314	529	<u>311</u>	<u>533</u>	8	308	538	314	529	<u>311</u>	<u>533</u>
464.h264ref	8	1313	135	<u>1313</u>	<u>135</u>	1369	129	8	1332	133	1289	137	<u>1310</u>	<u>135</u>
471.omnetpp	8	<u>596</u>	<u>83.9</u>	595	84.0	597	83.8	8	548	91.3	<u>548</u>	<u>91.2</u>	549	91.1
473.astar	8	797	70.4	805	69.8	<u>803</u>	<u>69.9</u>	8	797	70.4	805	69.8	<u>803</u>	<u>69.9</u>
483.xalancbmk	8	478	116	<u>479</u>	<u>115</u>	481	115	8	478	116	<u>479</u>	<u>115</u>	481	115

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
Large pages were not enabled for this run

Platform Notes

Data Reuse disabled in BIOS.

General Notes

This result was measured on the Servidor Itaotec MX203.
The Servidor Itaotec MX203 and the Servidor Itaotec MX223
are electronically equivalent.

Base Compiler Invocation

C benchmarks:
icc -m32

Continued on next page



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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/rcaneca/sh/SmartHeap_8.1/lib -lsmartheap
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32



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Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -auto-ilp32

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs

Continued on next page



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Peak Optimization Flags (Continued)

471.omnetpp (continued):
-L/home/rcaneca/sh/SmartHeap_8.1/lib -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>
<http://www.spec.org/cpu2006/flags/Itaotec-Intel-Linux64-Platform.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>
<http://www.spec.org/cpu2006/flags/Itaotec-Intel-Linux64-Platform.xml>

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For other inquiries, please contact webmaster@spec.org.

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