



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECint®_rate2006 = 369

Servidor Itaotec MP224 (Intel Xeon X5650)

SPECint_rate_base2006 = 347

CPU2006 license: 9001

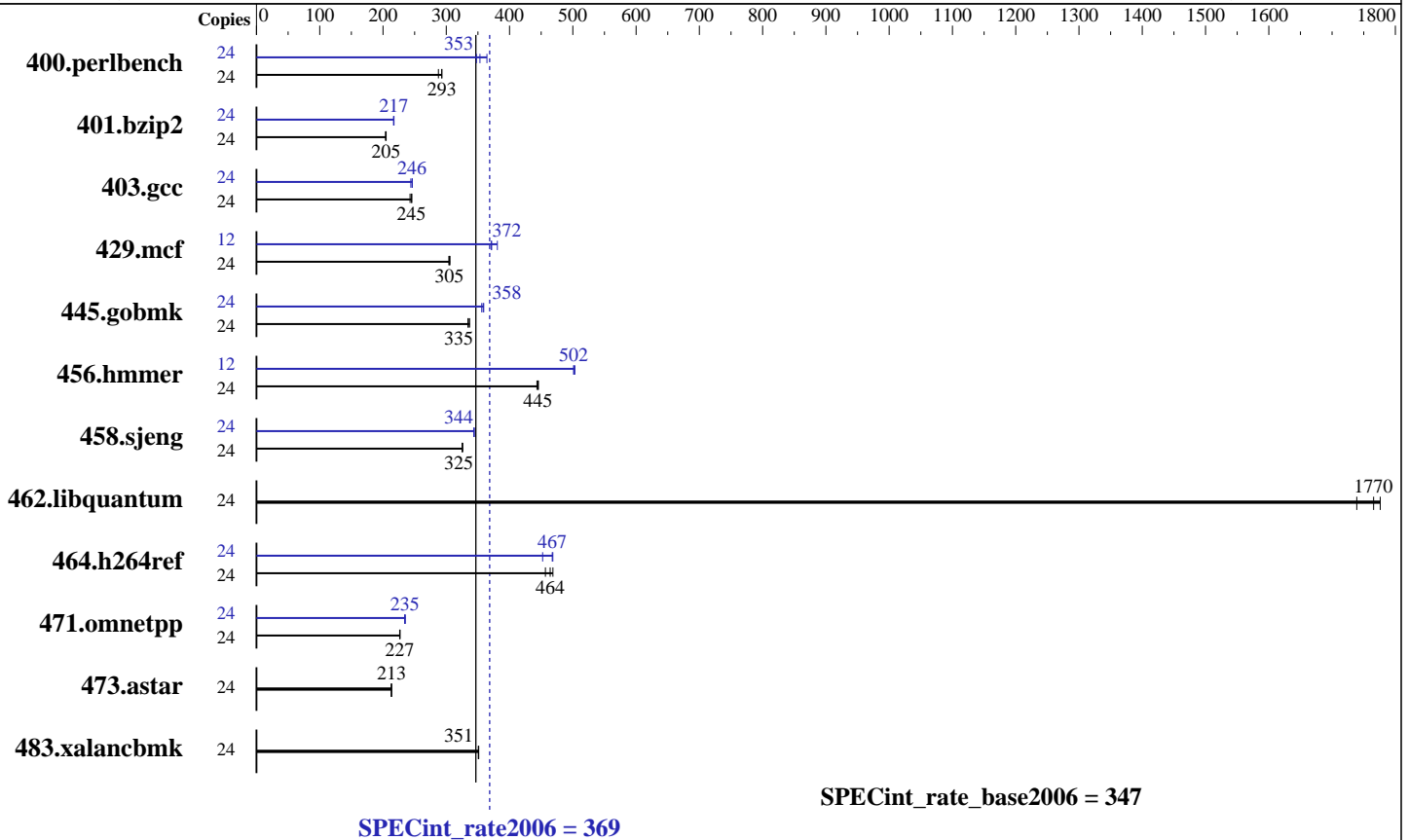
Test date: Jul-2011

Test sponsor: Itaotec

Hardware Availability: Apr-2011

Tested by: Itaotec

Software Availability: Jan-2011



Hardware

CPU Name: Intel Xeon X5650
 CPU Characteristics: Intel Turbo Boost Technology up to 3.06 GHz
 CPU MHz: 2667
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)
 Disk Subsystem: 1 x 500 GB SATA-2, 7200 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ Compiler XE for applications running on IA-32, Version 12.0.2 Build 20110112
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECint_rate2006 = 369

Servidor Itautec MP224 (Intel Xeon X5650)

SPECint_rate_base2006 = 347

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Jul-2011
Hardware Availability: Apr-2011
Software Availability: Jan-2011

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	815	288	<u>801</u>	<u>293</u>	800	293	24	<u>664</u>	<u>353</u>	674	348	644	364
401.bzip2	24	1136	204	1130	205	<u>1132</u>	<u>205</u>	24	<u>1066</u>	<u>217</u>	1069	217	1065	217
403.gcc	24	787	246	796	243	<u>788</u>	<u>245</u>	24	784	246	<u>786</u>	<u>246</u>	792	244
429.mcf	24	720	304	716	306	<u>718</u>	<u>305</u>	12	295	371	<u>294</u>	<u>372</u>	287	381
445.gobmk	24	747	337	753	334	<u>751</u>	<u>335</u>	24	<u>703</u>	<u>358</u>	701	359	707	356
456.hammer	24	505	443	<u>503</u>	<u>445</u>	502	446	12	<u>223</u>	<u>502</u>	222	503	223	501
458.sjeng	24	891	326	894	325	<u>893</u>	<u>325</u>	24	<u>844</u>	<u>344</u>	845	344	844	344
462.libquantum	24	<u>282</u>	<u>1770</u>	280	1780	286	1740	24	<u>282</u>	<u>1770</u>	280	1780	286	1740
464.h264ref	24	<u>1144</u>	<u>464</u>	1163	457	1134	469	24	1134	468	<u>1136</u>	<u>467</u>	1174	452
471.omnetpp	24	661	227	<u>662</u>	<u>227</u>	663	226	24	<u>639</u>	<u>235</u>	637	235	641	234
473.astar	24	789	214	790	213	<u>790</u>	<u>213</u>	24	789	214	790	213	<u>790</u>	<u>213</u>
483.xalancbmk	24	<u>472</u>	<u>351</u>	471	351	472	351	24	<u>472</u>	<u>351</u>	471	351	472	351

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
Large pages were not enabled for this run

Platform Notes

Data Reuse disabled in BIOS.

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECint_rate2006 = 369

Servidor Itautec MP224 (Intel Xeon X5650)

SPECint_rate_base2006 = 347

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Jul-2011
Hardware Availability: Apr-2011
Software Availability: Jan-2011

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/rcaneca/sh/SmartHeap_8.1/lib -lsmartheap
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECint_rate2006 = 369

Servidor Itautec MP224 (Intel Xeon X5650)

SPECint_rate_base2006 = 347

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Jul-2011
Hardware Availability: Apr-2011
Software Availability: Jan-2011

Peak Portability Flags (Continued)

456.hmmcr: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -auto-ilp32

456.hmmcr: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/home/rcaneca/sh/SmartHeap_8.1/lib -lsmartheap

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECint_rate2006 = 369

Servidor Itautec MP224 (Intel Xeon X5650)

SPECint_rate_base2006 = 347

CPU2006 license: 9001

Test date: Jul-2011

Test sponsor: Itautec

Hardware Availability: Apr-2011

Tested by: Itautec

Software Availability: Jan-2011

Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/Itautec-Intel-Linux64-Platform.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/Itautec-Intel-Linux64-Platform.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

Report generated on Wed Jul 23 22:44:12 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 16 August 2011.