



SPEC® CINT2006 Result

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Itautec

SPECint®_rate2006 = 450

Servidor Itautec MX215 (Intel Xeon E5-2650L)

SPECint_rate_base2006 = 437

CPU2006 license: 9001

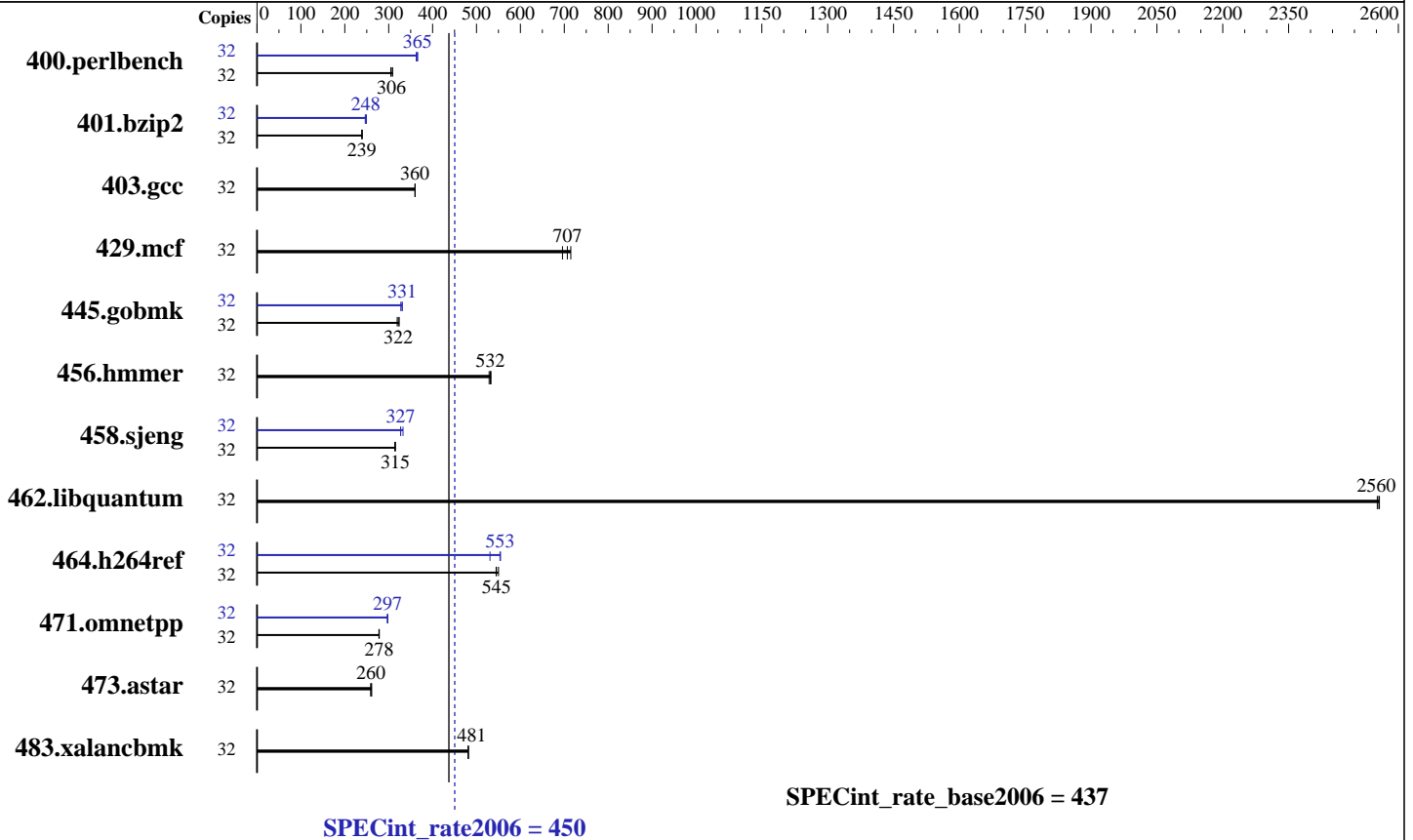
Test date: Mar-2012

Test sponsor: Itautec

Hardware Availability: Jun-2012

Tested by: Itautec

Software Availability: Dec-2011



Hardware

CPU Name: Intel Xeon E5-2650L
 CPU Characteristics: Intel Turbo Boost Technology up to 2.30 GHz
 CPU MHz: 1800
 FPU: Integrated
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 20 MB I+D on chip per chip
 Other Cache: None
 Memory: 64 GB (16 x 4 GB 2Rx4 PC3-12800R-11, ECC)
 Disk Subsystem: 2 x 146 GB, SAS, 15000 RPM, RAID 0
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server Release 6.2, 2.6.32-220.el6.x86_64
 Compiler: C/C++: Version 12.1.0 of Intel Compiler XE Build 20111011
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	32	1012	309	<u>1022</u>	<u>306</u>	1026	305	32	854	366	<u>856</u>	<u>365</u>	862	363
401.bzip2	32	1289	240	<u>1291</u>	<u>239</u>	1293	239	32	<u>1246</u>	<u>248</u>	1239	249	1252	247
403.gcc	32	<u>716</u>	<u>360</u>	716	360	715	360	32	<u>716</u>	<u>360</u>	716	360	715	360
429.mcf	32	408	715	419	696	<u>413</u>	<u>707</u>	32	408	715	419	696	<u>413</u>	<u>707</u>
445.gobmk	32	1036	324	<u>1043</u>	<u>322</u>	1051	319	32	1025	328	1015	331	<u>1015</u>	<u>331</u>
456.hammer	32	565	529	<u>562</u>	<u>532</u>	560	533	32	565	529	<u>562</u>	<u>532</u>	560	533
458.sjeng	32	<u>1230</u>	<u>315</u>	1233	314	1228	315	32	<u>1183</u>	<u>327</u>	1184	327	1164	333
462.libquantum	32	259	2560	<u>259</u>	<u>2560</u>	260	2550	32	259	2560	<u>259</u>	<u>2560</u>	260	2550
464.h264ref	32	<u>1298</u>	<u>545</u>	1287	550	1300	545	32	<u>1280</u>	<u>553</u>	1334	531	1277	555
471.omnetpp	32	718	278	<u>719</u>	<u>278</u>	720	278	32	672	298	673	297	<u>673</u>	<u>297</u>
473.astar	32	860	261	<u>865</u>	<u>260</u>	867	259	32	860	261	<u>865</u>	<u>260</u>	867	259
483.xalancbmk	32	459	481	<u>459</u>	<u>481</u>	459	481	32	459	481	<u>459</u>	<u>481</u>	459	481

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
Large pages were not enabled for this run

General Notes

This result was measured on the Servidor Itautec MX215.
The Servidor Itautec MX215 and Servidor Itautec MX225
are electronically equivalent.

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/home/rcaneca/sh/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32
400.perlbench: icc -m64
401.bzip2: icc -m64
458.sjeng: icc -m64
C++ benchmarks:
icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -auto-ilp32

456.hmmmer: basepeak = yes

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll14 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll12 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/home/rcaneca/sh/SmartHeap_8.1/lib -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



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The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/Itaotec-Servidor_Itaotec-Intel-Linux-Platform.html

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2006/flags/Itaotec-Servidor_Itaotec-Intel-Linux-Platform.xml

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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