



# SPEC® CINT2006 Result

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## Supermicro

**SPECint®\_rate2006 = 55.0**

SuperServer 1017C-TF (X9SCL-F, Intel G530)

**SPECint\_rate\_base2006 = 52.9**

CPU2006 license: 001176

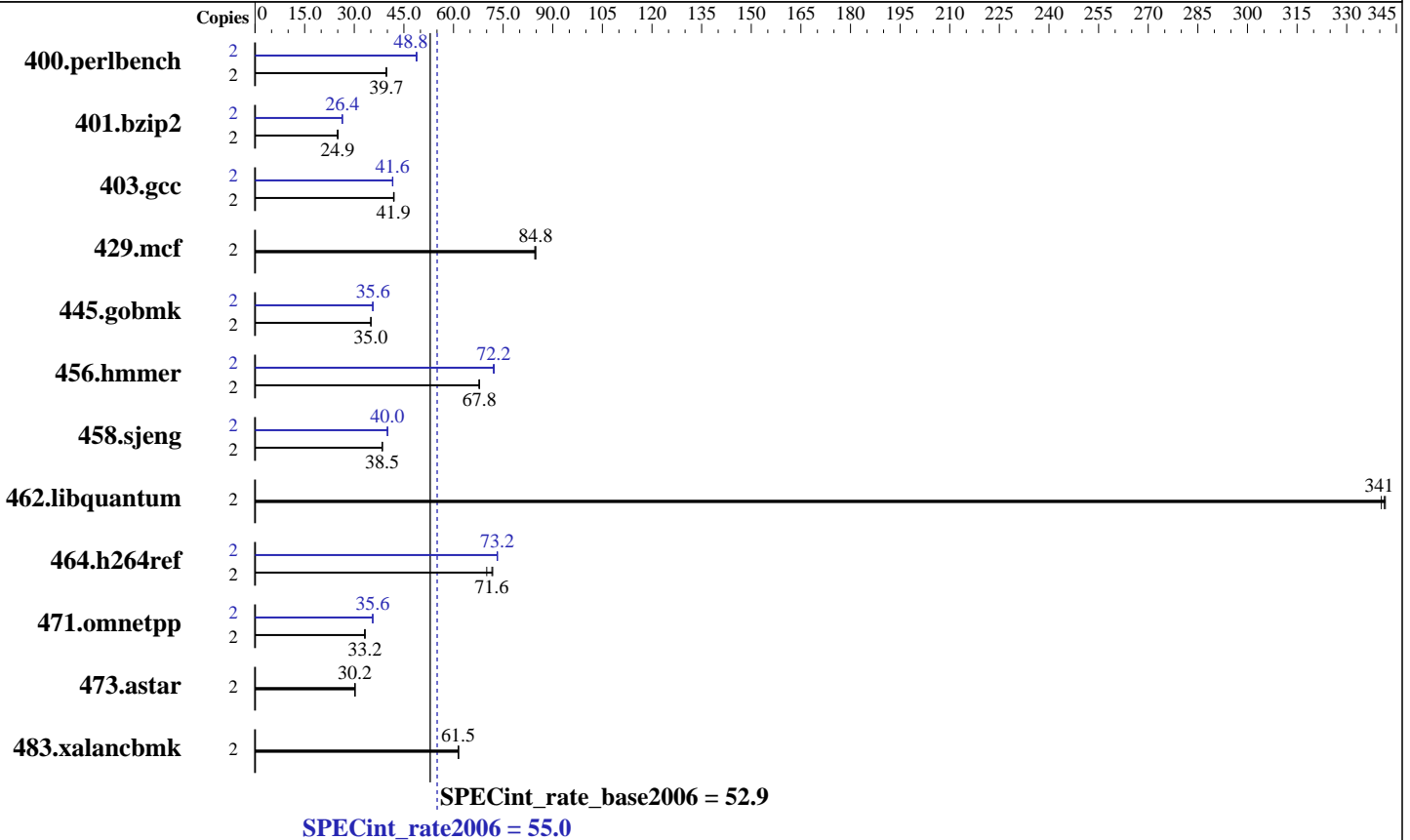
Test date: Apr-2012

Test sponsor: Supermicro

Hardware Availability: Apr-2011

Tested by: Supermicro

Software Availability: Oct-2011



### Hardware

CPU Name: Intel Celeron G530  
 CPU Characteristics:  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 2 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600E-9, ECC, running at 1066 MHz and CL7)  
 Disk Subsystem: 1 x 500 GB SATA II, 7200 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server Release 6.1, Kernel 2.6.32-131.0.15.el6.x86\_64  
 Compiler: C/C++; Version 12.1.0.225 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	492	39.7	492	39.7	<b>492</b>	<b>39.7</b>	2	400	48.9	401	48.7	<b>400</b>	<b>48.8</b>
401.bzip2	2	<b>774</b>	<b>24.9</b>	774	24.9	769	25.1	2	731	26.4	733	26.3	<b>731</b>	<b>26.4</b>
403.gcc	2	383	42.0	<b>384</b>	<b>41.9</b>	384	41.9	2	388	41.5	387	41.7	<b>387</b>	<b>41.6</b>
429.mcf	2	215	84.8	216	84.6	<b>215</b>	<b>84.8</b>	2	215	84.8	216	84.6	<b>215</b>	<b>84.8</b>
445.gobmk	2	599	35.0	<b>599</b>	<b>35.0</b>	599	35.0	2	589	35.6	<b>589</b>	<b>35.6</b>	590	35.6
456.hammer	2	275	67.9	276	67.7	<b>275</b>	<b>67.8</b>	2	259	72.1	258	72.3	<b>259</b>	<b>72.2</b>
458.sjeng	2	<b>629</b>	<b>38.5</b>	629	38.5	629	38.5	2	604	40.0	605	40.0	<b>604</b>	<b>40.0</b>
462.libquantum	2	122	340	<b>121</b>	<b>341</b>	121	342	2	122	340	<b>121</b>	<b>341</b>	121	342
464.h264ref	2	632	70.0	<b>618</b>	<b>71.6</b>	616	71.8	2	604	73.3	<b>605</b>	<b>73.2</b>	605	73.2
471.omnetpp	2	<b>376</b>	<b>33.2</b>	377	33.1	376	33.3	2	<b>351</b>	<b>35.6</b>	351	35.6	351	35.6
473.astar	2	465	30.2	<b>465</b>	<b>30.2</b>	465	30.2	2	465	30.2	<b>465</b>	<b>30.2</b>	465	30.2
483.xalancbmk	2	<b>224</b>	<b>61.5</b>	224	61.5	224	61.5	2	<b>224</b>	<b>61.5</b>	224	61.5	224	61.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/home/usr/cpu2006/libs/32:/home/usr/cpu2006/libs/64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:  
icc -m32

C++ benchmarks:  
icpc -m32



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
-Wl,-z,muldefs -L/smartheap -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:  
icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -opt-mem-layout-trans=3

456.hmmr: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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