



# SPEC<sup>®</sup> CFP2006 Result

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## Fujitsu

PRIMERGY TX100 S3p, Intel Xeon E3-1240 v2, 3.40 GHz

SPECfp<sup>®</sup>\_rate2006 = 138

SPECfp\_rate\_base2006 = 134

CPU2006 license: 19

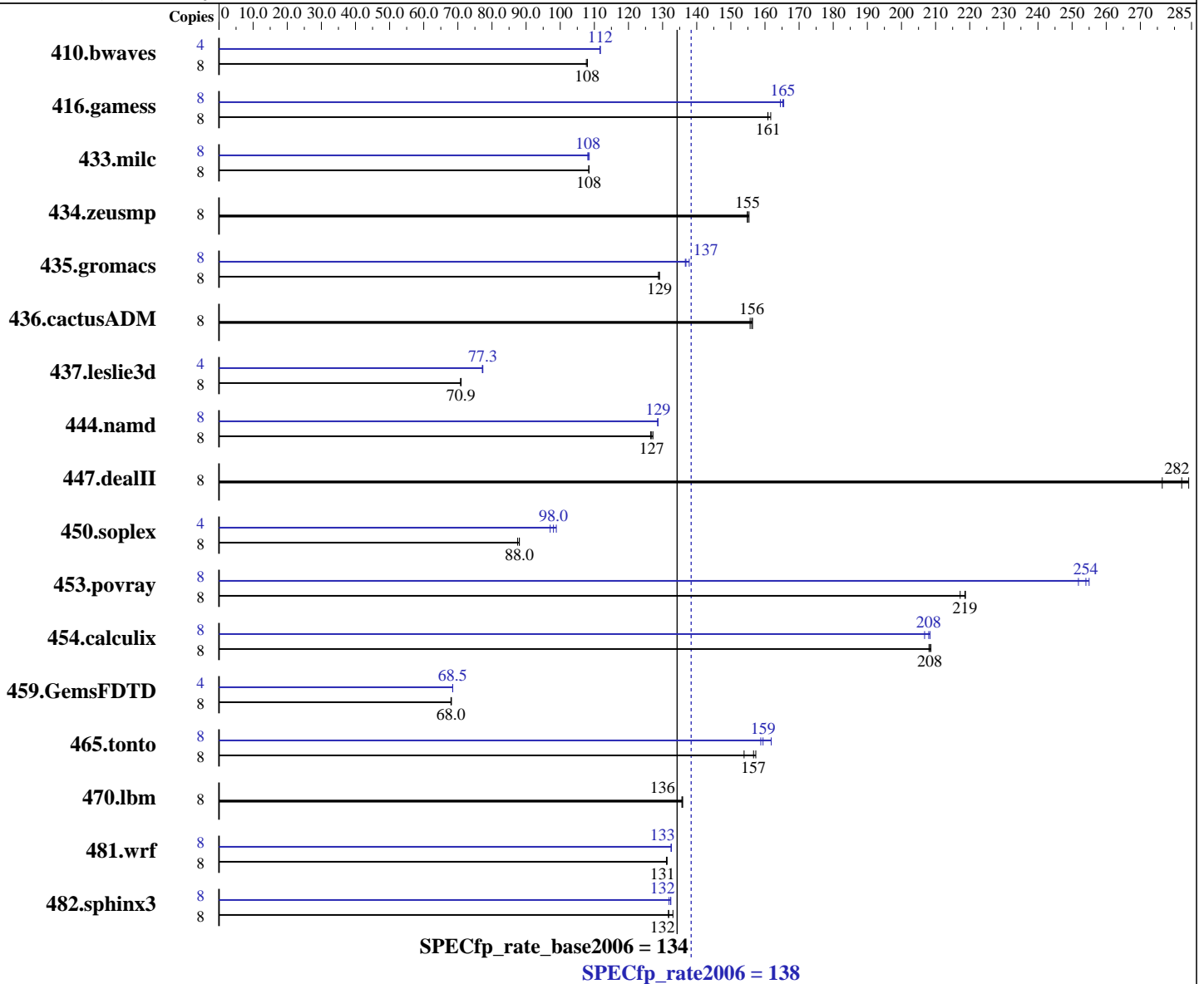
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: May-2012

Hardware Availability: May-2012

Software Availability: Feb-2012



### Hardware

CPU Name: Intel Xeon E3-1240 v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.8 GHz  
 CPU MHz: 3400  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

### Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)  
 2.6.32-220.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.293 of Intel C++ Studio XE for Linux;  
 Fortran: Version 12.1.0.293 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4

Continued on next page



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L3 Cache: 8 MB I+D on chip per chip  
Other Cache: None  
Memory: 16 GB (2 x 8 GB 2Rx8 PC3-12800E-11, ECC)  
Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM  
Other Hardware: None

System State: Run level 3 (multi-user)  
Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	<u>1007</u>	<u>108</u>	1010	108	1007	108	4	487	112	487	112	<u>487</u>	<u>112</u>
416.gamess	8	<u>974</u>	<u>161</u>	969	162	974	161	8	<u>948</u>	<u>165</u>	952	164	946	166
433.milc	8	<u>677</u>	<u>108</u>	677	108	677	108	8	677	109	679	108	<u>679</u>	<u>108</u>
434.zeusmp	8	<u>470</u>	<u>155</u>	470	155	469	155	8	<u>470</u>	<u>155</u>	470	155	469	155
435.gromacs	8	443	129	444	129	<u>443</u>	<u>129</u>	8	415	138	<u>417</u>	<u>137</u>	418	137
436.cactusADM	8	<u>612</u>	<u>156</u>	611	156	614	156	8	<u>612</u>	<u>156</u>	611	156	614	156
437.leslie3d	8	1060	71.0	<u>1061</u>	<u>70.9</u>	1063	70.8	4	486	77.3	<u>487</u>	<u>77.3</u>	487	77.2
444.namd	8	<u>506</u>	<u>127</u>	507	126	505	127	8	499	129	<u>499</u>	<u>129</u>	499	129
447.dealII	8	322	284	331	276	<u>324</u>	<u>282</u>	8	322	284	331	276	<u>324</u>	<u>282</u>
450.soplex	8	758	88.0	762	87.5	<u>758</u>	<u>88.0</u>	4	344	97.1	337	98.9	<u>340</u>	<u>98.0</u>
453.povray	8	196	217	<u>195</u>	<u>219</u>	195	219	8	<u>168</u>	<u>254</u>	167	255	169	252
454.calculix	8	316	209	317	208	<u>317</u>	<u>208</u>	8	<u>317</u>	<u>208</u>	317	208	319	207
459.GemsFDTD	8	<u>1248</u>	<u>68.0</u>	1249	68.0	1246	68.1	4	619	68.5	<u>620</u>	<u>68.5</u>	620	68.5
465.tonto	8	512	154	500	157	<u>503</u>	<u>157</u>	8	486	162	<u>494</u>	<u>159</u>	496	159
470.lbm	8	<u>809</u>	<u>136</u>	810	136	809	136	8	<u>809</u>	<u>136</u>	810	136	809	136
481.wrf	8	681	131	682	131	<u>681</u>	<u>131</u>	8	674	133	<u>674</u>	<u>133</u>	675	132
482.sphinx3	8	1184	132	1172	133	<u>1183</u>	<u>132</u>	8	1178	132	1182	132	<u>1178</u>	<u>132</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64"

Continued on next page



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### General Notes (Continued)

Binaries compiled on a system with 2x E5-2650 CPU + 96 GB memory using RHEL6.2  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>  
For information about Fujitsu please visit: <http://www.fujitsu.com>

### Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

### Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

### Base Optimization Flags

C benchmarks:  
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

Continued on next page



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## Base Optimization Flags (Continued)

### C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

### Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

### Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

## Peak Compiler Invocation

### C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

### C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

### Fortran benchmarks:

ifort -m64

### Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
465.tonto: -DSPEC\_CPU\_LP64

Continued on next page



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**Software Availability:** Feb-2012

## Peak Portability Flags (Continued)

470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-opt-mem-layout-trans=3

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto  
-inline-calloc -opt-malloc-options=3

Continued on next page



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## Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch  
-auto-p32 -ansi-alias -opt-mem-layout-trans=3

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -static -auto-ilp32  
-opt-mem-layout-trans=3

481.wrf: Same as 454.calculix

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>  
<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>  
<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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