



SPEC® CINT2006 Result

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Supermicro

Supermicro C7P67 motherboard (Intel Core i7-2700K, 3.50 GHz)

SPECint®_rate2006 = 181

SPECint_rate_base2006 = 174

CPU2006 license: 001176

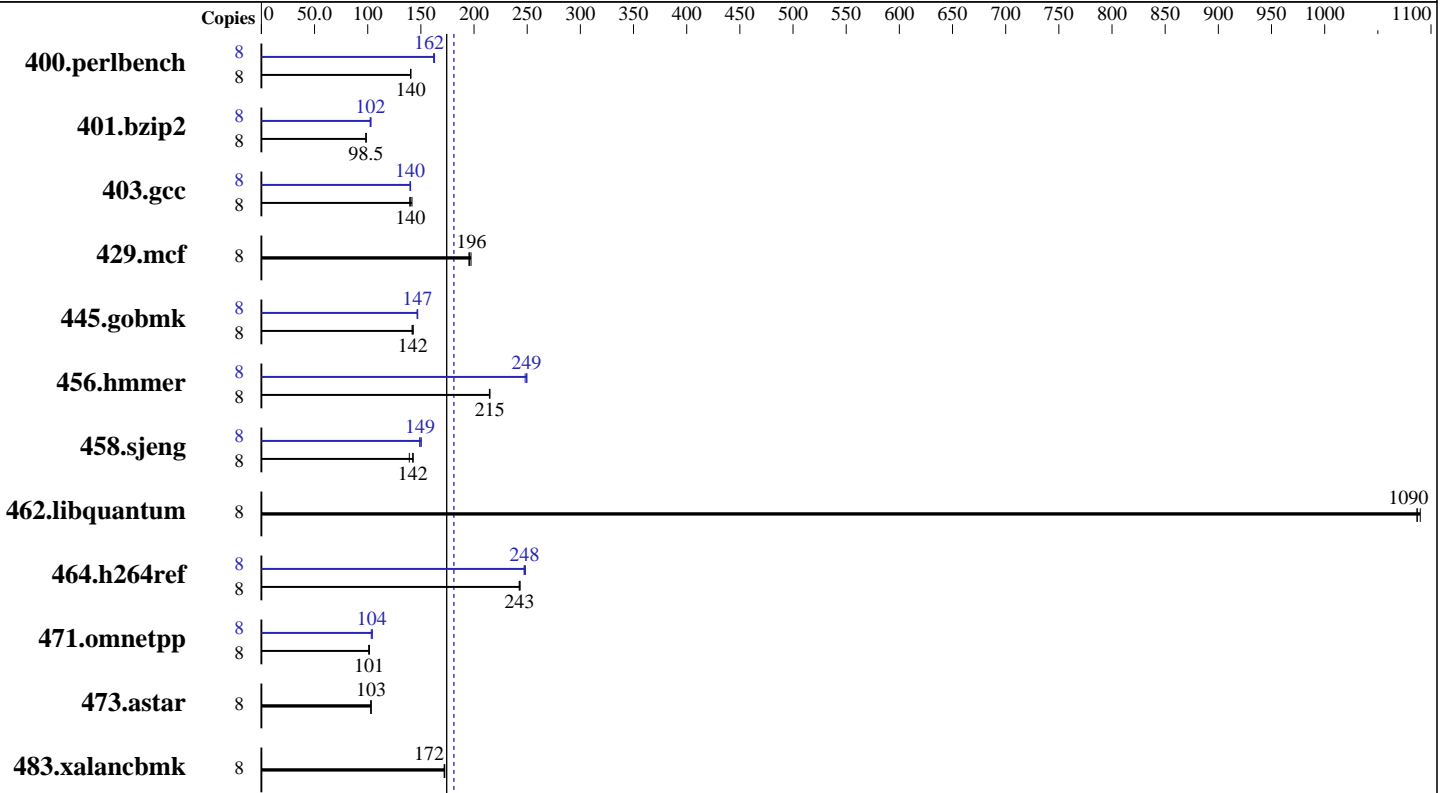
Test sponsor: Supermicro

Tested by: Supermicro

Test date: May-2012

Hardware Availability: Oct-2011

Software Availability: Dec-2011



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Hardware

CPU Name: Intel Core i7-2700K
 CPU Characteristics: Intel Turbo Boost Technology up to 3.90 GHz
 CPU MHz: 3500
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 8 GB (4 x 2 GB 2Rx8 PC3-12800U-11)
 Disk Subsystem: 1 x 120 GB OCZ SSD
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server Release 6.2, Kernel 2.6.32-220.el6.x86_64
 Compiler: C/C++; Version 12.1.0.225 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	<u>557</u>	<u>140</u>	557	140	556	141	8	<u>482</u>	<u>162</u>	482	162	480	163
401.bzip2	8	<u>784</u>	<u>98.5</u>	786	98.2	783	98.6	8	754	102	<u>753</u>	<u>102</u>	748	103
403.gcc	8	462	140	455	142	<u>459</u>	<u>140</u>	8	<u>460</u>	<u>140</u>	461	140	459	140
429.mcf	8	370	197	<u>373</u>	<u>196</u>	374	195	8	370	197	<u>373</u>	<u>196</u>	374	195
445.gobmk	8	<u>590</u>	<u>142</u>	592	142	588	143	8	<u>572</u>	<u>147</u>	571	147	573	146
456.hammer	8	347	215	<u>347</u>	<u>215</u>	348	214	8	301	248	299	250	<u>299</u>	<u>249</u>
458.sjeng	8	696	139	678	143	<u>680</u>	<u>142</u>	8	<u>648</u>	<u>149</u>	643	150	650	149
462.libquantum	8	152	1090	<u>152</u>	<u>1090</u>	153	1090	8	152	1090	<u>152</u>	<u>1090</u>	153	1090
464.h264ref	8	728	243	<u>729</u>	<u>243</u>	730	242	8	713	248	717	247	<u>715</u>	<u>248</u>
471.omnetpp	8	<u>494</u>	<u>101</u>	492	102	495	101	8	484	103	479	104	<u>481</u>	<u>104</u>
473.astar	8	545	103	544	103	<u>545</u>	<u>103</u>	8	545	103	544	103	<u>545</u>	<u>103</u>
483.xalancbmk	8	<u>320</u>	<u>172</u>	320	172	321	172	8	<u>320</u>	<u>172</u>	320	172	321	172

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

Platform Notes

As tested, the system used a Supermicro CSE-732D2-500B chassis. The chassis is configured with a PWS-502-PQ power supply, 1 SNK-P0046A4 heatsink, as well as 1 FAN-0124L4 front cooling fan and 1 FAN-0124L4 rear exhaust fan.

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5



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Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/smartheap -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64
401.bzip2: icc -m64
456.hmmer: icc -m64
458.sjeng: icc -m64

C++ benchmarks:
icpc -m32



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Peak Portability Flags

```

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

```

Peak Optimization Flags

C benchmarks:

```

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
           -auto-ilp32 -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
           -ansi-alias

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
           -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
             -ansi-alias

```

C++ benchmarks:

```

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias
             -opt-ra-region-strategy=block -Wl,-z,muldefs
             -L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

```



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>
<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>
<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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