



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

SPECint®_rate2006 = 380

Express5800/R120d-2E (Intel Xeon E5-2430L)

SPECint_rate_base2006 = 364

CPU2006 license: 9006

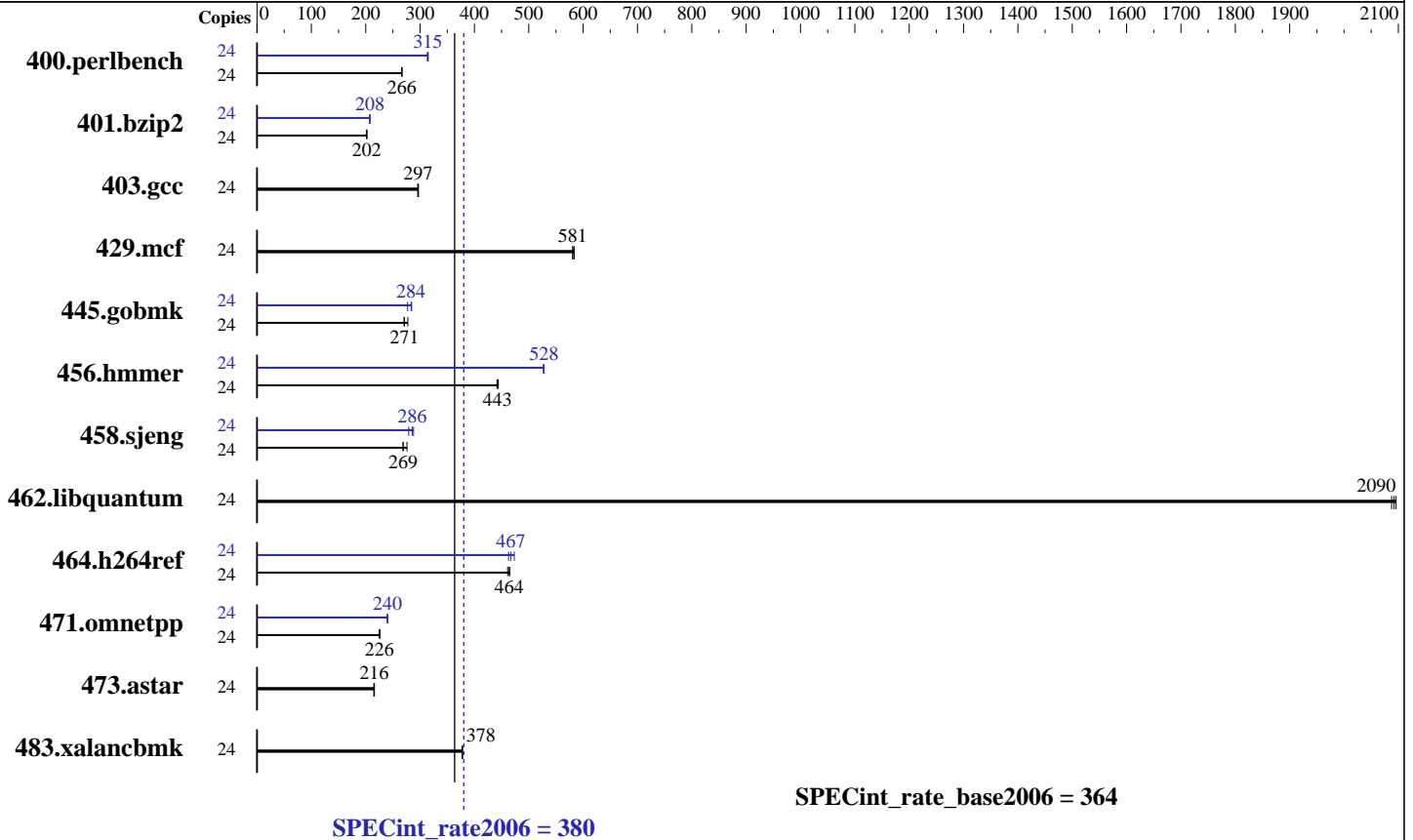
Test date: May-2012

Test sponsor: NEC Corporation

Hardware Availability: May-2012

Tested by: NEC Corporation

Software Availability: Dec-2011



Hardware

CPU Name: Intel Xeon E5-2430L
 CPU Characteristics: Intel Turbo Boost Technology up to 2.50 GHz
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 15 MB I+D on chip per chip
 Other Cache: None
 Memory: 96 GB (12 x 8 GB 2Rx4 PC3L-12800R-11, ECC, running at 1333 MHz and CL9)
 Disk Subsystem: 1 x 250 GB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)
 Kernel 2.6.32-220.el6.x86_64
 Compiler: C/C++: Version 12.1.2.273 of Intel C++ Studio XE for Linux;
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

SPECint_rate2006 = 380

Express5800/R120d-2E (Intel Xeon E5-2430L)

SPECint_rate_base2006 = 364

CPU2006 license: 9006

Test date: May-2012

Test sponsor: NEC Corporation

Hardware Availability: May-2012

Tested by: NEC Corporation

Software Availability: Dec-2011

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	880	266	879	267	880	266	24	748	313	745	315	745	315
401.bzip2	24	1145	202	1149	202	1148	202	24	1115	208	1115	208	1115	208
403.gcc	24	651	297	651	297	653	296	24	651	297	651	297	653	296
429.mcf	24	377	581	375	584	377	581	24	377	581	375	584	377	581
445.gobmk	24	928	271	907	278	930	271	24	888	284	909	277	885	285
456.hammer	24	504	444	507	442	506	443	24	425	527	424	528	424	528
458.sjeng	24	1052	276	1081	269	1079	269	24	1008	288	1017	286	1040	279
462.libquantum	24	238	2090	237	2100	238	2090	24	238	2090	237	2100	238	2090
464.h264ref	24	1152	461	1142	465	1145	464	24	1122	473	1138	467	1148	463
471.omnetpp	24	665	226	665	226	665	225	24	625	240	626	240	624	240
473.astar	24	780	216	783	215	782	216	24	780	216	783	215	782	216
483.xalancbmk	24	439	378	438	378	438	378	24	439	378	438	378	438	378

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:
Energy Performance: Performance

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64"

The Express5800/R120d-1E and the Express5800/R120d-2E models are electronically equivalent. The results have been measured on the Express5800/R120d-1E model.

Added glibc-static-2.12-1.47.el6.x86_64.rpm to enable static linking

Transparent Huge Pages enabled with:

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

SPECint_rate2006 = 380

Express5800/R120d-2E (Intel Xeon E5-2430L)

SPECint_rate_base2006 = 364

CPU2006 license: 9006

Test date: May-2012

Test sponsor: NEC Corporation

Hardware Availability: May-2012

Tested by: NEC Corporation

Software Availability: Dec-2011

General Notes (Continued)

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

SPECint_rate2006 = 380

Express5800/R120d-2E (Intel Xeon E5-2430L)

SPECint_rate_base2006 = 364

CPU2006 license: 9006

Test date: May-2012

Test sponsor: NEC Corporation

Hardware Availability: May-2012

Tested by: NEC Corporation

Software Availability: Dec-2011

Peak Compiler Invocation (Continued)

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:

`icpc -m32`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-auto-ilp32`

401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-opt-prefetch -auto-ilp32 -ansi-alias`

403.gcc: `basepeak = yes`

429.mcf: `basepeak = yes`

445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)`
`-ansi-alias -opt-mem-layout-trans=3`

456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`

458.sjeng: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-unroll4 -auto-ilp32`

462.libquantum: `basepeak = yes`

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

SPECint_rate2006 = 380

Express5800/R120d-2E (Intel Xeon E5-2430L)

SPECint_rate_base2006 = 364

CPU2006 license: 9006

Test date: May-2012

Test sponsor: NEC Corporation

Hardware Availability: May-2012

Tested by: NEC Corporation

Software Availability: Dec-2011

Peak Optimization Flags (Continued)

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>
<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120d-RevA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>
<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120d-RevA.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 08:19:47 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 19 June 2012.