



# SPEC® CINT2006 Result

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## Acer Incorporated

SPECint®\_rate2006 = 187

### Acer AT310 F2 (Xeon E3-1280 v2)

SPECint\_rate\_base2006 = 180

CPU2006 license: 97

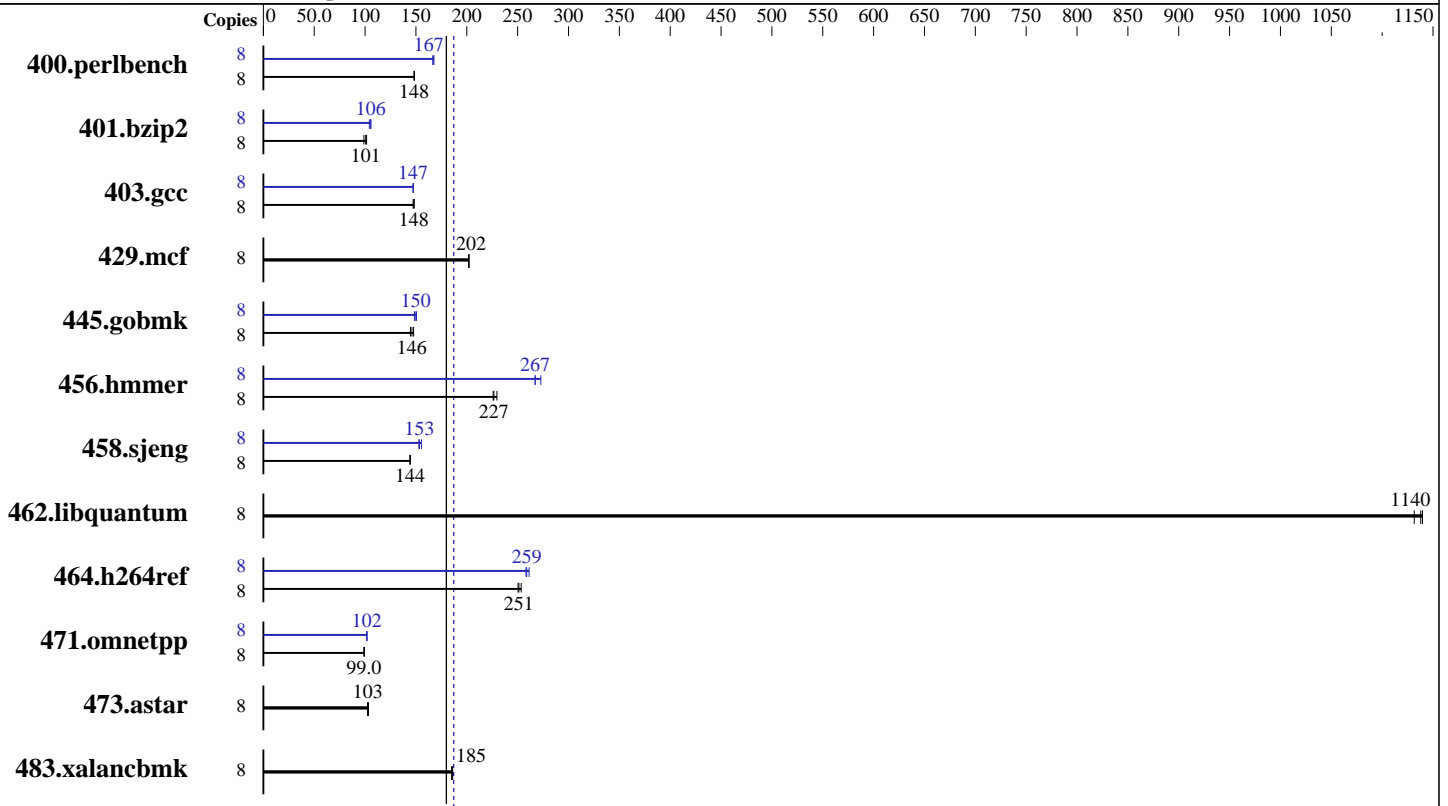
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Jun-2012

Hardware Availability: Jan-2012

Software Availability: Dec-2011



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### Hardware

CPU Name: Intel Xeon E3-1280 v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 4.00 GHz  
 CPU MHz: 3600  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 16 GB (2 x 8 GB 2Rx8 PC3-10600E-9,ECC)  
 Disk Subsystem: 1 x 640 GB SATA, 7200 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.1 (Santiago)  
 2.6.32-131.0.15.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	527	148	527	148	<u>527</u>	<u>148</u>	8	<u>468</u>	<u>167</u>	466	168	469	167
401.bzip2	8	781	98.8	<u>768</u>	<u>101</u>	761	101	8	731	106	741	104	<u>731</u>	<u>106</u>
403.gcc	8	434	148	437	147	<u>435</u>	<u>148</u>	8	<u>437</u>	<u>147</u>	437	148	438	147
429.mcf	8	<u>361</u>	<u>202</u>	361	202	361	202	8	<u>361</u>	<u>202</u>	361	202	361	202
445.gobmk	8	580	145	<u>574</u>	<u>146</u>	568	148	8	565	149	557	151	<u>560</u>	<u>150</u>
456.hammer	8	330	226	<u>329</u>	<u>227</u>	325	230	8	<u>279</u>	<u>267</u>	274	273	279	267
458.sjeng	8	<u>671</u>	<u>144</u>	671	144	670	145	8	<u>631</u>	<u>153</u>	633	153	623	155
462.libquantum	8	145	1140	146	1130	<u>146</u>	<u>1140</u>	8	145	1140	146	1130	<u>146</u>	<u>1140</u>
464.h264ref	8	698	253	707	250	<u>705</u>	<u>251</u>	8	678	261	<u>684</u>	<u>259</u>	686	258
471.omnetpp	8	<u>505</u>	<u>99.0</u>	504	99.2	505	99.0	8	<u>491</u>	<u>102</u>	492	102	491	102
473.astar	8	548	102	544	103	<u>546</u>	<u>103</u>	8	548	102	544	103	<u>546</u>	<u>103</u>
483.xalancbmk	8	<u>298</u>	<u>185</u>	298	185	297	186	8	<u>298</u>	<u>185</u>	298	185	297	186

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

Sysinfo program /usr/cpu2006/config/sysinfo.rev6800  
\$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3  
running on localhost.localdomain Tue Jun 12 11:46:37 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) CPU E3-1280 V2 @ 3.60GHz
 1 "physical id"s (chips)
 8 "processors"
```

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 4
siblings  : 8
```

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## Platform Notes (Continued)

physical 0: cores 0 1 2 3  
cache size : 8192 KB

From /proc/meminfo  
MemTotal: 16300904 kB  
HugePages\_Total: 0  
Hugepagesize: 2048 kB

/usr/bin/lsb\_release -d  
Red Hat Enterprise Linux Server release 6.1 (Santiago)

From /etc/\*release\* /etc/\*version\*  
redhat-release: Red Hat Enterprise Linux Server release 6.1 (Santiago)  
system-release: Red Hat Enterprise Linux Server release 6.1 (Santiago)  
system-release-cpe: cpe:/o:redhat:enterprise\_linux:6server:ga:server

uname -a:  
Linux localhost.localdomain 2.6.32-131.0.15.el6.x86\_64 #1 SMP Tue May 10 15:42:40 EDT 2011 x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 3 Jun 12 11:45 last=5

SPEC is set to: /usr/cpu2006  
Filesystem Type Size Used Avail Use% Mounted on  
/dev/mapper/VolGroup-lv\_root  
ext4 50G 8.1G 39G 18% /

Additional information from dmidecode:  
Memory:  
2x Hynix/Hyundai HMT41GU7MFR8A-H9 8 GB 1333 MHz 2 rank

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL6.1  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:  
icc -m32

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xAVX -ipo -O3 -no-prec-div -opt-prefetch

C++ benchmarks:  
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/smartheap -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:  
icpc -m32



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## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
 401.bzip2: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LINUX  
 483.xalanbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch  
 -auto-ilp32 -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
 -ansi-alias

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -prof-use(pass 2) -unroll4  
 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
 -ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias  
 -opt-ra-region-strategy=block -Wl,-z,muldefs  
 -L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalanbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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