



SPEC® CINT2006 Result

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Supermicro

Supermicro X9SAE-V motherboard (Intel Core i7-3770T, 2.50 GHz)

SPECint®_rate2006 = 173

SPECint_rate_base2006 = 166

CPU2006 license: 001176

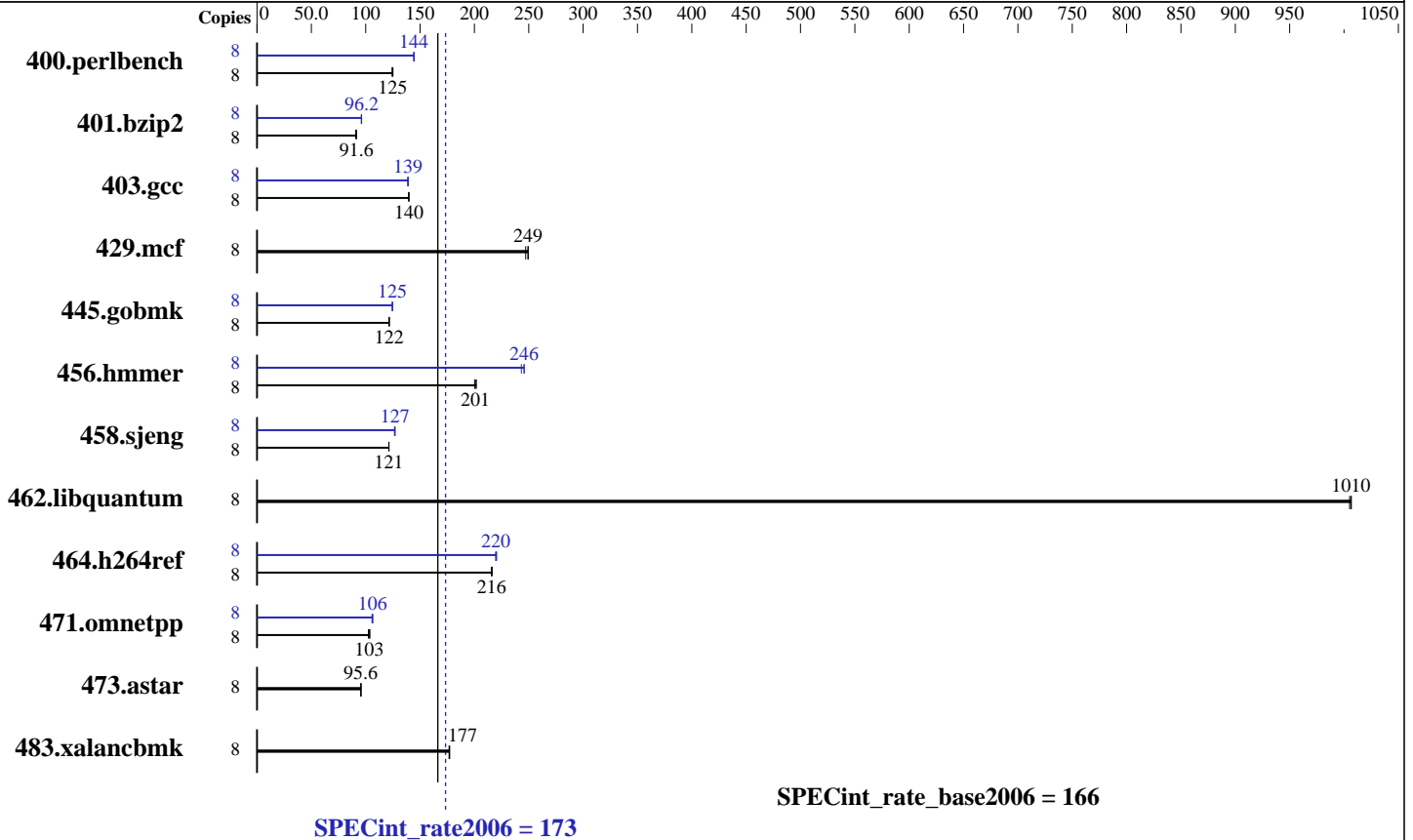
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Sep-2012

Hardware Availability: Apr-2012

Software Availability: Jun-2012



Hardware

CPU Name: Intel Core i7-3770T
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz
 CPU MHz: 2500
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 16 GB (4 x 4 GB 2Rx8 PC3-12800U-11)
 Disk Subsystem: 1 x 300 GB SATA II, 10000 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server Release 6.3, Kernel 2.6.32-279.el6.x86_64
 Compiler: C/C++; Version 12.1.0.225 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



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Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
400.perlbench	8	625	125	629	124	<u>626</u>	<u>125</u>	8	<u>542</u>	<u>144</u>	542	144	540	145		
401.bzip2	8	851	90.7	843	91.6	<u>843</u>	<u>91.6</u>	8	802	96.2	<u>803</u>	<u>96.2</u>	805	95.9		
403.gcc	8	461	140	<u>461</u>	<u>140</u>	462	139	8	463	139	<u>464</u>	<u>139</u>	464	139		
429.mcf	8	295	247	<u>293</u>	<u>249</u>	292	250	8	295	247	<u>293</u>	<u>249</u>	292	250		
445.gobmk	8	689	122	691	121	<u>691</u>	<u>122</u>	8	<u>674</u>	<u>125</u>	674	124	674	125		
456.hammer	8	373	200	370	202	<u>371</u>	<u>201</u>	8	307	243	<u>304</u>	<u>246</u>	304	246		
458.sjeng	8	798	121	<u>798</u>	<u>121</u>	798	121	8	765	126	<u>763</u>	<u>127</u>	763	127		
462.libquantum	8	<u>165</u>	<u>1010</u>	165	1010	165	1010	8	<u>165</u>	<u>1010</u>	165	1010	165	1010		
464.h264ref	8	818	216	821	216	<u>820</u>	<u>216</u>	8	806	220	803	220	<u>805</u>	<u>220</u>		
471.omnetpp	8	482	104	<u>483</u>	<u>103</u>	488	102	8	<u>470</u>	<u>106</u>	473	106	470	106		
473.astar	8	<u>588</u>	<u>95.6</u>	587	95.7	588	95.5	8	<u>588</u>	<u>95.6</u>	587	95.7	588	95.5		
483.xalancbmk	8	312	177	<u>312</u>	<u>177</u>	311	177	8	312	177	<u>312</u>	<u>177</u>	311	177		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

As tested, the system used a Supermicro CSE-732D4-500B chassis. The chassis is configured with a PWS-502-PQ power supply, 1 SNK-P0046A4 heatsink, as well as 1 FAN-0124L4 rear cooling fan.
Sysinfo program /usr/cpu2006/Docs/sysinfo
\$Rev: 6775 \$ \$Date:: 2011-08-16 # \$ 8787f7622badcf24e01c368b1db4377c
running on localhost Wed Sep 12 19:29:27 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo
model name : Intel(R) Core(TM) i7-3770T CPU @ 2.50GHz
1 "physical id"s (chips)
8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with
Continued on next page



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Platform Notes (Continued)

```

caution.)
  cpu cores : 4
  siblings  : 8
  physical 0: cores 0 1 2 3
  cache size : 8192 KB

From /proc/meminfo
MemTotal:      16406456 kB
HugePages_Total:    0
Hugepagesize:    2048 kB

/usr/bin/lsb_release -d
  Red Hat Enterprise Linux Server release 6.3 (Santiago)

From /etc/*release* /etc/*version*
  redhat-release: Red Hat Enterprise Linux Server release 6.3 (Santiago)
  system-release: Red Hat Enterprise Linux Server release 6.3 (Santiago)
  system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
  Linux localhost 2.6.32-279.el6.x86_64 #1 SMP Wed Jun 13 18:24:36 EDT 2012
  x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Sep 12 19:26

SPEC is set to: /usr/cpu2006
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/mapper/VolGroup-lv_root
                ext4      50G   9.9G   37G   22% /

(End of data from sysinfo program)

```

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/smartheap -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32
401.bzp2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
-auto-ilp32 -ansi-alias
403.gcc: -xAVX -ipo -O3 -no-prec-div
429.mcf: basepeak = yes
445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3
456.hmmcr: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4
-auto-ilp32
462.libquantum: basepeak = yes
464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias
-opt-ra-region-strategy=block -Wl,-z,muldefs
-L/smartheap -lsmartheap
473.astar: basepeak = yes
483.xalancbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.xml>

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For other inquiries, please contact webmaster@spec.org.

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