



# SPEC<sup>®</sup> CFP2006 Result

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## Huawei Huawei XH320

SPECfp<sup>®</sup>2006 = 42.4

SPECfp\_base2006 = 41.2

CPU2006 license: 3175

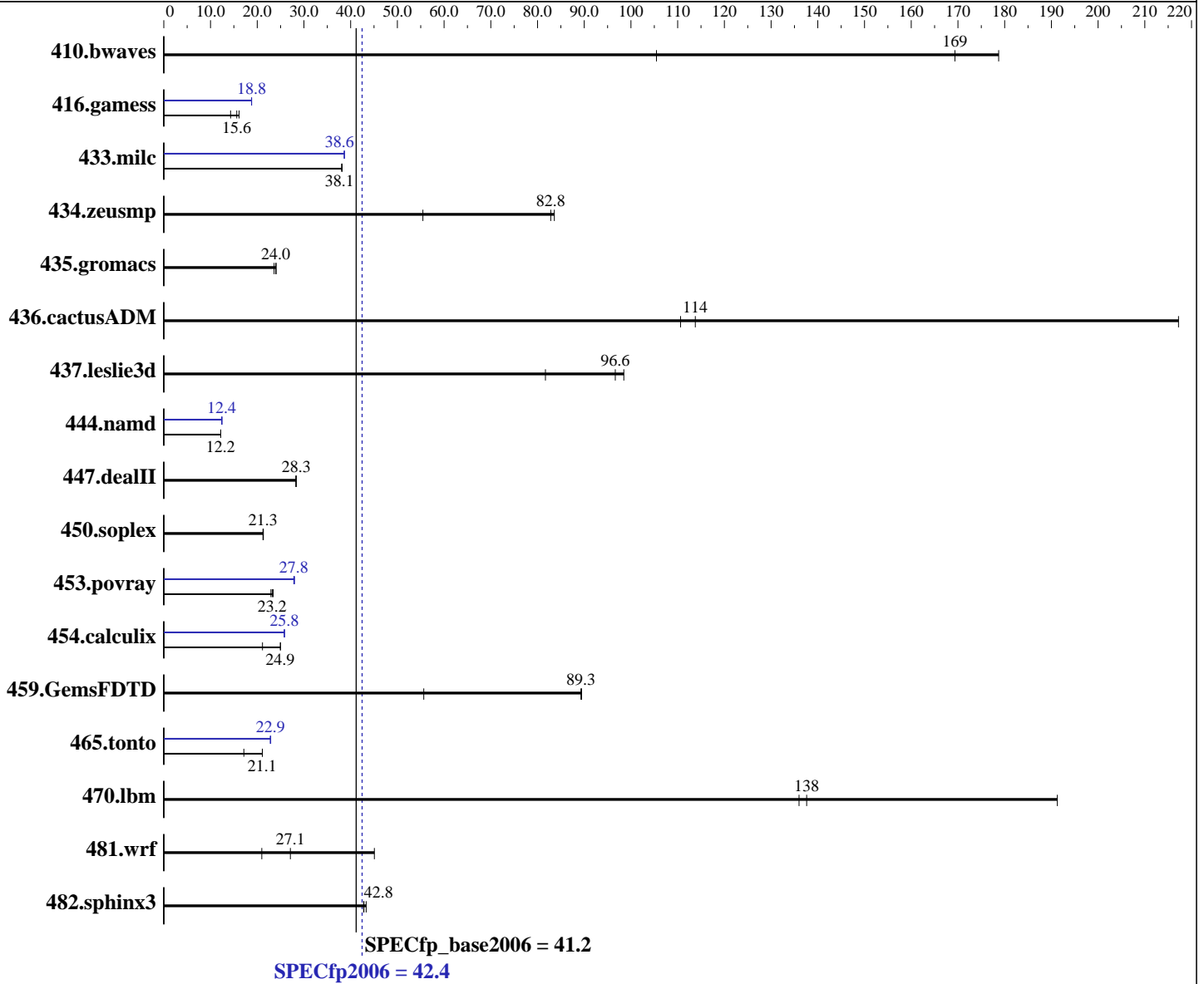
Test sponsor: Huawei

Tested by: Huawei

Test date: Apr-2014

Hardware Availability: May-2012

Software Availability: Nov-2013



### Hardware

CPU Name: Intel Xeon E5-2403  
 CPU Characteristics:  
 CPU MHz: 1800  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 2.6.32-431.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;  
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4

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L3 Cache: 10 MB I+D on chip per chip  
Other Cache: None  
Memory: 96 GB (12 x 8 GB 2Rx4 PC3L-12800R-11, ECC)  
Disk Subsystem: 1 x 300 GB SAS, 7200 RPM  
Other Hardware: None

System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	129	105	<b>80.2</b>	<b>169</b>	76.0	179	129	105	<b>80.2</b>	<b>169</b>	76.0	179
416.gamess	1214	16.1	<b>1253</b>	<b>15.6</b>	1370	14.3	<b>1042</b>	<b>18.8</b>	1041	18.8	1042	18.8
433.milc	<b>241</b>	<b>38.1</b>	241	38.2	241	38.1	238	38.6	237	38.7	<b>238</b>	<b>38.6</b>
434.zeusmp	164	55.5	109	83.6	<b>110</b>	<b>82.8</b>	164	55.5	109	83.6	<b>110</b>	<b>82.8</b>
435.gromacs	302	23.6	<b>298</b>	<b>24.0</b>	297	24.0	302	23.6	<b>298</b>	<b>24.0</b>	297	24.0
436.cactusADM	55.0	217	<b>105</b>	<b>114</b>	108	111	55.0	217	<b>105</b>	<b>114</b>	108	111
437.leslie3d	115	81.7	95.5	98.5	<b>97.3</b>	<b>96.6</b>	115	81.7	95.5	98.5	<b>97.3</b>	<b>96.6</b>
444.namd	658	12.2	658	12.2	<b>658</b>	<b>12.2</b>	645	12.4	646	12.4	<b>645</b>	<b>12.4</b>
447.dealII	405	28.3	404	28.3	<b>404</b>	<b>28.3</b>	405	28.3	404	28.3	<b>404</b>	<b>28.3</b>
450.soplex	393	21.2	391	21.3	<b>392</b>	<b>21.3</b>	393	21.2	391	21.3	<b>392</b>	<b>21.3</b>
453.povray	<b>229</b>	<b>23.2</b>	227	23.4	232	22.9	191	27.8	<b>191</b>	<b>27.8</b>	190	28.0
454.calculix	330	25.0	<b>331</b>	<b>24.9</b>	391	21.1	320	25.8	319	25.8	<b>320</b>	<b>25.8</b>
459.GemsFDTD	119	89.4	<b>119</b>	<b>89.3</b>	191	55.7	119	89.4	<b>119</b>	<b>89.3</b>	191	55.7
465.tonto	466	21.1	<b>467</b>	<b>21.1</b>	574	17.1	<b>431</b>	<b>22.9</b>	430	22.9	432	22.8
470.lbm	71.8	191	101	136	<b>99.8</b>	<b>138</b>	71.8	191	101	136	<b>99.8</b>	<b>138</b>
481.wrf	<b>412</b>	<b>27.1</b>	248	45.1	532	21.0	<b>412</b>	<b>27.1</b>	248	45.1	532	21.0
482.sphinx3	456	42.8	450	43.3	<b>455</b>	<b>42.8</b>	456	42.8	450	43.3	<b>455</b>	<b>42.8</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
Set Power Efficiency Mode to Performance  
Sysinfo program /spec/config/sysinfo.rev6818  
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191  
running on rhel65x64x2.huawei.com Tue Apr 15 02:40:45 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

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**Test date:** Apr-2014  
**Hardware Availability:** May-2012  
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## Platform Notes (Continued)

```

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2403 0 @ 1.80GHz
 2 "physical id"s (chips)
 8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 4
  siblings  : 4
  physical 0: cores 0 1 2 3
  physical 1: cores 0 1 2 3
cache size : 10240 KB

From /proc/meminfo
MemTotal:      99010156 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
Linux rhel65x64x2.huawei.com 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54
EST 2013 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Apr 14 15:55

SPEC is set to: /spec
Filesystem      Type      Size      Used Avail Use% Mounted on
/dev/sdal       ext4      49G       11G   36G   23% /

Additional information from dmidecode:
BIOS Insyde Corp. RMIBV372 12/21/2013
Memory:
 12x Samsung M393B1K70QB0-YK0 8 GB 1066 MHz 2 rank

(End of data from sysinfo program)

```

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"  
OMP\_NUM\_THREADS = "8"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB  
memory using RedHat EL 6.4

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## General Notes (Continued)

Transparent Huge Pages enabled with:  
 echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
 runspec command invoked through numactl i.e.:  
 numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:  
 icc -m64

C++ benchmarks:  
 icpc -m64

Fortran benchmarks:  
 ifort -m64

Benchmarks using both Fortran and C:  
 icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:  
 -xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

C++ benchmarks:  
 -xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

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## Base Optimization Flags (Continued)

Fortran benchmarks:  
 -xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:  
 -xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

## Peak Compiler Invocation

C benchmarks:  
 icc -m64

C++ benchmarks:  
 icpc -m64

Fortran benchmarks:  
 ifort -m64

Benchmarks using both Fortran and C:  
 icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32  
 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
 -auto-ilp32

447.dealIII: basepeak = yes

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## Peak Optimization Flags (Continued)

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>  
<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>  
<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml>



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