



SPEC® CINT2006 Result

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NEC Corporation

SPECint®_rate2006 = 71.7

Express5800/GT110g (Intel Celeron G1840)

SPECint_rate_base2006 = 69.2

CPU2006 license: 9006

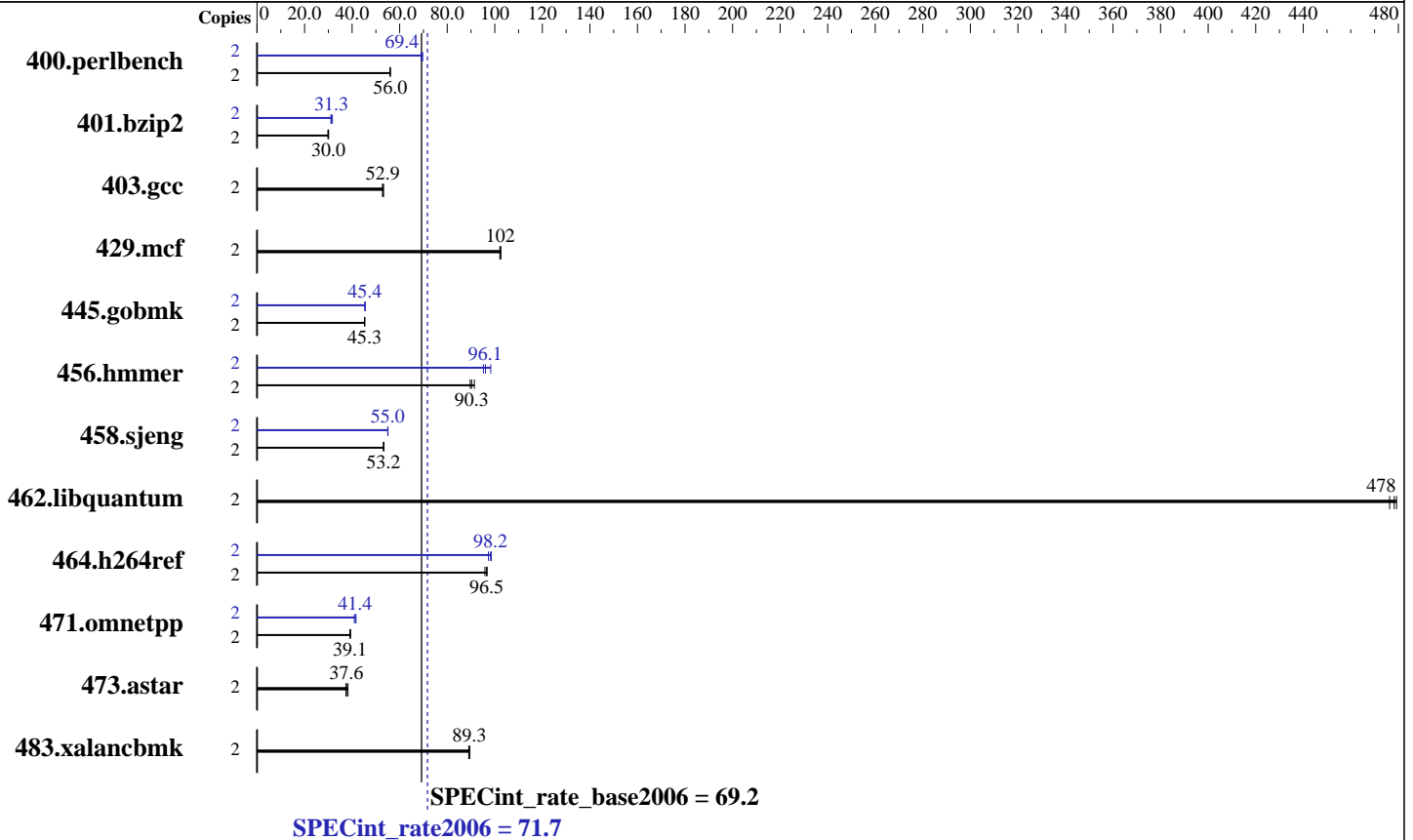
Test date: Aug-2014

Test sponsor: NEC Corporation

Hardware Availability: Jul-2014

Tested by: NEC Corporation

Software Availability: Jan-2014



Hardware

CPU Name: Intel Celeron G1840
 CPU Characteristics:
 CPU MHz: 2800
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 2 MB I+D on chip per chip
 Other Cache: None
 Memory: 16 GB (2 x 8 GB 2Rx8 PC3-12800E-11, ECC, running at 1333 MHz and CL9)
 Disk Subsystem: 1 x 500 GB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
 Kernel 2.6.32-431.el6.x86_64
 Compiler: C/C++: Version 14.0.2.144 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	349	55.9	<u>349</u>	<u>56.0</u>	348	56.1	2	283	69.0	<u>282</u>	<u>69.4</u>	280	69.7
401.bzip2	2	641	30.1	648	29.8	<u>642</u>	<u>30.0</u>	2	<u>617</u>	<u>31.3</u>	609	31.7	621	31.1
403.gcc	2	302	53.2	305	52.8	<u>305</u>	<u>52.9</u>	2	302	53.2	305	52.8	<u>305</u>	<u>52.9</u>
429.mcf	2	178	103	179	102	<u>178</u>	<u>102</u>	2	178	103	179	102	<u>178</u>	<u>102</u>
445.gobmk	2	<u>463</u>	<u>45.3</u>	463	45.3	464	45.2	2	462	45.4	462	45.4	<u>462</u>	<u>45.4</u>
456.hammer	2	204	91.4	<u>207</u>	<u>90.3</u>	208	89.7	2	<u>194</u>	<u>96.1</u>	190	98.3	196	95.2
458.sjeng	2	453	53.4	<u>455</u>	<u>53.2</u>	455	53.2	2	<u>440</u>	<u>55.0</u>	439	55.1	440	55.0
462.libquantum	2	86.5	479	<u>86.6</u>	<u>478</u>	87.0	476	2	86.5	479	<u>86.6</u>	<u>478</u>	87.0	476
464.h264ref	2	<u>459</u>	<u>96.5</u>	457	96.8	462	95.9	2	<u>451</u>	<u>98.2</u>	449	98.5	455	97.3
471.omnetpp	2	<u>320</u>	<u>39.1</u>	318	39.4	320	39.1	2	<u>302</u>	<u>41.4</u>	306	40.9	300	41.6
473.astar	2	376	37.4	367	38.2	<u>373</u>	<u>37.6</u>	2	376	37.4	367	38.2	<u>373</u>	<u>37.6</u>
483.xalancbmk	2	155	89.1	<u>154</u>	<u>89.3</u>	154	89.5	2	155	89.1	<u>154</u>	<u>89.3</u>	154	89.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:
Energy Performance: Performance

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>



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Base Compiler Invocation

C benchmarks:

icc -m32

C++ benchmarks:

icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32



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Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
 401.bzip2: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LINUX
 483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
 -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
 -L/sh -lsmartheap

473.astar: basepeak = yes

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Peak Optimization Flags (Continued)

483.xalanbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120-RevB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120-RevB.xml>

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For other inquiries, please contact webmaster@spec.org.

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