



# SPEC<sup>®</sup> CFP2006 Result

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## Fujitsu

PRIMERGY TX1330 M1, Intel Xeon E3-1231 v3, 3.40 GHz

SPECfp<sup>®</sup>\_rate2006 = 151

SPECfp\_rate\_base2006 = 146

CPU2006 license: 19

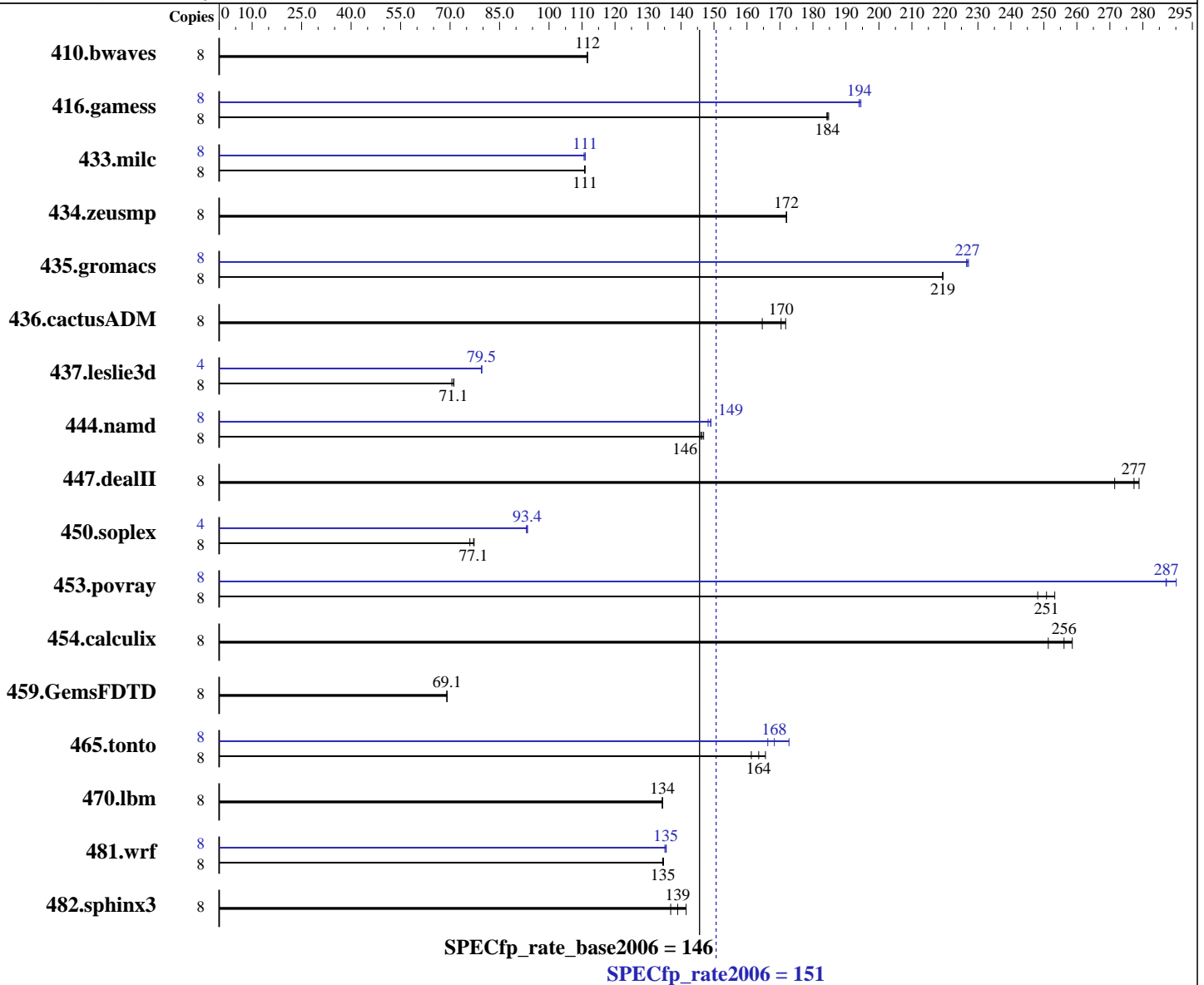
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jul-2014

Hardware Availability: Jul-2014

Software Availability: Nov-2013



### Hardware

CPU Name: Intel Xeon E3-1231 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.80 GHz  
 CPU MHz: 3400  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 2.6.32-431.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;  
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4

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L3 Cache: 8 MB I+D on chip per chip  
Other Cache: None  
Memory: 32 GB (4 x 8 GB 2Rx8 PC3L-12800E-11, ECC)  
Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM  
Other Hardware: None

System State: Run level 5 (multi-user)  
Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	8	974	112	974	112	<b>974</b>	<b>112</b>	8	974	112	974	112	<b>974</b>	<b>112</b>		
416.gamess	8	850	184	848	185	<b>849</b>	<b>184</b>	8	<b>807</b>	<b>194</b>	805	195	808	194		
433.milc	8	662	111	<b>662</b>	<b>111</b>	663	111	8	662	111	<b>662</b>	<b>111</b>	664	111		
434.zeusmp	8	423	172	423	172	<b>423</b>	<b>172</b>	8	423	172	423	172	<b>423</b>	<b>172</b>		
435.gromacs	8	<b>260</b>	<b>219</b>	260	219	260	219	8	<b>252</b>	<b>227</b>	252	227	251	227		
436.cactusADM	8	557	172	581	165	<b>561</b>	<b>170</b>	8	557	172	581	165	<b>561</b>	<b>170</b>		
437.leslie3d	8	<b>1058</b>	<b>71.1</b>	1057	71.2	1066	70.5	4	<b>473</b>	<b>79.5</b>	473	79.5	472	79.6		
444.namd	8	439	146	437	147	<b>438</b>	<b>146</b>	8	<b>431</b>	<b>149</b>	430	149	433	148		
447.dealII	8	328	279	<b>330</b>	<b>277</b>	337	271	8	328	279	<b>330</b>	<b>277</b>	337	271		
450.soplex	8	878	76.0	863	77.3	<b>865</b>	<b>77.1</b>	4	<b>357</b>	<b>93.4</b>	357	93.5	358	93.1		
453.povray	8	<b>170</b>	<b>251</b>	172	248	168	253	8	147	290	<b>148</b>	<b>287</b>	148	287		
454.calculix	8	255	259	<b>258</b>	<b>256</b>	263	251	8	255	259	<b>258</b>	<b>256</b>	263	251		
459.GemsFDTD	8	<b>1229</b>	<b>69.1</b>	1229	69.0	1229	69.1	8	<b>1229</b>	<b>69.1</b>	1229	69.0	1229	69.1		
465.tonto	8	475	166	488	161	<b>481</b>	<b>164</b>	8	456	173	<b>468</b>	<b>168</b>	473	166		
470.lbm	8	<b>818</b>	<b>134</b>	818	134	818	134	8	<b>818</b>	<b>134</b>	818	134	818	134		
481.wrf	8	<b>664</b>	<b>135</b>	663	135	665	134	8	659	136	<b>660</b>	<b>135</b>	661	135		
482.sphinx3	8	1139	137	<b>1122</b>	<b>139</b>	1101	142	8	1139	137	<b>1122</b>	<b>139</b>	1101	142		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64:/SPECcpu2006/sh"

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### General Notes (Continued)

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1> /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>  
For information about Fujitsu please visit: <http://www.fujitsu.com>

### Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

### Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64



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## Base Optimization Flags

### C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

### C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

### Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

### Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

## Peak Compiler Invocation

### C benchmarks:

icc -m64

### C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

### Fortran benchmarks:

ifort -m64

### Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Peak Optimization Flags

### C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2)  
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)  
-auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

### C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2)  
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2)  
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)  
-opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2)  
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4  
-ansi-alias

### Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

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## Peak Optimization Flags (Continued)

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4  
-auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2)  
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20130924.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20130924.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.

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