SPEC® CFP2006 Result

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Dell Inc.

PowerEdge R920 (Intel Xeon E7-2850 v2, 2.30 GHz)

$SPECfp^{®}\_rate2006 = \quad 627$

$SPECfp\_rate\_base2006 = \quad 614$

CPU2006 license: 55	**Test date:** Sep-2014
Test sponsor: Dell Inc.	**Hardware Availability:** Mar-2014
Tested by: Dell Inc.	**Software Availability:** Nov-2013

410.bwaves 48 — 536
416.gamess 48 — 703 / 48 — 691
433.milc 48 — 525 / 48 — 525
434.zeusmp 48 — 663
435.gromacs 48 — 815 / 48 — 808
436.cactusADM 48 — 726
437.leslie3d 24 — 385 / 48 — 346
444.namd 48 — 541 / 48 — 529
447.dealII 48 — 1150
450.soplex 24 — 358 / 48 — 346
453.povray 48 — 1080 / 48 — 954
454.calculix 48 — 973
459.GemsFDTD 48 — 322
465.tonto 48 — 690 / 48 — 665
470.lbm 48 — 629
481.wrf 48 — 636 / 48 — 630
482.sphinx3 48 — 575

SPECfp_rate_base2006 = 614

SPECfp_rate2006 = 627

Hardware

CPU Name:	Intel Xeon E7-2850 v2
CPU Characteristics:	Intel Turbo Boost Technology up to 2.80 GHz
CPU MHz:	2300
FPU:	Integrated
CPU(s) enabled:	24 cores, 2 chips, 12 cores/chip, 2 threads/core
CPU(s) orderable:	2 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) 3.0.76-0.11-default
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux; Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
Auto Parallel:	No
File System:	ext2
System State:	Run level 3 (multi-user)

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CPU2006 license: 55
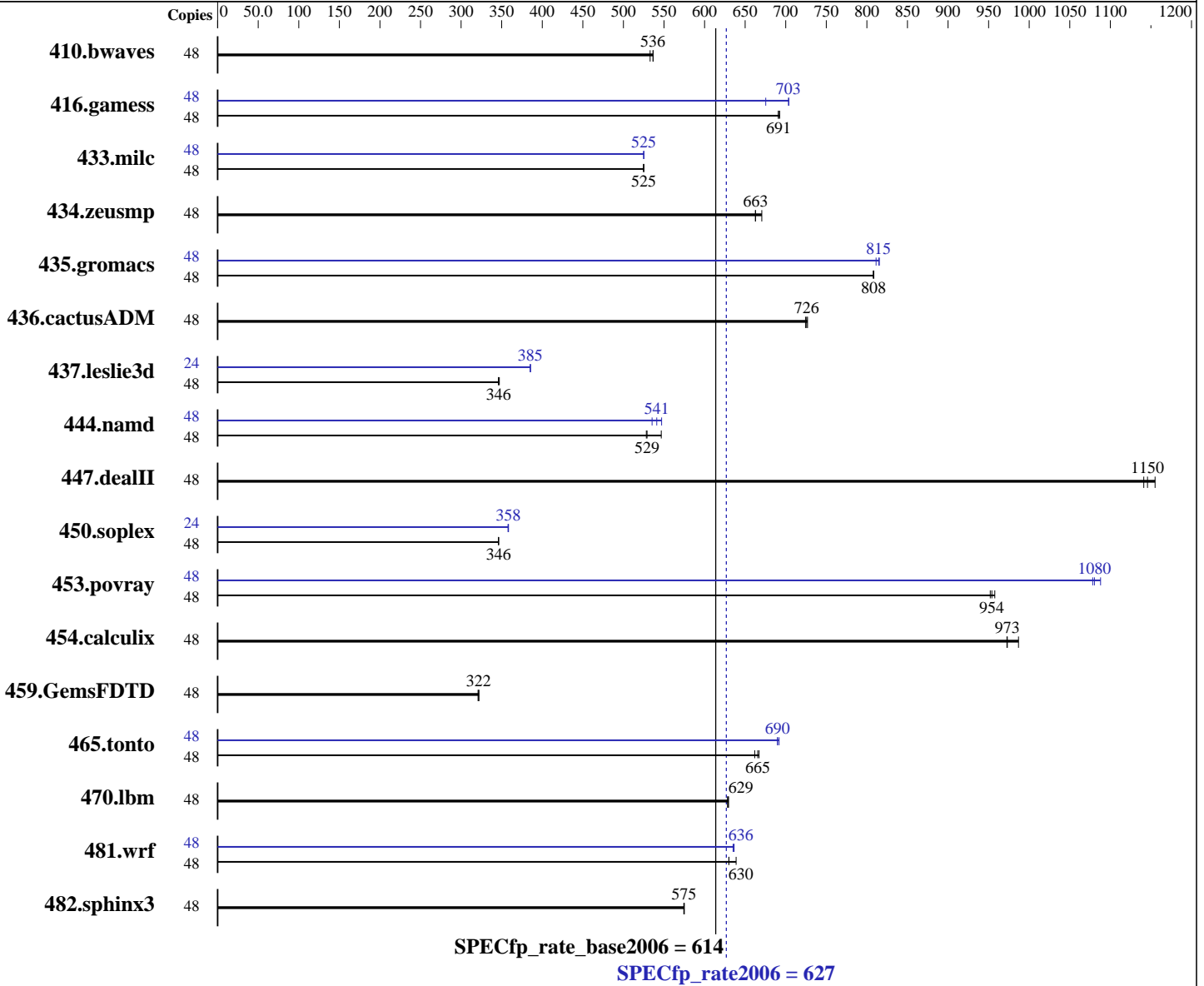**Test sponsor:** Dell Inc.
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L3 Cache:	24 MB I+D on chip per chip
Other Cache:	None
Memory:	512 GB (32 x 16 GB 2Rx4 PC3L-12800R-11, ECC, running at 1066 MHz)
Disk Subsystem:	1 x 300 GB 15000 RPM SAS
Other Hardware:	None

Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	None

Results Table

Benchmark	Copies	Base Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Peak Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	48	**1216**	**536**	1216	537	1224	533	48	**1216**	**536**	1216	537	1224	533
416.gamess	48	1357	693	1360	691	**1360**	**691**	48	**1336**	**703**	1392	675	1335	704
433.milc	48	840	525	839	525	**839**	**525**	48	839	525	839	525	**839**	**525**
434.zeusmp	48	659	663	**659**	**663**	651	671	48	659	663	**659**	**663**	651	671
435.gromacs	48	**424**	**808**	424	809	424	808	48	422	811	420	815	**421**	**815**
436.cactusADM	48	789	727	**790**	**726**	792	724	48	789	727	**790**	**726**	792	724
437.leslie3d	48	1304	346	**1303**	**346**	1300	347	24	**585**	**385**	586	385	585	385
444.namd	48	704	547	**728**	**529**	729	528	48	**711**	**541**	704	547	719	535
447.dealII	48	475	1160	**479**	**1150**	481	1140	48	475	1160	**479**	**1150**	481	1140
450.soplex	48	1156	346	**1156**	**346**	1157	346	24	559	358	**559**	**358**	558	358
453.povray	48	**268**	**954**	267	958	268	952	48	235	1090	237	1080	**236**	**1080**
454.calculix	48	401	987	407	973	**407**	**973**	48	401	987	407	973	**407**	**973**
459.GemsFDTD	48	1587	321	**1584**	**322**	1582	322	48	1587	321	**1584**	**322**	1582	322
465.tonto	48	**710**	**665**	714	662	708	667	48	683	692	685	690	**685**	**690**
470.lbm	48	**1049**	**629**	1050	628	1048	630	48	**1049**	**629**	1050	628	1048	630
481.wrf	48	**851**	**630**	839	639	851	630	48	**843**	**636**	844	635	843	636
482.sphinx3	48	1627	575	**1628**	**575**	1629	574	48	1627	575	**1628**	**575**	1629	574

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS settings:
Virtualization Technology disabled
Execute Disable disabled

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Platform Notes (Continued)

```
System Profile set to Performance
Sysinfo program /root/CPU2006-1.2/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on R920-SL11SP3 Wed Sep 10 00:13:57 2014

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E7-2850 v2 @ 2.30GHz
      2 "physical id"s (chips)
      48 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 12
      siblings  : 24
      physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
      physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
   cache size : 24576 KB

From /proc/meminfo
   MemTotal:        529357260 kB
   HugePages_Total:        0
   Hugepagesize:        2048 kB

/usr/bin/lsb_release -d
   SUSE Linux Enterprise Server 11 (x86_64)

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 11 (x86_64)
      VERSION = 11
      PATCHLEVEL = 3

uname -a:
   Linux R920-SL11SP3 3.0.76-0.11-default #1 SMP Fri Jun 14 08:21:43 UTC 2013
   (ccab990) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Sep 9 08:49 last=S

SPEC is set to: /root/CPU2006-1.2
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/sda1      ext2  266G   14G  252G   6% /

Additional information from dmidecode:
  BIOS Dell Inc. 1.2.2 05/05/2014
  Memory:
   32x 00AD04B300AD HMT42GR7AFR4A-PB 16 GB 1066 MHz

(End of data from sysinfo program)
```

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General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/root/CPU2006-1.2/libs/32:/root/CPU2006-1.2/libs/64:/root/CPU2006-1.2/sh"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RedHat EL 6.4
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>        /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
```
    icc   -m64
```

C++ benchmarks:
```
    icpc  -m64
```

Fortran benchmarks:
```
    ifort -m64
```

Benchmarks using both Fortran and C:
```
    icc   -m64 ifort -m64
```

Base Portability Flags

```
   410.bwaves: -DSPEC_CPU_LP64
   416.gamess: -DSPEC_CPU_LP64
     433.milc: -DSPEC_CPU_LP64
   434.zeusmp: -DSPEC_CPU_LP64
  435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
  437.leslie3d: -DSPEC_CPU_LP64
     444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
   454.calculix: -DSPEC_CPU_LP64 -nofor_main
  459.GemsFDTD: -DSPEC_CPU_LP64
     465.tonto: -DSPEC_CPU_LP64
      470.lbm: -DSPEC_CPU_LP64
       481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
    482.sphinx3: -DSPEC_CPU_LP64
```

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Base Optimization Flags

C benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3
```

C++ benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3
```

Fortran benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:
```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3
```

Peak Compiler Invocation

C benchmarks:
```
icc     -m64
```

C++ benchmarks (except as noted below):
```
icpc    -m64
```
```
    450.soplex: icpc -m32
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc     -m64 ifort -m64
```

Peak Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
   435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
   437.leslie3d: -DSPEC_CPU_LP64
      444.namd: -DSPEC_CPU_LP64
     447.dealII: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
   454.calculix: -DSPEC_CPU_LP64 -nofor_main
  459.GemsFDTD: -DSPEC_CPU_LP64
```

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Peak Portability Flags (Continued)

```
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Peak Optimization Flags

C benchmarks:

```
433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
          -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
          -prof-use(pass 2) -auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes
```

C++ benchmarks:

```
444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
          -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
          -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
            -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
            -prof-use(pass 2) -unroll4 -ansi-alias
```

Fortran benchmarks:

```
410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
            -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes
```

Continued on next page

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Peak Optimization Flags (Continued)

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xAVX -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.html
http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.html

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You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.xml
http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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