



SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge R630 (Intel Xeon E5-2609 v3, 1.90 GHz)

SPECfp®_rate2006 = 326

SPECfp_rate_base2006 = 320

CPU2006 license: 55

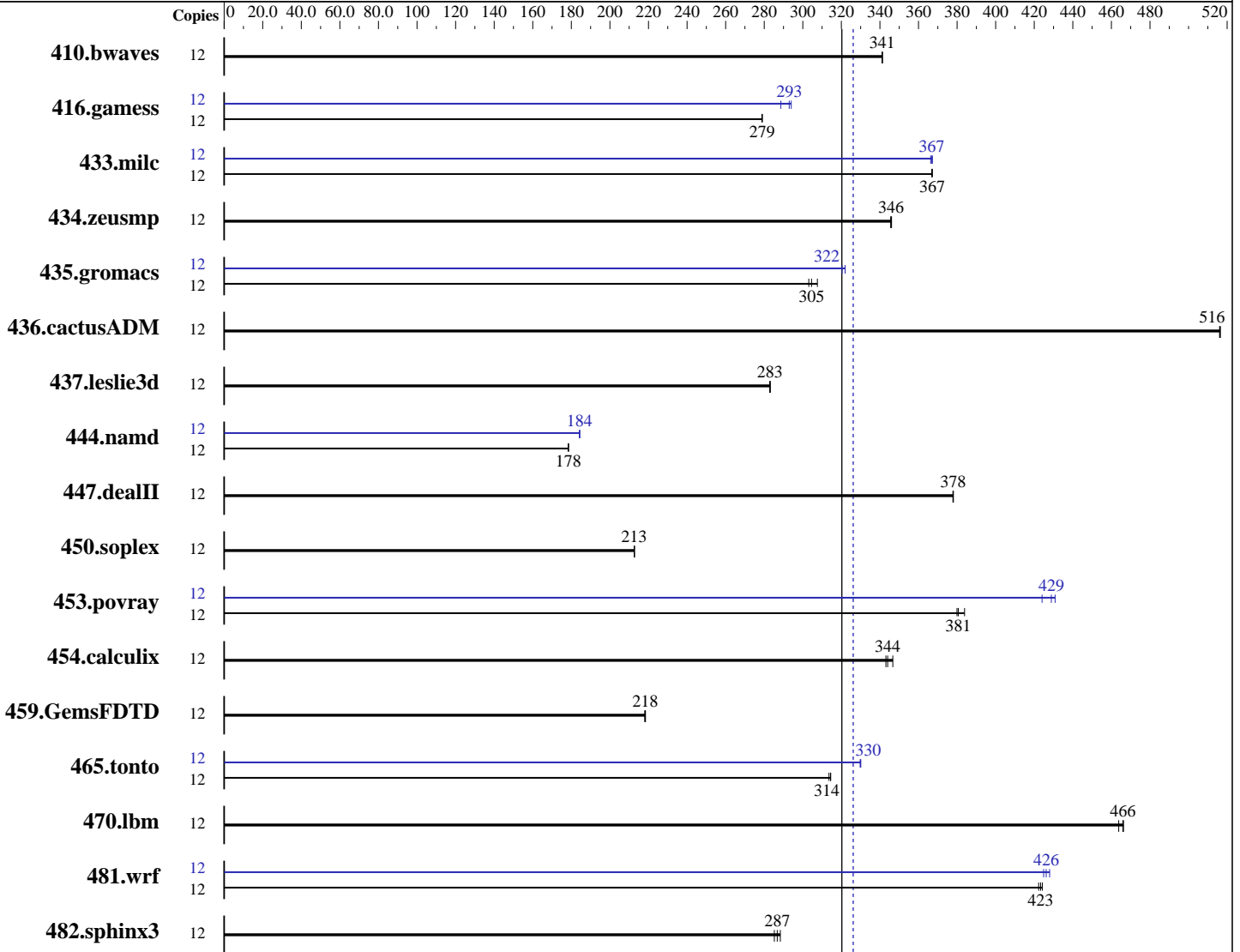
Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Sep-2014

Hardware Availability: Sep-2014

Software Availability: Nov-2013



SPECfp_rate_base2006 = 320

SPECfp_rate2006 = 326

Hardware

CPU Name: Intel Xeon E5-2609 v3
 CPU Characteristics:
 CPU MHz: 1900
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
 2.6.32-431.el6.x86_64
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: ext4

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge R630 (Intel Xeon E5-2609 v3, 1.90 GHz)

SPECfp_rate2006 = 326

SPECfp_rate_base2006 = 320

CPU2006 license: 55

Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Sep-2014

Hardware Availability: Sep-2014

Software Availability: Nov-2013

L3 Cache: 15 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)
Disk Subsystem: 1 x 300 GB 15000 RPM SAS
Other Hardware: None

System State: Run level 5 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	12	478	341	478	341	478	341	12	478	341	478	341	478	341		
416.gamess	12	842	279	842	279	843	279	12	814	289	802	293	799	294		
433.milc	12	300	367	300	367	300	367	12	300	367	301	366	300	367		
434.zeusmp	12	316	346	316	346	316	346	12	316	346	316	346	316	346		
435.gromacs	12	283	303	279	308	281	305	12	266	322	266	322	267	320		
436.cactusADM	12	278	516	278	516	278	516	12	278	516	278	516	278	516		
437.leslie3d	12	398	283	399	283	399	283	12	398	283	399	283	399	283		
444.namd	12	539	179	539	178	540	178	12	522	184	522	184	523	184		
447.dealII	12	363	378	363	378	363	378	12	363	378	363	378	363	378		
450.soplex	12	470	213	471	213	471	213	12	470	213	471	213	471	213		
453.povray	12	168	380	166	384	168	381	12	148	431	149	429	151	424		
454.calculix	12	288	344	289	343	286	347	12	288	344	289	343	286	347		
459.GemsFDTD	12	583	218	584	218	583	218	12	583	218	584	218	583	218		
465.tonto	12	377	314	375	314	376	314	12	358	330	358	330	358	330		
470.lbm	12	356	464	354	466	354	466	12	356	464	354	466	354	466		
481.wrf	12	316	424	317	422	317	423	12	313	428	315	425	314	426		
482.sphinx3	12	816	287	820	285	811	288	12	816	287	820	285	811	288		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS settings:
Snoop Mode set to Early Snoop
Virtualization Technology Disabled

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp_rate2006 = 326

PowerEdge R630 (Intel Xeon E5-2609 v3, 1.90 GHz)

SPECfp_rate_base2006 = 320

CPU2006 license: 55

Test date: Sep-2014

Test sponsor: Dell Inc.

Hardware Availability: Sep-2014

Tested by: Dell Inc.

Software Availability: Nov-2013

Platform Notes (Continued)

```

Execute Disable disabled
System Profile set to Performance
Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on localhost.localdomain Thu Sep 11 09:16:58 2014

```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2609 v3 @ 1.90GHz
 2 "physical id"s (chips)
 12 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 6
  siblings  : 6
  physical 0: cores 0 1 2 3 4 5
  physical 1: cores 0 1 2 3 4 5
cache size : 15360 KB

```

```

From /proc/meminfo
MemTotal:      264568684 kB
HugePages_Total:    0
Hugepagesize:   2048 kB

```

```

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)

```

```

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

```

```

uname -a:
Linux localhost.localdomain 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54
EST 2013 x86_64 x86_64 x86_64 GNU/Linux

```

run-level 5 Sep 10 23:35

```

SPEC is set to: /root/cpu2006-1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       ext4  222G   14G  197G   7% /

```

Additional information from dmidecode:

BIOS Dell Inc. 1.0.4 08/28/2014

Memory:

```

2x 00AD00B300AD HMA42GR7MFR4N-TFTD 16 GB 1600 MHz 2 rank
9x 00AD063200AD HMA42GR7MFR4N-TFT1 16 GB 1600 MHz 2 rank
5x 00CE00B300CE M393A2G40DB0-CPB 16 GB 1600 MHz 2 rank

```

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp_rate2006 = 326

PowerEdge R630 (Intel Xeon E5-2609 v3,
1.90 GHz)

SPECfp_rate_base2006 = 320

CPU2006 license: 55

Test date: Sep-2014

Test sponsor: Dell Inc.

Hardware Availability: Sep-2014

Tested by: Dell Inc.

Software Availability: Nov-2013

Platform Notes (Continued)

8x Not Specified Not Specified

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64:/root/cpu2006-1.2/sh"
```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1> /proc/sys/vm/drop_caches
```

runspec command invoked through numactl i.e.:

```
numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
```

```
416.gamess: -DSPEC_CPU_LP64
```

```
433.milc: -DSPEC_CPU_LP64
```

```
434.zeusmp: -DSPEC_CPU_LP64
```

```
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
```

```
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
```

```
437.leslie3d: -DSPEC_CPU_LP64
```

```
444.namd: -DSPEC_CPU_LP64
```

```
447.deallI: -DSPEC_CPU_LP64
```

```
450.soplex: -DSPEC_CPU_LP64
```

```
453.povray: -DSPEC_CPU_LP64
```

```
454.calculix: -DSPEC_CPU_LP64 -nofor_main
```

Continued on next page

Standard Performance Evaluation Corporation

info@spec.org

http://www.spec.org/

Page 4



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp_rate2006 = 326

PowerEdge R630 (Intel Xeon E5-2609 v3,
1.90 GHz)

SPECfp_rate_base2006 = 320

CPU2006 license: 55

Test date: Sep-2014

Test sponsor: Dell Inc.

Hardware Availability: Sep-2014

Tested by: Dell Inc.

Software Availability: Nov-2013

Base Portability Flags (Continued)

459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge R630 (Intel Xeon E5-2609 v3,
1.90 GHz)

SPECfp_rate2006 = 326

SPECfp_rate_base2006 = 320

CPU2006 license: 55

Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Sep-2014

Hardware Availability: Sep-2014

Software Availability: Nov-2013

Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
-auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
-auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge R630 (Intel Xeon E5-2609 v3,
1.90 GHz)

SPECfp_rate2006 = 326

SPECfp_rate_base2006 = 320

CPU2006 license: 55

Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Sep-2014

Hardware Availability: Sep-2014

Software Availability: Nov-2013

Peak Optimization Flags (Continued)

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Wed Oct 8 19:40:57 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 8 October 2014.