



SPEC® CINT2006 Result

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Supermicro

SuperServer 6028TP-HTR
(X10DRT-P, Intel Xeon E5-2667 v3)

SPECint®_rate2006 = 843

SPECint_rate_base2006 = 809

CPU2006 license: 001176

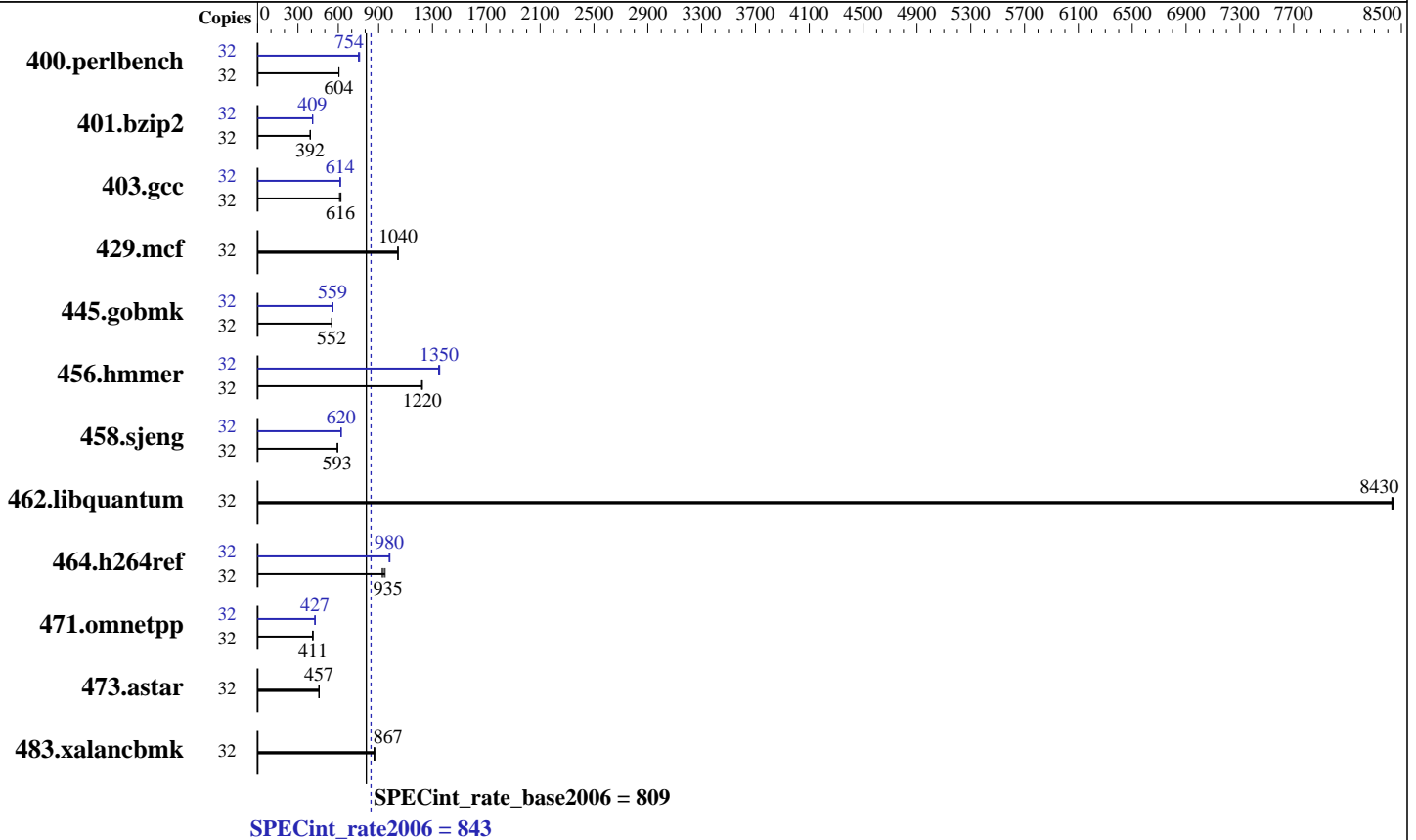
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Oct-2014

Hardware Availability: Sep-2014

Software Availability: Sep-2014



Hardware

CPU Name: Intel Xeon E5-2667 v3
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz
 CPU MHz: 3200
 FPU: Integrated
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 20 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)
 Disk Subsystem: 1 x 2000 GB SATA III, 7200 RPM
 Other Hardware: None

Software

Operating System: CentOS Linux release 7.0,
Kernel 3.10.0-123.8.1.el7.x86_64
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.0



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	32	<u>517</u>	<u>604</u>	517	605	518	604	32	<u>415</u>	<u>754</u>	412	759	417	750
401.bzip2	32	787	392	789	391	<u>789</u>	<u>392</u>	32	754	409	756	409	<u>755</u>	<u>409</u>
403.gcc	32	416	620	423	609	<u>418</u>	<u>616</u>	32	418	616	<u>420</u>	<u>614</u>	421	613
429.mcf	32	279	1040	280	1040	<u>280</u>	<u>1040</u>	32	279	1040	280	1040	<u>280</u>	<u>1040</u>
445.gobmk	32	<u>608</u>	<u>552</u>	608	552	608	552	32	601	558	600	559	<u>601</u>	<u>559</u>
456.hammer	32	245	1220	<u>244</u>	<u>1220</u>	244	1220	32	<u>221</u>	<u>1350</u>	221	1350	222	1350
458.sjeng	32	653	593	<u>653</u>	<u>593</u>	650	596	32	625	620	623	622	<u>624</u>	<u>620</u>
462.libquantum	32	78.7	8430	78.6	8440	<u>78.6</u>	<u>8430</u>	32	78.7	8430	78.6	8440	<u>78.6</u>	<u>8430</u>
464.h264ref	32	747	948	764	926	<u>758</u>	<u>935</u>	32	<u>722</u>	<u>980</u>	720	984	724	978
471.omnetpp	32	490	409	485	412	<u>486</u>	<u>411</u>	32	468	428	<u>468</u>	<u>427</u>	470	426
473.astar	32	<u>491</u>	<u>457</u>	491	457	491	457	32	<u>491</u>	<u>457</u>	491	457	491	457
483.xalancbmk	32	255	867	<u>255</u>	<u>867</u>	253	872	32	255	867	<u>255</u>	<u>867</u>	253	872

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:
Early Snoop = Disable
Enforce POR = Disable

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/Trial/SPEC2006_v11/libs/32:/home/Trial/SPEC2006_v11/libs/64:/home/Trial/SPEC2006_v11/sh"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled

Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches

runspec command invoked through numactl i.e.:

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General Notes (Continued)

numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:

icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

C++ benchmarks:

icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

400.perlbench: icc -m64

401.bzip2: icc -m64

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Peak Compiler Invocation (Continued)

456.hmmr: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:

`icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`

401.bzip2: `-DSPEC_CPU_LP64`

456.hmmr: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LINUX`

483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32`

401.bzip2: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias`

403.gcc: `-xCORE-AVX2 -ipo -O3 -no-prec-div`

429.mcf: `basepeak = yes`

445.gobmk: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3`

456.hmmr: `-xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`

458.sjeng: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32`

462.libquantum: `basepeak = yes`

464.h264ref: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias`

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Peak Optimization Flags (Continued)

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>
<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>
<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revE.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Wed Nov 12 10:06:54 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 6 November 2014.