



SPEC[®] CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SuperStorage Server 5048R-E1CR36L
(X10SRH-CF/CLN4F, Intel Xeon E5-2697 v3)

SPECfp[®]2006 = **103**

SPECfp_base2006 = **99.4**

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: May-2014

Hardware Availability: Sep-2014

Software Availability: Nov-2013



Hardware

CPU Name: Intel Xeon E5-2697 v3
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz
 CPU MHz: 2600
 FPU: Integrated
 CPU(s) enabled: 14 cores, 1 chip, 14 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 6.5,
Kernel 2.6.32-431.el6.x86_64
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE
for Linux;
Fortran: Version 14.0.0.080 of Intel Fortran
Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4
 System State: Run level 3 (multi-user)

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SuperStorage Server 5048R-E1CR36L
(X10SRH-CF/CLN4F, Intel Xeon E5-2697 v3)

SPECfp2006 = **103**

SPECfp_base2006 = **99.4**

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: May-2014

Hardware Availability: Sep-2014

Software Availability: Nov-2013

L3 Cache: 35 MB I+D on chip per chip
Other Cache: None
Memory: 128 GB (8 x 16 GB 2Rx4 PC4-2133P-R)
Disk Subsystem: 1 x 1000 GB SATA II, 7200 RPM
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---------------|-------------|-------------|-------------|------------|-------------|-------------|-------------|-------------|-------------|------------|-------------|-------------|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 41.4 | 328 | 41.6 | 326 | 41.6 | 326 | 41.4 | 328 | 41.6 | 326 | 41.6 | 326 |
| 416.gamess | 545 | 35.9 | 545 | 35.9 | 544 | 36.0 | 462 | 42.4 | 464 | 42.2 | 459 | 42.6 |
| 433.milc | 120 | 76.6 | 120 | 76.3 | 120 | 76.7 | 120 | 76.5 | 120 | 76.8 | 120 | 76.4 |
| 434.zeusmp | 40.8 | 223 | 40.8 | 223 | 40.8 | 223 | 40.8 | 223 | 40.8 | 223 | 40.8 | 223 |
| 435.gromacs | 169 | 42.3 | 165 | 43.2 | 165 | 43.2 | 169 | 42.3 | 165 | 43.2 | 165 | 43.2 |
| 436.cactusADM | 16.2 | 738 | 16.4 | 729 | 16.2 | 738 | 16.2 | 738 | 16.4 | 729 | 16.2 | 738 |
| 437.leslie3d | 35.4 | 265 | 35.4 | 265 | 35.6 | 264 | 35.4 | 265 | 35.4 | 265 | 35.6 | 264 |
| 444.namd | 284 | 28.2 | 284 | 28.2 | 284 | 28.2 | 275 | 29.2 | 275 | 29.2 | 275 | 29.1 |
| 447.dealII | 192 | 59.5 | 195 | 58.6 | 192 | 59.5 | 192 | 59.5 | 195 | 58.6 | 192 | 59.5 |
| 450.soplex | 181 | 46.0 | 183 | 45.7 | 180 | 46.3 | 181 | 46.0 | 183 | 45.7 | 180 | 46.3 |
| 453.povray | 93.4 | 57.0 | 92.6 | 57.5 | 94.0 | 56.6 | 84.9 | 62.6 | 85.0 | 62.6 | 84.6 | 62.9 |
| 454.calculix | 174 | 47.3 | 174 | 47.4 | 175 | 47.2 | 159 | 51.7 | 159 | 51.7 | 159 | 52.0 |
| 459.GemsFDTD | 62.6 | 170 | 62.6 | 170 | 62.4 | 170 | 59.5 | 178 | 59.5 | 178 | 59.5 | 178 |
| 465.tonto | 232 | 42.4 | 231 | 42.5 | 231 | 42.5 | 196 | 50.2 | 197 | 50.0 | 195 | 50.5 |
| 470.lbm | 32.2 | 426 | 32.2 | 426 | 32.0 | 429 | 32.2 | 426 | 32.2 | 426 | 32.0 | 429 |
| 481.wrf | 97.6 | 114 | 97.8 | 114 | 97.0 | 115 | 97.6 | 114 | 97.8 | 114 | 97.0 | 115 |
| 482.sphinx3 | 269 | 72.4 | 268 | 72.8 | 268 | 72.6 | 272 | 71.6 | 272 | 71.6 | 273 | 71.3 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:
Early Snoop = Disabled
Enforce POR = Disabled
Hyper-Threading (ALL) = Enabled
Memory Frequency = 2133



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SuperStorage Server 5048R-E1CR36L
(X10SRH-CF/CLN4F, Intel Xeon E5-2697 v3)

SPECfp2006 = 103

SPECfp_base2006 = 99.4

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: May-2014
Hardware Availability: Sep-2014
Software Availability: Nov-2013

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"
OMP_NUM_THREADS = "14"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deallI: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SuperStorage Server 5048R-E1CR36L
(X10SRH-CF/CLN4F, Intel Xeon E5-2697 v3)

SPECfp2006 = 103

SPECfp_base2006 = 99.4

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: May-2014
Hardware Availability: Sep-2014
Software Availability: Nov-2013

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SuperStorage Server 5048R-E1CR36L
(X10SRH-CF/CLN4F, Intel Xeon E5-2697 v3)

SPECfp2006 = 103

SPECfp_base2006 = 99.4

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: May-2014

Hardware Availability: Sep-2014

Software Availability: Nov-2013

Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revE.html>



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SuperStorage Server 5048R-E1CR36L
(X10SRH-CF/CLN4F, Intel Xeon E5-2697 v3)

SPECfp2006 = 103

SPECfp_base2006 = 99.4

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: May-2014

Hardware Availability: Sep-2014

Software Availability: Nov-2013

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revE.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Wed Nov 12 10:17:33 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 7 November 2014.